

VARIAN 620/L-100
MAINTENANCE MANUAL

Specifications Subject to Change Without Notice



varian data machines/a varian subsidiary

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CUSTOMER: VID

END USER: VID

SALES ORDER: 37-16986

DATE:

FROM: C. M. FUJIOKA

BRIEF DESCRIPTION & NOTES

This 620/L-103 has been modified per VID document #998712-01.

The memory timing and control board (slot 6) has a bootstrap loader protect option (DM-395A) protecting addresses 0 to 77₈ and the upper 400₈ locations.

APPROVED BY: _____

PROD. MGR.

DATE: _____

DESCRIPTION
OF CHANGEED
DET
CHK
DATE
REV

DRAWN

DATE

APPROVED

DATE

CODE

CHECKED

DATE

APPROVED

DATE

CLASS

SYSTEM. MEMO



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NOT OTHERWISE SPEC: FRAC ± ANG ±

SCALE

FIN. ✓ DEC .X ± .XX ± .XXX ±

DIVISION

SIZE

DRAWING NO.

REV

VENDOR: VDM IRVINE

VDM MODEL E-2861 B

This consists of:

- 1) 1 620L-103 16K CPU
- 2) 1 E 2847 BOOTSTRAP PROTECT (03-998 116)
- 3) LESS THE FOLLOWING:

PRIORITY INTERRUPT MODULE (PIM)


REAL TIME CLOCK

FRONT PANEL, LATCH & HINGE BRACKETS

DOCUMENTATION & TAPES (See Note 2)

- NOTES:
- 1) Test unit with front panel, then remove panel. VDM/PA will install adapter board to permit operation without front panel.
 - 2) Does not include program tapes or listings. Only documentation required is Maintenance Manual, Volume 2.

SPEC CONTROL DRAWING

DESCRIPTION OF CHANGE	WAS CHANGED TO #2	DRAWN	DATE	APPROVED	DATE	CODE	
			R. Anderson	11/1/72			
			CHECKED	DATE	APPROVED	DATE	CLASS
				<i>[Signature]</i>	11/10/72	A	
620 L CPU for VID (GATOR)							
		 varian	NOT OTHERWISE SPEC: FRAC ± ANG ±			SCALE	
DFT	47		FIN. ✓	DEC. X ±	XX ±	XXX ±	—
CHK	JTF		VDM/Palo Alto		A	03-998114	B
DATE	11/1/72		DIVISION		SIZE	DRAWING NO.	REV
REV	1						

03-998114

A

DO NOT SCALE DRAWING

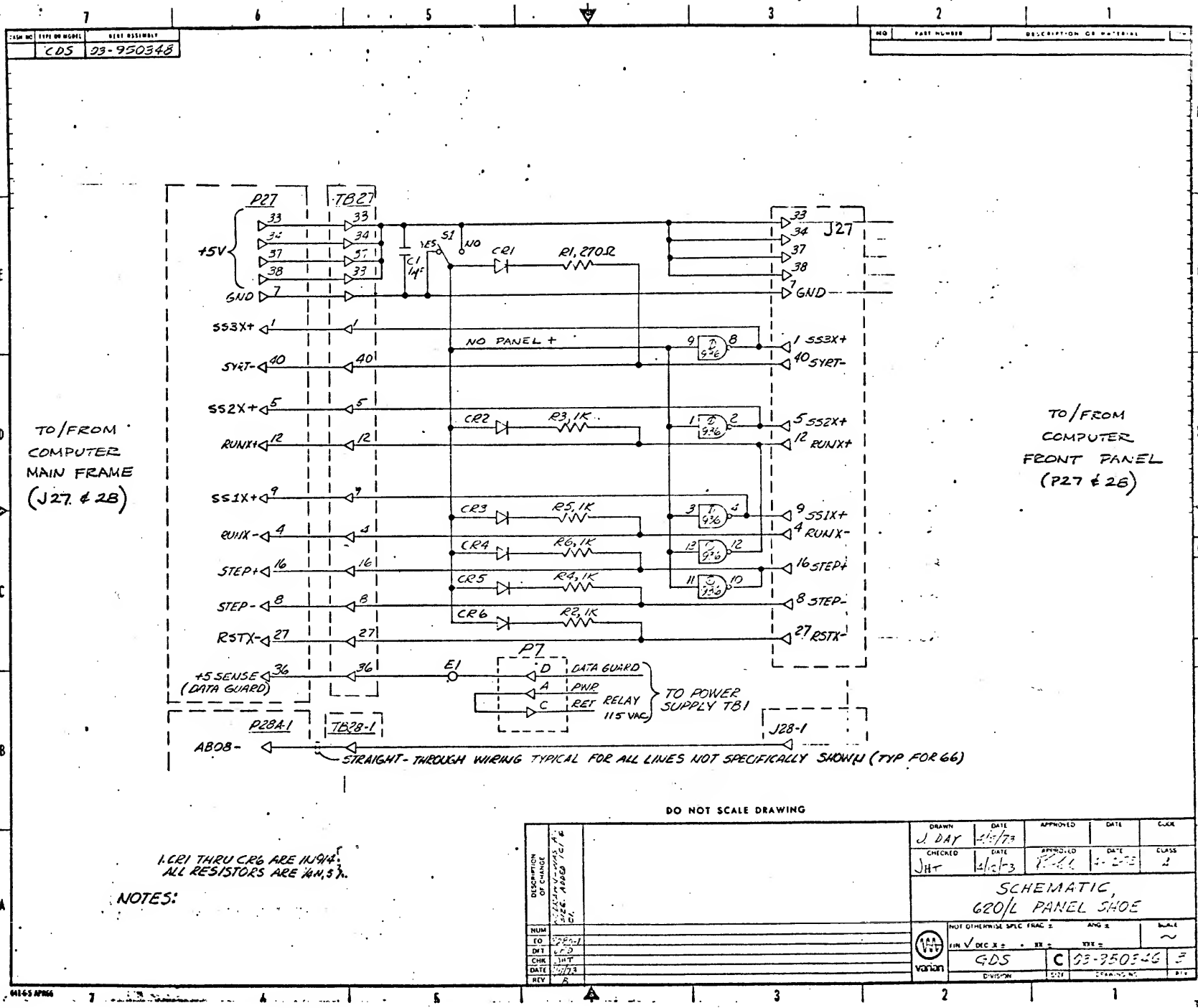
ADDENDUM

620/i and 620/L Power Failure/Restart
Manuals

98 A 9902 442

February 1972

Not only must the PF/R threshold be readjusted when the power supply and/or the PF/R card is changed, it must also be readjusted whenever a change from 50 to 60 Hz (or vice versa) operation is made, or whenever there is an increase in the load on the +5V dc supply.



VENDOR: VDM IRVINE

VDM MODEL E-2847 Loader Protect


This is Loader-protect option for 620L-100 (Model 620L-115) with following modification:

Locations 0 to 77_8 , inclusive, are protected in the same manner as the upper 400_8 locations.

Any attempt to write in the locations specified above will be converted to a read cycle. No HALT or interrupt will be generated.

The purpose of the additional protection is to preserve the Power-on interrupt locations and the GATOR manual-interrupt location (0).

DESCRIPTION OF CHANGE	VDM MODEL E-2847 VDM		DRAWN	DATE	APPROVED	DATE	CODE
			R. Anderson	11/10/72			
			CHECKED	DATE	APPROVED	DATE	CLASS
					<i>[Signature]</i>	11-10-72	A
			SPEC CONTROL DIAG: E-2847 LOADER PROTECT				
			NOT OTHERWISE SPEC: FRAC ±			ANG ±	SCALE
			FIN. <input checked="" type="checkbox"/> DEC. X ±			XX ±	XXX ±
			GDS		A	03-998116	
			DIVISION		SIZE	DRAWING NO.	
						REV	


varian

DWG NO

98A0935

REVISIONS

SYM	DESCRIPTION	APPROVED	DATE
A	PRODUCTION RELEASE PER EN 81815	<i>MS/L</i>	3/08/73

DR K. Ellinor 2/27/73

CHK

DSGN

ENGR

Walt 3/28/73APPD *A. H. Peterson* 3/28/73

APPD



varian data machines / a varian subsidiary
2722 michelson drive / irvine / california / 92664

TITLE

ENGINEERING DESCRIPTION
BOOTSTRAP LOADER PROTECT
(E-2847)

THIS DOCUMENT MAY CONTAIN
PROPRIETARY INFORMATION
AND SUCH INFORMATION MAY
NOT BE DISCLOSED TO OTHERS
FOR ANY PURPOSE OR USED
TO PRODUCE THE ARTICLE
OR SUBJECT, WITHOUT WRIT-
TEN PERMISSION FROM VDM

CODE
IDENT NO.**21101**

SIZE

A

DWG NO.

98A0935

REV

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SCALE

SHEET 1 OF 10

00153-0008

ENGINEERING DATA FORM

OPTION _____ Bootstrap Loader Protect

MODEL _____

NO. OF LOGIC CARDS REQ'D. _____ Assembled on the memory timing and control

NO. OF CARD SLOTS REQ'D. _____ 1

LOCATION OF SLOTS (NUMBERING) _____ CPU Card Slot 6

CONNECTORS REQ'D. (EXCLUDING I/O) _____ N/A

KEYING _____

ST'D. DEVICE ADDRESS _____ N/A

WIRELIST NUMBER _____ N/A (PC Board)

MANUAL PUBLICATIONS NUMBER _____ This document

PERIPHERAL EQUIPT. REQ'D _____ For test only: BIC and paper tape system

MFG'R. _____

MODEL _____


GEN'L. SPECS _____

NOTES:

Drawings:

Top Assembly	44P0671
Logic Diagram	91D0436
Bracket Assembly	04C0656

Software and test procedures are part of this drawing.

 varian data machines a varian subsidiary 2722 michelson drive irvine/california/92664	CODE IDENT. NO		98A0935	REV A
	PREPARED BY	APPR.	SHT 2 OF 10	

SECTION 1 GENERAL DESCRIPTION

The loader protect feature consists of additional logic located on the memory timing and control board plus additional backplane wiring to a switch. Drawing describes the modification procedure.

The purpose of the loader protect is to prevent writing into the last 400₈ locations of a selected 4K memory increment and locations 0 through 77₈ of the first 4K memory increment.



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SECTION 2

FUNCTIONAL DESCRIPTION

The loader protect feature prevents writing into memory locations which are used by the bootstrap and binary load/dump routines. These locations are normally the last 400 octal addresses of the core memory. The loader protect circuitry is located on the timing and control card located in CPU card slot 6. The card contains jumper pads for the three most significant address terms (L12X+, L13X+, L14X+). Jumpers are installed at the factory or in the field when the system memory capacity exceeds 4K. Address may be adjusted to protect any memory increment from 4 to 32K.

NOTE: If no jumpers are installed, the last 400 octal locations of each 4K section is controlled by the loader protect circuitry. (See Table 1 for jumper configuration).

There is a switch mounted on the chassis inside the front panel that enables and disables the option.

There are no additional instructions or control terms for this option. All addresses for write type operation are compared for error. An error occurs whenever a write type operation is attempted in a protected area with the option enabled.



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SECTION 3 THEORY OF OPERATION

All addresses being accessed in memory are checked by the loader protect option. When the address being accessed is equal to that of a protected address, and LPEX+ (loader protect enable from switch) is true, and H2XX+ (decode for a store instruction) is true, then the WRTX+ (write control to memory) is forced to ground. This causes the memory cycle to be a read/restore cycle.

After an error, the WRTX+ signal will be forced to ground causing a read cycle, and the computer will continue to cycle, changing all write cycles in a protected area. During a trap-in operation, the read/write command (WRTX+) and the memory start pulse (MSPX+) are forced to the low state and the CPU continues to cycle.

3.1 TRAP-IN OPERATION

When executing a trap-in request with the option enabled, if the memory address provided by the trapping device is in the protected area, the following events occur. The read/write command (WRTX+) and the memory start pulse (MSPX+) are forced to the low state.

3.2 LOADER PROTECT ENABLE/DISABLE SWITCH

A toggle switch located on the chassis inside the front panel is used to enable the loader protect circuitry. When the switch is in the disable position, all memory locations are available for storage. However, when the switch is in the enable position, the loader protect feature prevents writing into the memory locations X7400 through X7777 and locations 0 through 778.

*X = 4K core segment

3.3 ADDRESS SELECTION

Table 1 shows jumper placement on the memory timing and control board for address selection. Typically, the system memo will specify the proper jumpering.



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TABLE 1

MEMORY SIZE	JUMPERS REQ.		
	A-A	B-B	C-C
4K	No	No	No
8K	No	No	Yes
12K	No	Yes	No
16K	No	Yes	Yes
20K	Yes	No	No
24K	Yes	No	Yes
28K	Yes	Yes	No
32K	Yes	Yes	Yes

A-A, B-B and C-C are jumper pads for the three most significant address terms and are located on the memory timing control board.



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SECTION 4
MNEMONIC LIST

H2XX+	Store instruction decode
ICLX-	Inhibit clock
L02X+ through L14X+	Memory address register lines
LPEX+	Loader protect enable from switch
MSPX+	Memory start pulse
TPIX-I	Trap-in
WRTX+	Read/write command



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SECTION 5 TEST DESCRIPTION

5.1 PROGRAM MODE TEST

Using Aid II program with loader protect disabled write a fixed data pattern in locations 0 through 77 and X7400 through X7777.

i.e.

0, 77, 177777, .	X = 0-7 for
X7400, X7777, 177777, .	4 → 32K
	1 ^E 4K = 0
	8K = 1

Enable loader protect then write a different fixed pattern in locations 0 through 77 and locations X7400 through X7777.

i.e.

0, 77, 0.
X7400, X777, 0, .

Now verify that the original pattern did not get altered by reading locations 0 77 and X7400 X7777 and comparing it with the original pattern by using the search function of Aid II program.

i.e.

S	0, 77, 177777, N
S	X7400, X7777, 177777, N

If any errors occurred, they will be listed as follows: There should be no errors.

i.e.

Address	Contents
000043	(000000)

5.2 TRAP-IN TEST

Using the Aid II program load the following program into core starting at location 100.



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100	100021	Initialize BIC
101	006010	LDAI
102	0	
103	006020	LDBI
104	-77	
105	101537	Sen Buff Ready
106	000111	
107	001000	JMP
110	000105	
111	103120	OAR BIC Initial Reg.
112	103221	OBR BIC Final Reg.
113	100020	Activate BIC
114	100537	Start Reader
115	005000	NOP
116	101020	Sen BIC Not Busy
117	000122	
120	001000	JMP
121	000115	
122	100021	Initialize BIC
123	006010	LDAI
124	0X7400	
125	006020	LDBI
126	0X7777	
127	101537	Sen Buff Ready
130	000133	
131	001000	
132	000127	
133	103120	OAR BIC Initial Reg.
134	103221	OBR BIC Final Reg.
135	100020	Activate BIC
136	100537	Start Reader
137	005000	NOP
140	101020	Sen BIC Not Busy
141	0X6000	
142	001000	JMP
143	000137	



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Place a test tape in the reader, ~~press~~ then run the program starting at location 100. When it is complete, it will return to the Aid II program.

Verify that none of the protected locations of core get altered by using the search function of the Aid II program.

i.e.

S 0, 77, 177777, N

S X7400, X7777, 177777, N

Any errors will be listed as in previous test. There should be no errors.



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DOCUMENTATION RECORD

CUSTOMER VARIAN QADS
MODEL NO. 620/L-100
SYS. SERIAL NO. 81

JOB ORDER NO. 73284-29
SHIP DATE _____
CHECKED BY _____

UNIT	DOCUMENTATION NO.	REV	ART REV	S/N	DESCRIPTION	INSP.
	01E1035	-	-	-	FRAME ASSY	
	01D1036	-	-	-	FRONT PANEL ASSY	
	93D0275	-	-	-	POWER SUPPLY INSTALLATION	
	3238441	-	-	-	CORE STACK	
DM288	44P0506	P		11047	SENSE INHIBIT	
DM288	44P0506	P		11050	SENSE INHIBIT	
DM288	44P0506	P		8987	SENSE INHIBIT	
DM288	44P0506	P		9018	SENSE INHIBIT	
DM288	44P0506				SENSE INHIBIT	
DM288	44P0506				SENSE INHIBIT	
DM295	44P0515				DISPLAY BOARD	
DM327	44P0578	G		3075	DRIVE/SINK SW	
DM327	44P0578	G		3142	DRIVE/SINK SW	
DM327	44P0578				DRIVE/SINK SW	
DM336	44P0592	D		3684	REGISTER CARD	
DM336	44P0592	D		3688	REGISTER CARD	
DM336	44P0592	D		3675	REGISTER CARD	
DM336	44P0592				REGISTER CARD	
DM336	44P0592				REGISTER CARD	
DM336	44P0592				REGISTER CARD	
DM337	44P0593	N		1152	PROCESSOR CONT. #4	
DM337	44P0593				PROCESSOR CONT. #4	
DM338	44P0594	D		1435	HM/D & F.A.	
DM338	44P0594				HM/D & F.A.	
DM339	44P0595	B		1547	PROCESSOR CONT #1	
DM339	44P0595				PROCESSOR CONT #1	
DM340	44P0596	F		1314	PROCESSOR CONT. #2	
DM340	44P0596				PROCESSOR CONT. #2	
DM341	44P0597	E		1153	PROCESSOR CONT. #3	
DM341	44P0597				PROCESSOR CONT. #3	
DM342	44P0598	E		1380	DMA	
DM343	44P0671	F		092	MEMORY T AND CONT.	
DM343	44P0599				MEMORY T AND CONT.	

PREPARED BY Ma

DATE 5-30-74

NOTE:

967-0008



PARTS LIST FOR 620/L AND 620/L-100 CIRCUIT CARDS

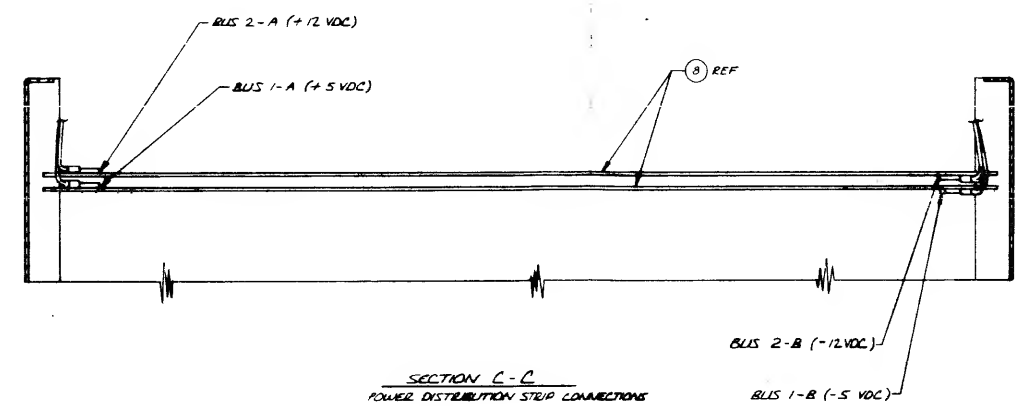
The following is a composite parts list for the 620/L and 620/L-100. It is divided into three sections. Section 1 contains the standard circuit cards, section 2 the power supply, and section 3 the controller cards. The parts for each circuit card are listed in numerical order according to Varian part numbers. The reference designations in the parts list also appear on the assembly drawings and logic diagrams.

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
<u>1. STANDARD LOGIC CARDS</u>			
44P0172 *	49A0002-000	Tex Inst SN7473N	IC 16,33,39,44,49
Priority	49A0004-000	Tex Inst SN7440N	IC 41
Interrupt	49A0007-000	Tex Inst SN7400N	IC 23,24
Module	49A0008-000	Tex Inst SN15846N	IC 6,10,12,18,19,25,26, 30,32,38,43,45,46,48,50
	49A0010-000	Fairchild SL18162	IC 4,5,11,28,29,36
	49A0011-000	Tex Inst SN15830N	IC 1,7,13,17,20,21,27,35,51
	49A0012-000	Tex Inst SN7474N	IC 2,3,9,15,31,37,42,47
	49A0014-000	Tex Inst SN15850N	IC 22,34
	49A0016-000	Tex Inst SN15833N	IC 8,14,40
44P0185	49A0002-000	Tex Inst SN7473N	IC 3,13,19,20,26,27,33
Power Failure/	49A0004-000	Tex Inst SN7440N	IC 22,23,32
Restart and	49A0007-000	Tex Inst SN7400N	IC 8,9,12,31
Real-Time	49A0008-000	Tex Inst SN15846N	IC 1,2,4,5,6,11,21,24,25, 34,35,36,37,39,40,41
Clock	49A0010-000	Fairchild SL18162	IC 14,15,16,17,18,28,30,42
	49A0011-000	Tex Inst SN15830N	IC 7,10
	49A0518-000	Motorola MC851L	IC 29,38
	76A2369-000	2N2369	Q 3,5,6,8,9,10,11,15,16,17,18
	76A3009-000	2N3009	Q 12,19
	76A4034-000	2N4034	Q 1,2,4,7,13,14
	76N4916-000	Fairchild 2N4916	Q 1,2,4,7,13,14
	76S1002-000	Motorola 2N3019	Q 20
	76S1046-000	Fairchild 2N3646	Q 12,19
	77N0753-000	IN753	CR 3,10
	77S1017-000	Fairchild EDN400	CR 1,2,4,5,6,7,9

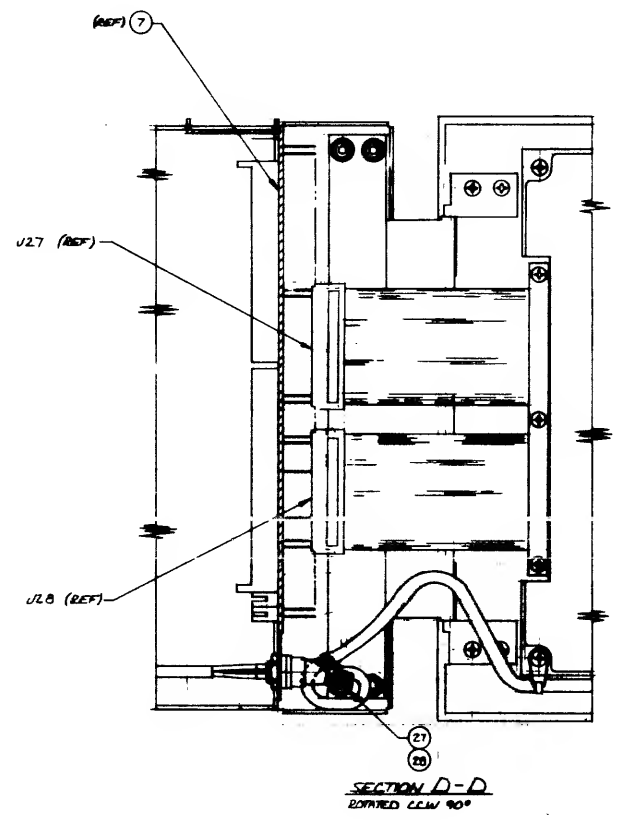
* See page 6 for another version of Priority Interrupt Module.

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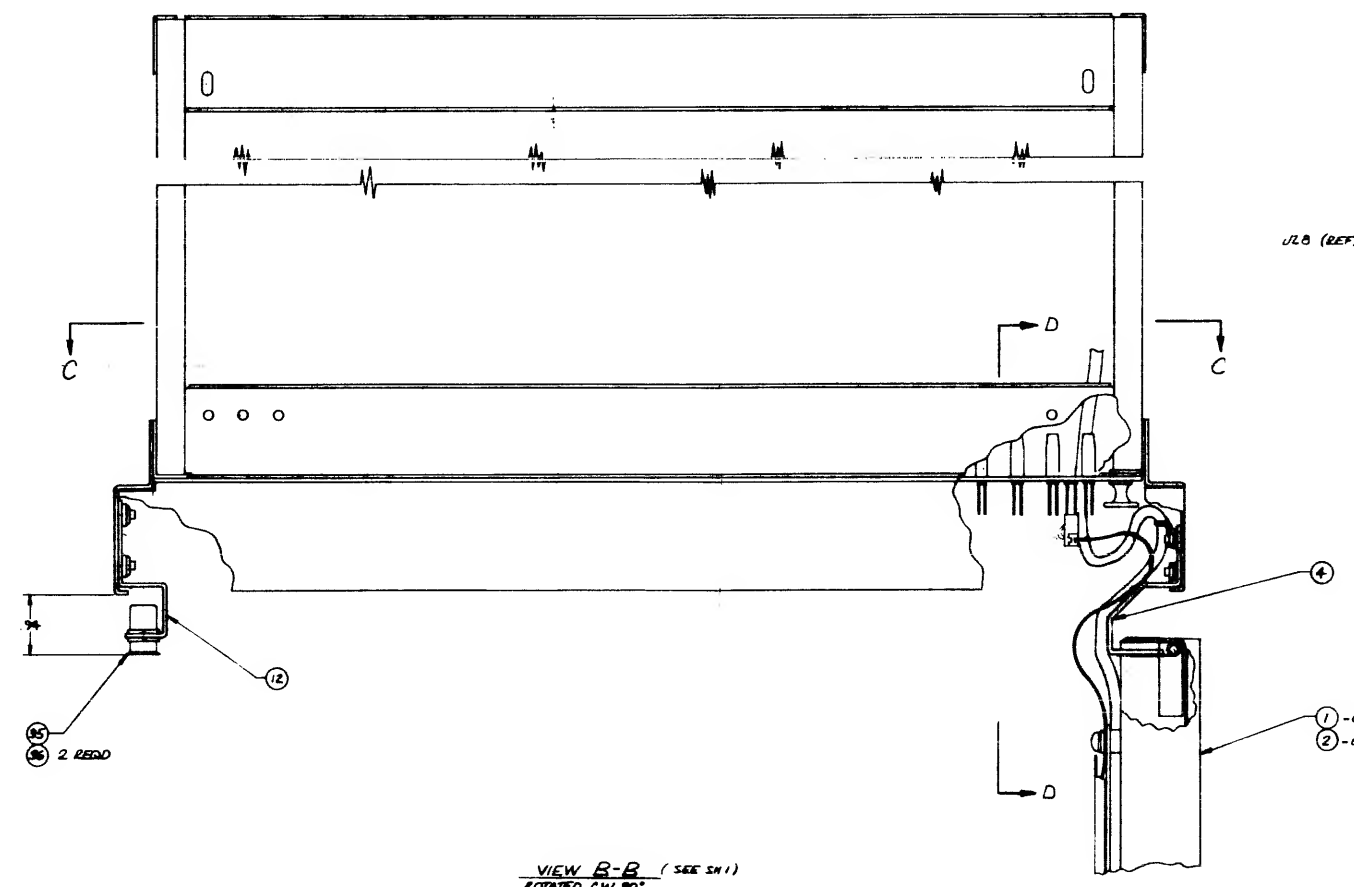
REVISIONS			
NO.	DATE	DESCRIPTION	APPROVED
1		SEE SHEET 1	



SECTION C-C
POWER DISTRIBUTION STRIP CONNECTIONS



SECTION D-D
ROTATED 45°



VIEW R-R (SEE SH 1)
ROTATED 45°

NOTE: UNLESS OTHERWISE SPECIFIED

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<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
44P0506 Sense/Inhibit	48A0002-001	Varian	A 101,201,401,501,701, 801,1001,1101
	48A0003-001	Varian	A 303,603,903,1203
	49A0022-000	Tex Inst SN74H11N	IC 4,5,7,9,10,12
	49A0032-000	Tex Inst SN7402N	IC 2
	49A0042-000	Tex Inst SN74H01N	IC 1,3,6,8,11
	49A0080-000	Tex Inst SN7524N	IC 101,201,401,501,701, 801,1001,1101
	49A0119-000	Motorola MH05859	A 302,602,902,1202
	76A2369-000	2N2369	Q 301 through 304, 601 through 604, 901 through 904, 1201 through 1204
	77S1017-000	Fairchild EDN400	CR 101,102,103,104 through 904,1201,1202,1203,1204, 305,605,905,1205
44P0515 Display Board	49A0004-000	Tex Inst SN7440N	IC 15
	49A0040-000	Tex Inst SN7404N	IC 3,7,11,13
	49A0110-000	Tex Inst SN75451BP	IC 1,2,4,5,6,8,9,10,12,14
44P0521 Memory Buffer	49A0023-000	Tex Inst SN74H04N	IC 2,4,7
	49A0025-000	Varian	A 1,2
	49A0042-000	Tex Inst SN74H01N	IC 1,3,5,6,8
	49A0124-000	Tex Inst SN7407N	IC 13,14,15
	49A0125-000	Tex Inst SN7408N	IC 9,10,11,12
44P0578 Driver/Sink Switch	49A0032-000	Tex Inst SN7402N	IC 1,3
	49A0041-000	Tex Inst SN74H51N	IC 4,7,8
	49A0044-000	Tex Inst SN7442N	IC 2,5,9,11
	49A0118-000	Tex Inst SN7427N	IC 6,10
	49A0119-000	Motorola MH05859	A 102,104,202,204,302,304,402, 404,502,504,602,604,702,704, 802,804
	76A0002-000	2N3725A	Q 25
	76A2369-000	2N2369	Q 1,2,5,9,12,13,14,17,18
	76A2904-000	2N2904	Q 23
	76A2907-000	Motorola 2N2907	Q 3,4,6,7,8,10,11,15,16,19,20
	76N3640-000	2N3640	Q 21,24
	76S1046-000	Fairchild 2N3646	Q 22,26
	77N0751-000	Tex Inst IN751A	CR 7,10,11,12,13,14
	77S1017-000	Fairchild EDN400	CR 1 through 6,8,9,15, 101 through 116, 201 through 216, 301 through 316, 401 through 416, 501 through 524, 601 through 624, 701 through 724, 801 through 824

Card P/N
and Name

Varian P/N

Manufacturer
and P/N

Reference
Designation

44P0592 Register Card	49A0000-000	Tex Inst SN7475N	IC 9,21,33
	49A0007-000	Tex Inst SN7400N	IC 13,24
	49A0010-000	Fairchild SL18162	IC 2,6,7
	49A0012-000	Tex Inst SN7474N	IC 4,8,14
	49A0023-000	Tex Inst SN74H04N	IC 12,18,41
	49A0040-000	Tex Inst SN7404N	IC 1
	49A0095-000	Tex Inst SN74H52N	IC 15
	49A0096-000	Tex Inst SN74181N	IC 19,25
	49A0102-000	Tex Inst SN74182N	IC 3
	49A0104-000	Motorola MC3001P	IC 26
	49A0106-000	Tex Inst SN74H53N	IC 5,10,11,16,17,22,23,27, 28,29,30,31,32,34,35,40, 42,43,44
	49A0141-000	Tex Inst SN74174N	IC 36,37,38,39
	49A0554-001	Tex Inst SN74H10N	IC 20
	77S1011-000	Varian	CR 1 through 6
44P0593 Processor Control Number 4	49A0000-000	Tex Inst SN7475N	IC 40
	49A0002-000	Tex Inst SN7473N	IC 4,18,19,43
	49A0004-000	Tex Inst SN7440N	IC 2
	49A0005-000	Tex Inst SN7410N	IC 10,13,44
	49A0006-000	Tex Inst SN7420N	IC 21
	49A0007-000	Tex Inst SN7400N	IC 5,12,42
	49A0010-000	Fairchild SL18162	IC 1,29
	49A0019-000	Tex Inst SN74H40N	IC 6,7,32
	49A0020-000	Tex Inst SN74H72N	IC 8
	49A0023-000	Tex Inst SN74H04N	IC 14,17,22
	49A0025-000	Varian	A 1,2
	49A0036-000	Tex Inst SN74H73N	IC 28
	49A0038-000	Tex Inst SN74H22N	IC 15
	49A0039-000	Tex Inst SN74H00N	IC 9,39,41,45
	49A0042-000	Tex Inst SN74H01N	IC 3,27
	49A0056-000	Tex Inst SN74H20N	IC 31,33,38
	49A0077-000	Tex Inst SN74H60N	IC 24
	49A0104-000	Motorola MC3001P	IC 16,25
	49A0106-000	Tex Inst SN74H53N	IC 26
	49A0127-000	Tex Inst SN74161N	IC 23
	49A0138-000	Tex Inst SN7437N	IC 11,30,37
	49A0146-000	Tex Inst SN74122N	IC 46
	49A0554-001	Tex Inst SN74H10N	IC 20,34,35,36

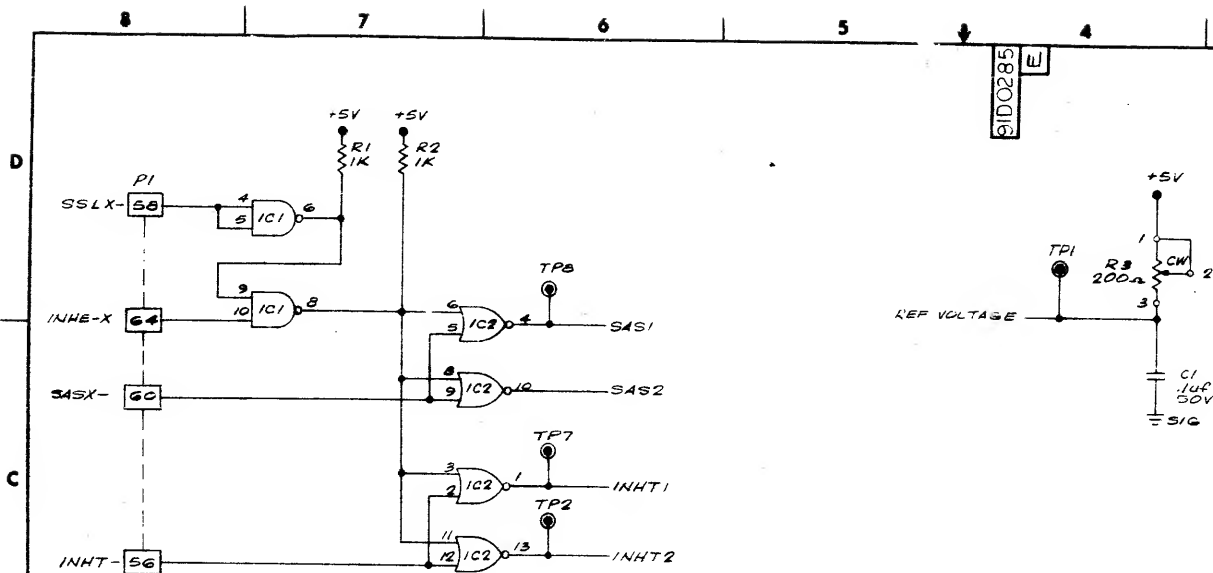
<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
44P0594 Multiply/ Divide, Extended Address	49A0000-000	Tex Inst SN7475N	IC 16
	49A0002-000	Tex Inst SN7473N	IC 2,29
	49A0006-000	Tex Inst SN7420N	IC 27
	49A0007-000	Tex Inst SN7400N	IC 21,22
	49A0008-000	Tex Inst SN15846N	IC 6,8,9,24,25
	49A0009-000	Tex Inst SN15862N	IC 19
	49A0010-000	Fairchild SL18162	IC 7
	49A0011-000	Tex Inst SN15830N	IC 10,14,28
	49A0036-000	Tex Inst SN74H73N	IC 15
	49A0038-000	Tex Inst SN74H22N	IC 1,4,5,13,18
	49A0039-000	Tex Inst SN74H00N	IC 17,32
	49A0042-000	Tex Inst SN74H01N	IC 3,20
	49A0128-001	Tex Inst SN7438N	IC 11,12,26,30,31
	49A0142-000	Tex Inst SN7412N	IC 23
44P0595 Processor Control Number 1	49A0000-000	Tex Inst SN7475N	IC 18,19,26,27
	49A0002-000	Tex Inst SN7473N	IC 1,4,8,34
	49A0004-000	Tex Inst SN7440N	IC 37
	49A0005-000	Tex Inst SN7410N	IC 36
	49A0007-000	Tex Inst SN7400N	IC 7,10
	49A0008-000	Tex Inst SN15846N	IC 2,5,12,13,15,16,20,24, 25,28,29,30,31,33
	49A0009-000	Tex Inst SN15862N	IC 11
	49A0011-000	Tex Inst SN15830N	IC 3,6
	49A0019-000	Tex Inst SN74H40N	IC 32
	49A0021-000	Tex Inst SN7401N	IC 14,21,22,23
	49A0039-000	Tex Inst SN74H00N	IC 9
	49A0042-000	Tex Inst SN74H01N	IC 17
	76N3055-000	2N3055	Q 1
	77N4730-000	IN4730A	CR 1
	77S1011-000	Varian	CR 2
44P0596 Processor Control Number 2	49A0000-000	Tex Inst SN7475N	IC 41
	49A0002-000	Tex Inst SN7473N	IC 9,20,27
	49A0004-000	Tex Inst SN7440N	IC 18,40,43
	49A0005-000	Tex Inst SN7410N	IC 10,11,30,48
	49A0006-000	Tex Inst SN7420N	IC 22,23,26,28,34
	49A0007-000	Tex Inst SN7400N	IC 4,5,7,12,19,37
	49A0008-000	Tex Inst SN15846N	IC 15,17,31,42
	49A0009-000	Tex Inst SN15862N	IC 1,6,29
	49A0010-000	Fairchild SL18162	IC 13,24,47
	49A0011-000	Tex Inst SN15830N	IC 3,21,25
	49A0019-000	Tex Inst SN74H40N	IC 32
	49A0038-000	Tex Inst SN74H22N	IC 8,14,38
	49A0039-000	Tex Inst SN74H00N	IC 36
	49A0042-000	Tex Inst SN74H01N	IC 2,16,35,39,45
	49A0056-000	Tex Inst SN74H20N	IC 33
	77S1011-000	Varian	CR 1

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
44P0597 Processor Control Number 3	49A0002-000	Tex Inst SN7473N	IC 2,12
	49A0004-000	Tex Inst SN7440N	IC 1,5,9,15,18,20,24,29
	49A0005-000	Tex Inst SN7410N	IC 4
	49A0006-000	Tex Inst SN7420N	IC 10,14,43
	49A0007-000	Tex Inst SN7400N	IC 13,42
	49A0008-000	Tex Inst SN15846N	IC 11,23,27,28,32,33
	49A0009-000	Tex Inst SN15862N	IC 6,31
	49A0019-000	Tex Inst SN74H40N	IC 26,34,41
	49A0038-000	Tex Inst SN74H22N	IC 37
	49A0039-000	Tex Inst SN74H00N	IC 19,38
	49A0042-000	Tex Inst SN74H01N	IC 7,16,21,22,30,36,39
	49A0128-001	Tex Inst SN7438N	IC 3,8
	49A0142-000	Tex Inst SN7412N	IC 25,35,40
	49A0554-000	Tex Inst SN74H10N	IC 17,44
	77S1011-000	Varian	CR 1,2
44P0598 Interrupt Trap	49A0002-000	Tex Inst SN7473N	IC 1,9
	49A0004-000	Tex Inst SN7440N	IC 24,28,32
	49A0005-000	Tex Inst SN7410N	IC 40
	49A0007-000	Tex Inst SN7400N	IC 10,15
	49A0008-000	Tex Inst SN15846N	IC 3,6,12,14,16,20,23,27, 35,37,38
	49A0010-000	Fairchild SL18162	IC 2,8
	49A0011-000	Tex Inst SN15830N	IC 36
	49A0021-000	Tex Inst SN7401N	IC 39
	49A0036-000	Tex Inst SN74H73N	IC 19
	49A0038-000	Tex Inst SN74H22N	IC 7
	49A0039-000	Tex Inst SN74H00N	IC 18,22,26,30,31,34
	49A0042-000	Tex Inst SN74H01N	IC 13,17,21,25,29,33
	49A0056-000	Tex Inst SN74H20N	IC 4,11
	49A0142-000	Tex Inst SN7412N	IC 5
	77S1011-000	Varian	CR 1,2
44P0599 Memory Timing and Control	49A0003-000	Tex Inst SN7472N	IC 9
	49A0019-000	Tex Inst SN74H40N	IC 17
	49A0023-000	Tex Inst SN74H04N	IC 7,13
	49A0039-000	Tex Inst SN74H00N	IC 6,11
	49A0041-000	Tex Inst SN74H51N	IC 16
	49A0056-000	Tex Inst SN74H20N	IC 12
	49A0079-000	Tex Inst SN74H54N	IC 8,15
	49A0146-000	Tex Inst SN74122N	IC 14
	49A0554-001	Tex Inst SN74H10N	IC 10
	77S1017-000	Fairchild EDN400	CR 1

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
44P0640 Memory Timing and Control	49A0003-000	Tex Inst SN7472N	IC 9
	49A0019-000	Tex Inst SN74H40N	IC 17
	49A0023-000	Tex Inst SN74H04N	IC 7,13
	49A0039-000	Tex Inst SN74H00N	IC 6,11
	49A0041-000	Tex Inst SN74H51N	IC 16
	49A0056-000	Tex Inst SN74H20N	IC 12
	49A0079-000	Tex Inst SN74H54N	IC 8,15
	49A0146-000	Tex Inst SN74122N	IC 14
	49A0554-001	Tex Inst SN74H10N	IC 10
	77S1017-000	Fairchild EDN400	CR 1
44P0683 Priority Interrupt Module	49A0002-000	Tex Inst SN7473N	C 6,7; D 6,7
	49A0012-000	Tex Inst SN7474N	B 3; C 2,3,4
	49A0022-000	Tex Inst SN74H11N	D 4
	49A0023-000	Tex Inst SN74H04N	C 1; E 3
	49A0036-000	Tex Inst SN74H73N	F 2
	49A0039-000	Tex Inst SN74H00N	E 1,6,7; F 3
	49A0040-000	Tex Inst SN7404N	A 1; B 1,2,3; E 5
	49A0056-000	Tex Inst SN74H20N	A 4,5; C 5; D 1,5
	49A0082-001	Tex Inst SN74H74N	A 6,7; B 6,7; F 1
	49A0093-001	Tex Inst SN74H50N	D 2
	49A0094-001	Tex Inst SN74H21N	E 4
	49A0104-000	Motorola MC3001P	D 3; F 6,7
	49A0128-001	Tex Inst SN7438N	A 2,3
	49A0138-000	Tex Inst SN7437N	E 2
	49A0554-001	Tex Inst SN74H10N	B 4

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
<u>2. POWER SUPPLY</u>			
44P0518 Heat Sink Board	76N3055-000	2N3005	Q 1,2
44P0526 Power Supply Board	76A0009-002 77N4001-004 77N4003-000 82A0030-001	Motorola MR751 IN4005 IN4003 Potter Brumfield KUP14AE6-115 VAC	CR 5,6,7,8,9,10,11,13 CR 15,16 CR 12,14 K 1
44P0528 Regulator Board	49A0103-001 76A0007-000 76A2904-000 77A0004-000 77N0751-000 77N4003-000	Fairchild U5R7723393 2N3054 2N2904 IN746A Tex Inst IN751A IN4003	IC 1,2,3,4 Q 1,2,3,5 Q 4 CR 6 CR 3 CR 4,5
83P0035 Power Supply	76A0008-000 77A0005-000	Motorola MR1121 Motorola MCR3935-2	CR 1,2,3,4 Q 1
<u>3. CONTROLLER CARDS</u>			
44P0013 Teletype Controller	49A0002-000 49A0004-000 49A0007-000 49A0008-000 49A0009-000 49A0010-000 49A0012-000 76A2369-000 76S1072-000 77S1017-000 82A0006-000 82A0006-003	Tex Inst SN7473N Tex Inst SN7440N Tex Inst SN7400N Tex Inst SN15846N Tex Inst SN15862N Fairchild SL18162 Tex Inst SN7474N 2N2369 Varian Fairchild EDN400 Aztec 20229 Aztec 20213	IC 6,10,11,22,23,27,28, 30,31,36,41 IC 25,40 IC 2,3,42 IC 1,4,13,14,15,17,19,20, 24,29,34,35,37,43 IC 5,38 IC 7,8,9,12,21,39,44 IC 16,18,26,32,33 Q 1 through 4 Q 1 through 4 CR 1 through 10 K 2 K 1

<u>Card P/N and Name</u>	<u>Varian P/N</u>	<u>Manufacturer and P/N</u>	<u>Reference Designation</u>
44P0026 Buffer Interlace Controller	49A0000-000 49A0002-000 49A0004-000 49A0005-000 49A0007-000 49A0008-000 49A0010-000 49A0011-000 49A0012-000 49A0014-000	Tex Inst SN7475N Tex Inst SN7473N Tex Inst SN7440N Tex Inst SN7410N Tex Inst SN7400N Tex Inst SN15846N Fairchild SL18162 Tex Inst SN15830N Tex Inst SN7474N Tex Inst SN15850N	IC 14,28,42,56 IC 57 IC 15,17,37 IC 31 IC 4,5,16,38 IC 2,6,7,13,20,21,23,27,34, 35,41,43,48,49,55,62,63 IC 1,18,19,29,32,33,46,47, 50,51,58,60,61 IC 3 IC 11,12,25,26,39,40,53,54 IC 8,9,10,22,24,30,36,44, 45,52,59
44P0689 Buffer Interlace Controller	49A0000-000 49A0002-000 49A0012-000 49A0022-000 49A0023-000 49A0039-000 49A0040-000 49A0042-000 49A0082-001 49A0093-001 49A0094-001 49A0104-000 49A0127-000 49A0128-001 49A0178-000 49A0554-001	Tex Inst SN7475N Tex Inst SN7473N Tex Inst SN7474N Tex Inst SN74H11N Tex Inst SN74H04N Tex Inst SN74H00N Tex Inst SN7404N Tex Inst SN74H01N Tex Inst SN74H74N Tex Inst SN74H50N Tex Inst SN74H21N Motorola MC3001P Tex Inst SN74161N Tex Inst SN7438N Tex Inst SN74175N Tex Inst SN74H10N	A 7; B 7; C 7; D 7 E 6; F 2; K 4 F 6; H 7; K 3 E 4; H 2 A 4; C 4; D 5; E 5; K 5 K 5 A 1; B 1; C 1; D 3 A 6; B 6; C 6; D 6 E 2 E 3; F 5,7 H 3 F 1,4; H 6; K 6 A 3; B 3; D 4 A 2; B 2; C 2; D 1,2; E 1; H H 1 F 3
44P0176 Paper Tape Controller	49A0000-000 49A0002-000 49A0004-000 49A0008-000 49A0009-000 49A0010-000 49A0011-000 49A0014-000 49A0018-000 49A0042-000 49A0146-000	Tex Inst SN7475N Tex Inst SN7473N Tex Inst SN7440N Tex Inst SN15846N Tex Inst SN15862N Fairchild SL18162 Tex Inst SN15830N Tex Inst SN15850N Tex Inst SN15851N Tex Inst SN74H01N Tex Inst SN74122N	IC 8,14 IC 52 IC 16,25,31,37,40,42, 45,48,51,55 IC 1,6,7,10,12,13,15, 18,19,24,27,29,30, 34,36,38,47,49,50,57 IC 21,41,53 IC 5,11,17,23,26,32,43 IC 35 IC 4,28,33,39,44 IC 22 IC 2,3,9,20 IC 46,56
44P0601 Bootstrap Loader	49A0007-000 49A0039-000 49A0508-000 49A0510-000 49A0516-000	Tex Inst SN7400N Tex Inst SN74H00N Tex Inst SN15846J Fairchild SL13016 Motorola MC833L	IC 2 IC 18 IC 4 IC 1,5 IC 3



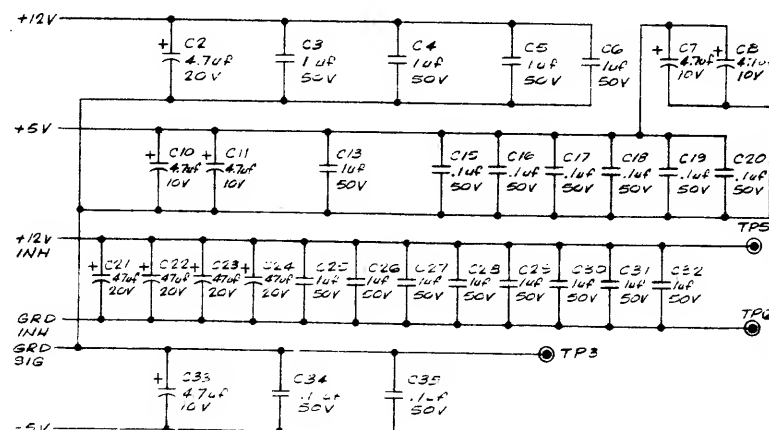
REV	DATE	DESCRIPTION	APPROVED	BY
X		PROTOTYPE RELEASE		
X2		PROTOTYPE RELEASE; REVISED ALL CIRCUITS		
A	1/0	PRODUCTION RELEASE PER 5310		
B	NA	TPI WAS ON SIGNAL SAS2 EN 5505		
C	SM	REMOVED RESISTOR R22 R27-2		
D	SM	WAS 2.1K, ADDED NOTE FOR ALL IN BOARD		
D	SM	REVISED DHT 4 PER EN 61270		
E	SM	0.005K WAS .0089K 14.5K ± 1% SW WAS 12.5K ± 1% SW: REVISED PER 201307		

5. VALUE FOR INDICATED RESISTORS IS:
FOR 44P0506-000 ASSY 2.2K OHMS
FOR 44P0506-001 ASSY 4.3 OHMS

- POWER DISTRIBUTION TO IC1 THRU IC12: PIN 7 - GRD
PIN 14 - +5V
- PREFIX COMPONENT REF DESIGN NO. WITH CIRCUIT NUMBER
EXAMPLE: CKT 1 - R101, R102
CKT 2 - R201, R202.
- COMPONENT VALUE TO BE DETERMINED AT FINAL TEST

1. ALL RESISTOR VALUES ARE IN OHMS, 1/4 W, 5%.

NOTE: UNLESS OTHERWISE SPECIFIED



REFERENCE DESIGNATIONS	
LAST USED	NOT USED
C95, C1104	C9,12,14
R21, R1217	
CR1205	
IC12, IC1101	
Q1204	
A1203	
TP8	
P1	
J1	

REFERENCE DRAWINGS	
40D0454	P.W. BOARD
44E0506	ASSEMBLY
44P0506	PARTS LIST
97D0533	ARTWORK
97D0555	SILKSCREEN
97D0556	SOLDER MASK

620/L
44E0506

DO NOT SCALE DRAWING

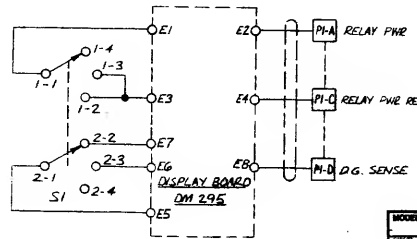
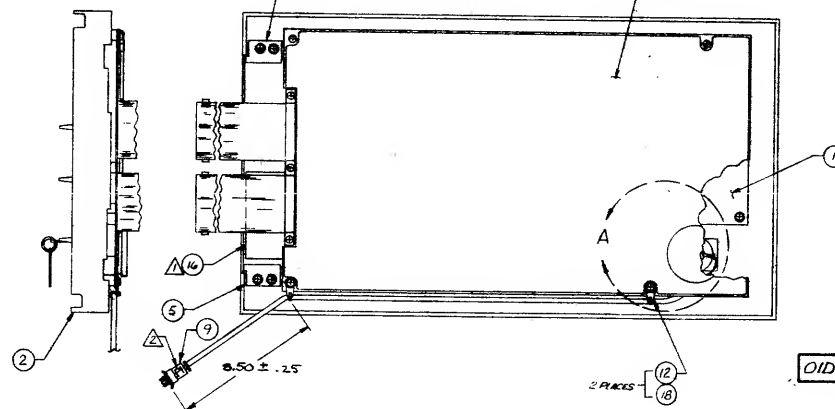
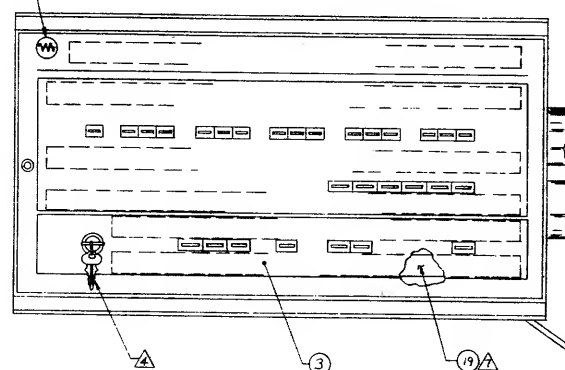
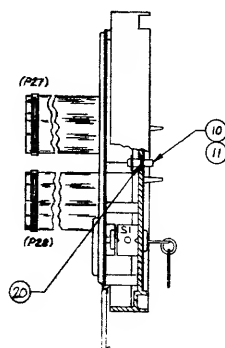
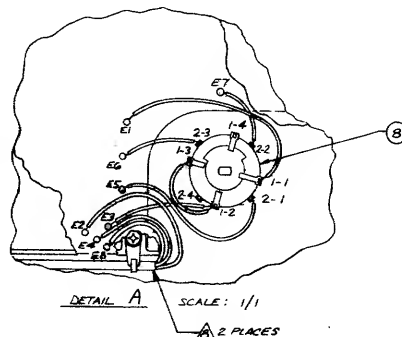
21101 D 91D0285

LOGIC DIAG
SEN: INH
DM288

21101 D 91D0285

WIRE LIST			
FROM	TO	FIND NO.	COLOR
E5	2-1	15	WHT
E6	2-3	15	WHT
E3	1-2	15	WHT
E2	1-2	15	WHT
E2	PI-A	17	RED
E4	PI-C	17	BEN
E8	PI-D	17	ORG
E7	2-2	15	WHT
E1	1-1	15	WHT

REVISIONS			
SYM	DATE	DESCRIPTION	APPROVED
A		PRODUCTION RELEASE PER EN 5314	
B	1M	ADDED TO WIRE LIST PER EN 6350	
B	1M	8.521.25 WAS 4.751.25 PER EN 5884	
C	6m	ADDED FIND NO. 20 TO FIND ZONE C7 PER EN 60184	

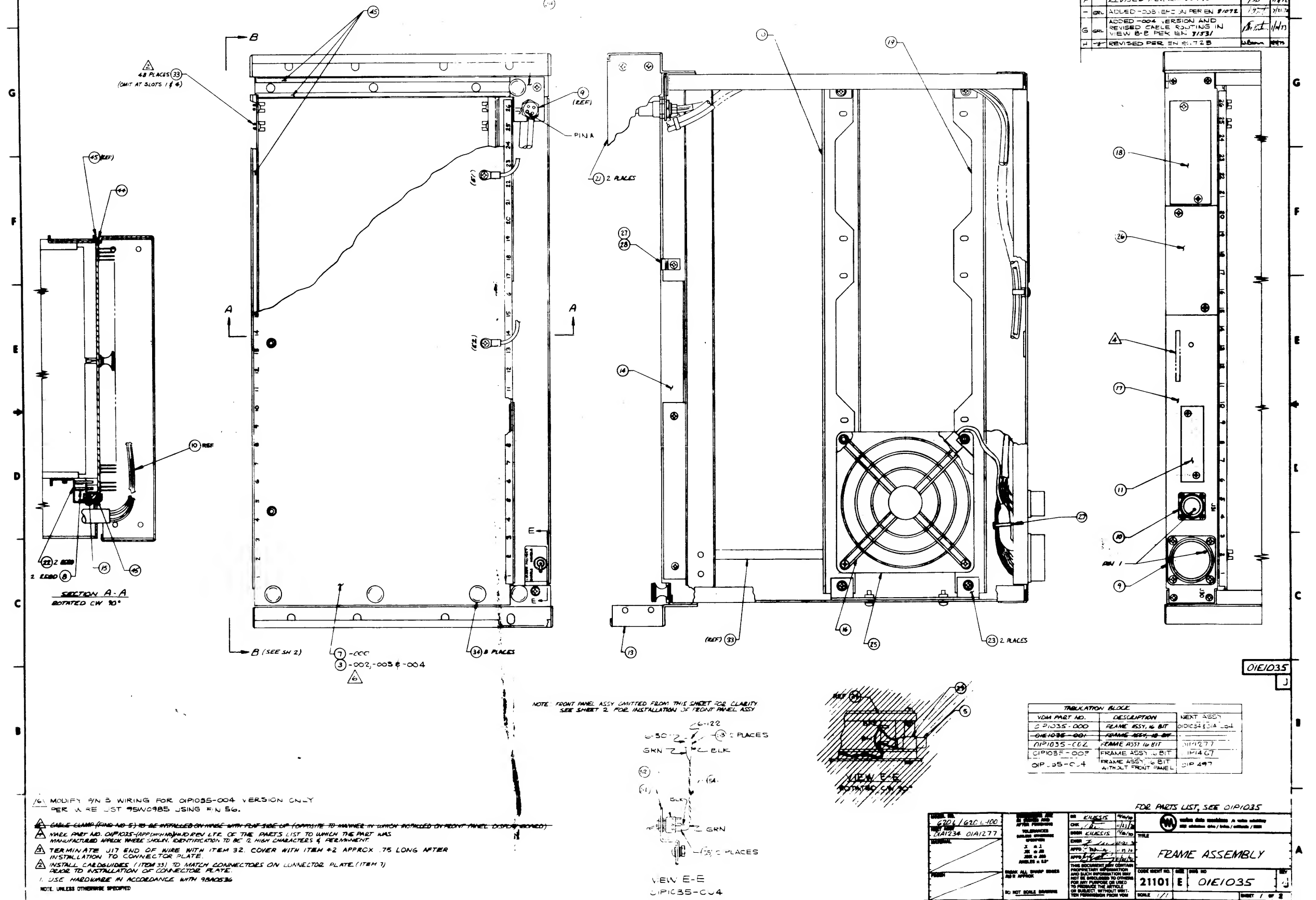


FOR PARTS LIST SEE 01D1036

MODEL NO. 620/L	DIMENSIONS ARE IN INCHES AND AFTER FINISHING	DR. KLUSSIS 1/1/71	DATE 2/1/71	CODE IDENT NO. 21101	DATE 01D1036	REV C
TEXT ARRY 01D1035	TOLERANCES UNLESS OTHERWISE SPECIFIED	CHK. 2/1/71	DATE 2/1/71	TITLE FRONT PANEL ASSY		
MATERIAL	X ± .1	ENGR. 1/1/71	DATE 1/1/71	SCALE 1/2		
FINISH	XX ± .05	APPR. 1/1/71	DATE 1/1/71	SHEET 1 OF 1		
DO NOT SCALE DRAWING		THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND SUCH INFORMATION MAY NOT BE DISCLOSED TO OTHERS FOR ANY PURPOSE OR USED TO PRODUCE THE ARTICLE OR SUBJECT WITHOUT WRITTEN PERMISSION FROM VDM		VDM version data machines / n. veron industry 1712. mickelson drive / irvine / california, 92614		

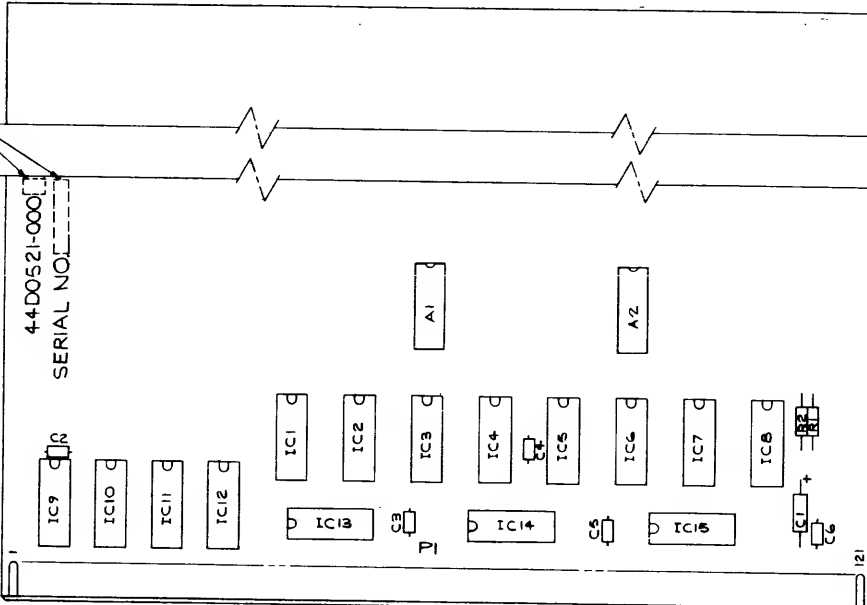
- △ CUT SHIELD BACK FLUSH WITH INSULATION, TYP BOTH ENDS
- △ APPLY TAPE (FIND NO 19) BETWEEN FIND NOS. 2 & 3 AS REQD.
- △ SPOT TIE AS REQUIRED.
- △ ROUTING OF WIRES IS SHOWN FOR REFERENCE ONLY.
- △ BAG KEYS (PART OF F/N B) AND ATTACH SECURELY TO ASSEMBLY.
3. USE HARDWARE IN ACCORDANCE WITH 98A0536.
- △ "PI" REFERENCE DESIGNATION SHOWN FOR INFORMATION ONLY AND DOES NOT APPEAR ON PART
- △ MAKE PART NO. 01D1036-000 & THE REV. LETTER OF THE PARTS LIST TO WHICH THE PART WAS MANUFACTURED A SERIAL NO. OF ASSEMBLY IN .12 HIGH CHARACTERS ON FIND NO 16; ATTACH TO FIND NO. 2 APPROX WHERE SHOWN.
- NOTE: UNLESS OTHERWISE SPECIFIED

REVISIONS				
SPR	DATE	DESCRIPTION	APPROVED	DATE
A		REDUCTION RELEASE PER EN 5047	<i>[Signature]</i>	6/1/72
B	JB	REMOVED PER EN 5047	<i>[Signature]</i>	6/1/72
C	SM	ADDED CABLE CLAMP AND SERVICE LOOP PER B-B 5584	<i>[Signature]</i>	6/5/72
D	SM	ZONE D2 REVISED VIEW: THE INTERFACES ADDED FIELD: SNAKE - DROPPED NEW EN 5047 AS PER EN 5047	<i>[Signature]</i>	6/6/72
E		REMOVED PER EN 5047	<i>[Signature]</i>	6/7/72
		REMOVED PER EN 5047	<i>[Signature]</i>	6/7/72
F		REMOVED PER EN 5047	<i>[Signature]</i>	6/7/72
G	GR	ADDED -004. VERSION AND REVISION CABLE ROUTING IN VIEW B-B PER EN 5153	<i>[Signature]</i>	6/7/72
H	GR	REMOVED PER EN 5153	<i>[Signature]</i>	6/7/72



REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
A	10/28/77	PRODUCTION RELEASE EN 5428	W/17
B	11/17/77	MODEL NO. HAS CROFT PER EN 80337	W/17
C	12/15/77	ADDED A1 AND A2 PER EN 81594	W/17

44DO521-000
SERIAL NO.



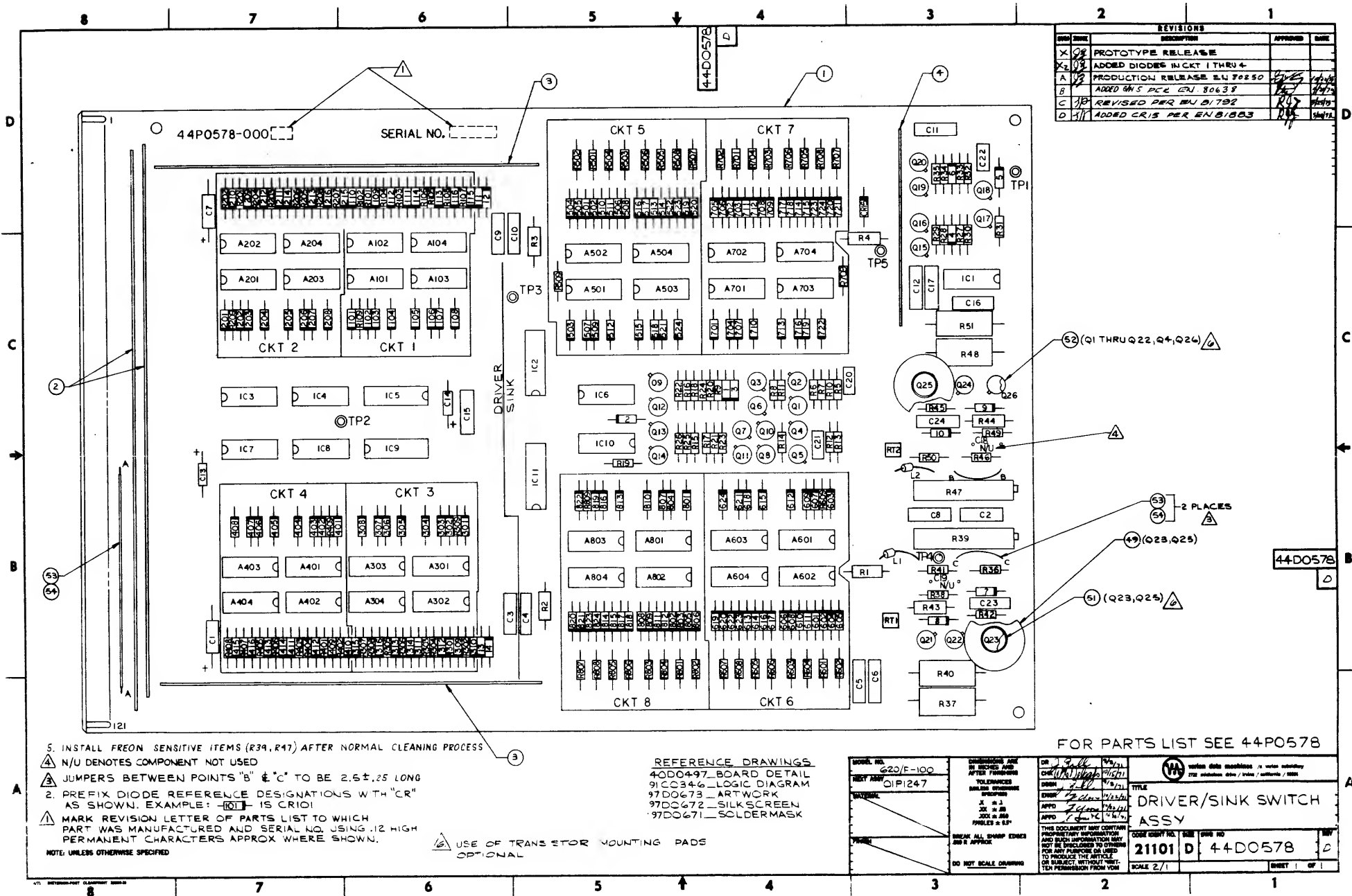
FOR PARTS LIST SEE 44PO521

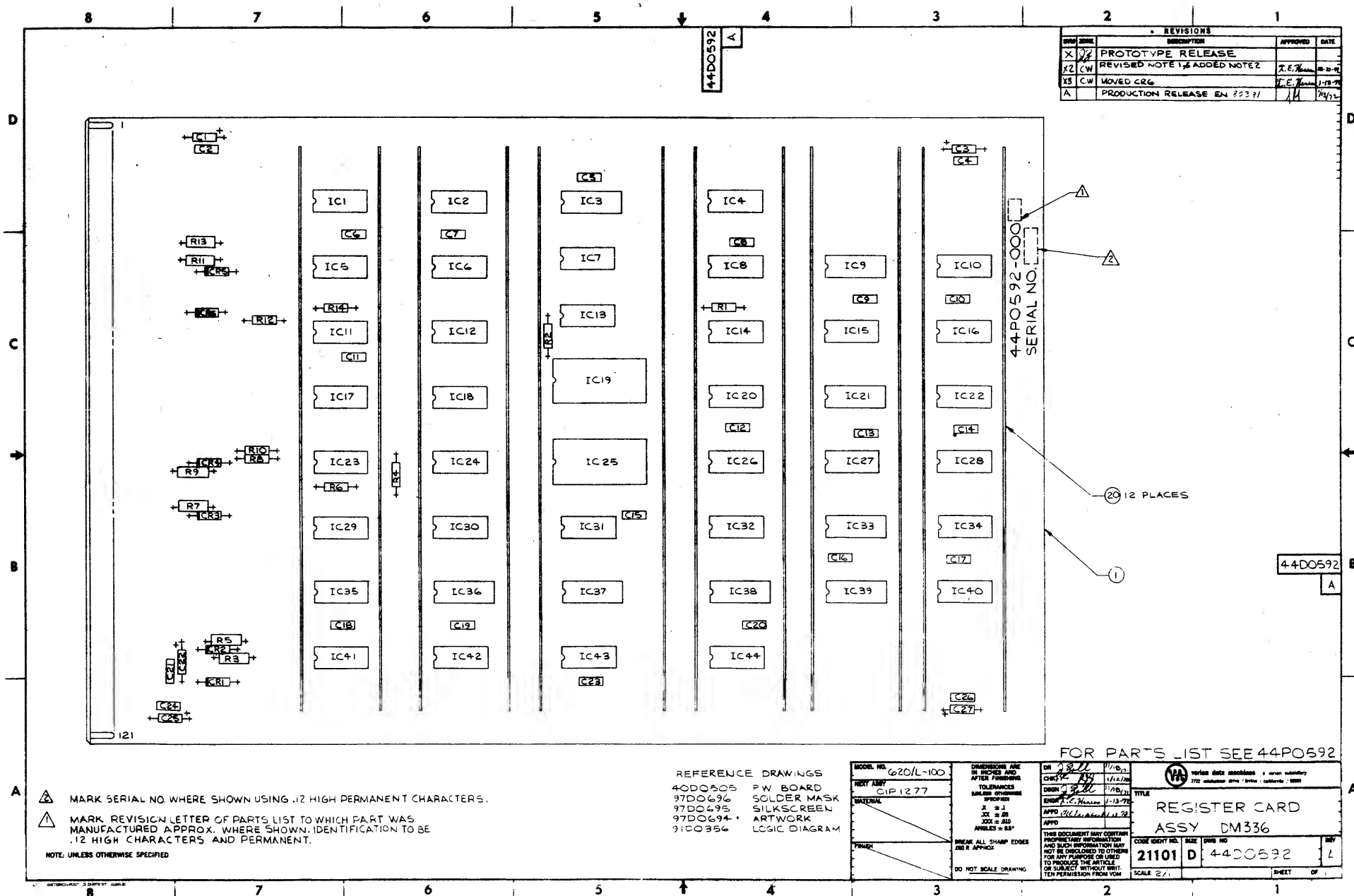
REFERENCE DRAWINGS
44DO469 BOARD DETAIL
91DO297 LOGIC DIAGRAM
97DO566 ARTWORK
97DO567 SILKSCREEN
97DO568 SOLDER MASK

DRAWING NO. 620 L NEXT REV. 01P1101 PREPARED BY CHECKED BY APPROVED BY DATE NO. OF COPIES NO. OF SETS NO. OF SETS NO. OF SETS	THIS DOCUMENT IS THE PROPERTY OF THE U.S. GOVERNMENT AND IS NOT TO BE DISTRIBUTED OUTSIDE THE U.S. GOVERNMENT WITHOUT PERMISSION FROM THE U.S. GOVERNMENT.	TITLE MEMORY BUFFER BOARD ASSY DRAWING NO. 21101 D PART NO. 44DO521 SCALE 2/1 SHEET 1 OF 1
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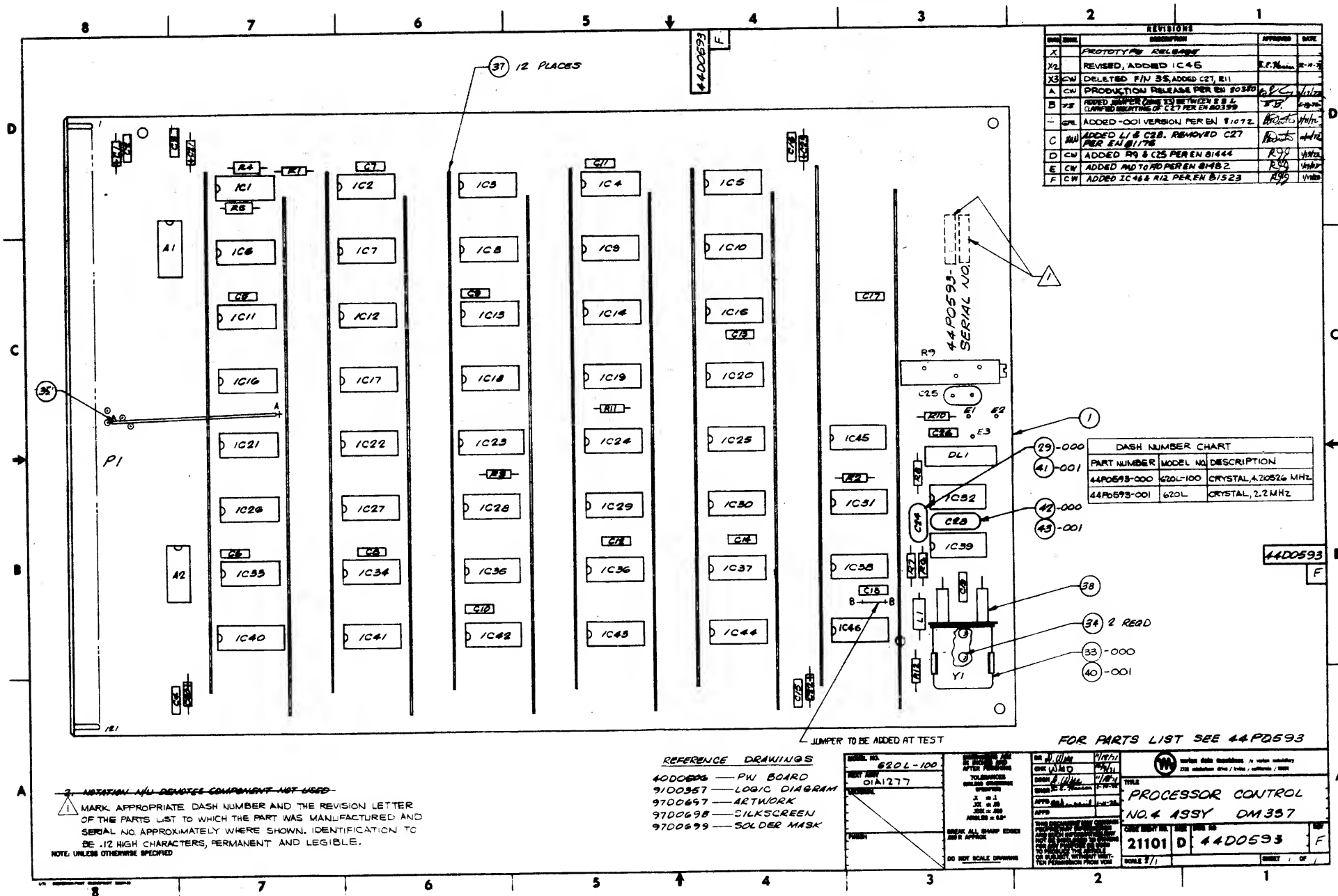
MARK REVISION LETTER OF THE PARTS LIST TO WHICH THE PART WAS MANUFACTURED AND THE SERIAL NUMBER OF THE ASSEMBLY APPROX WHERE SHOWN. IDENTIFICATION TO BE .12 HIGH CHARACTERS AND PERMANENT.

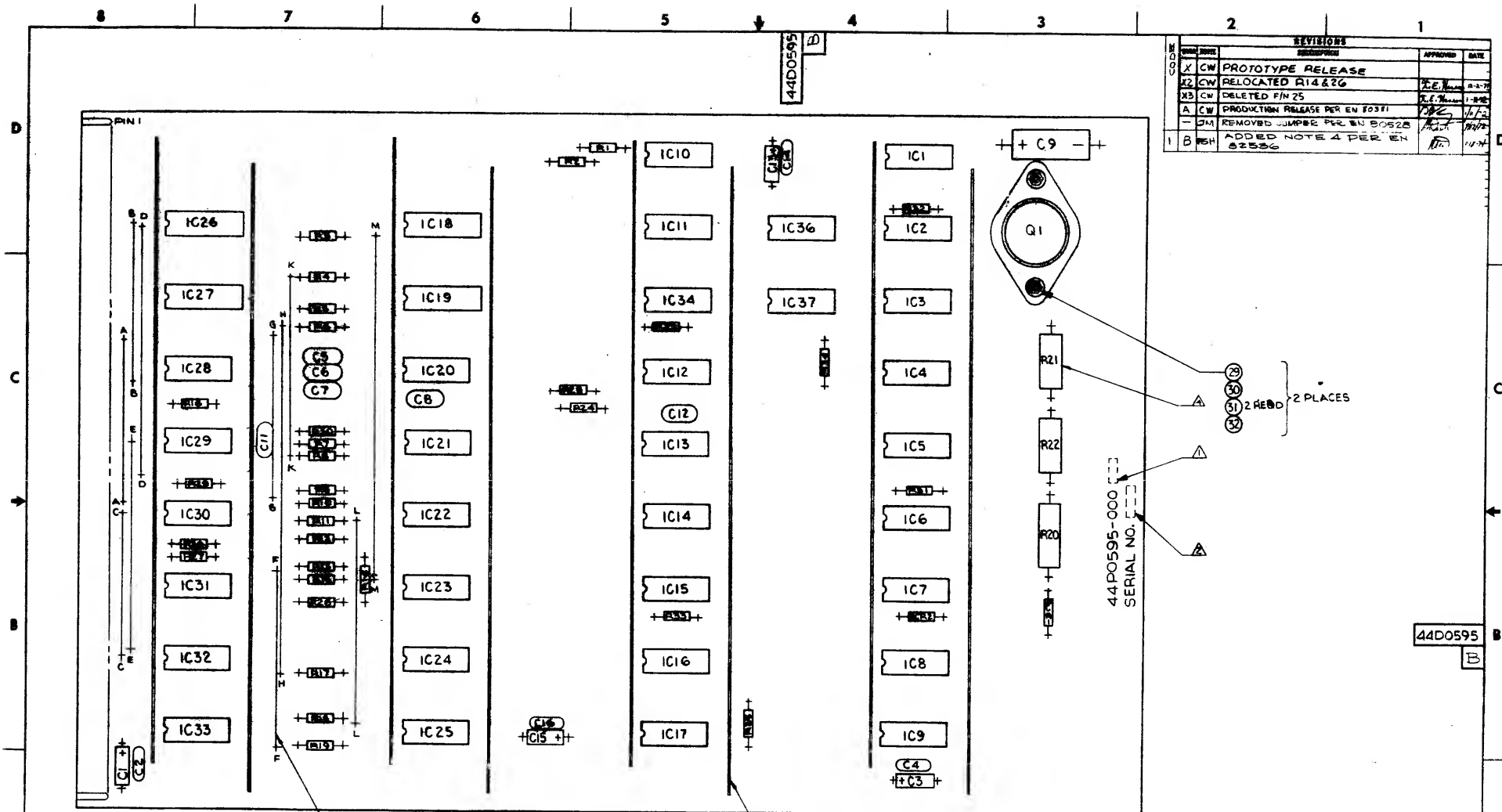
NOTE: UNLESS OTHERWISE SPECIFIED





CAUTION: EN 82268
AFFECTS THIS DWG.





REVISIONS			
NO.	DATE	DESCRIPTION	APPROVED
1	10/1/72	PROTOTYPE RELEASE	
2	10/1/72	RELOCATED R14 & R26	
3	10/1/72	DELETED F/N 25	
4	10/1/72	PRODUCTION RELEASE PER EN 80528	
5	10/1/72	REMOVED JUMPER PER EN 80528	
6	10/1/72	ADDED NOTE 4 PER EN 82556	

△ DELAY INSERTION OF R21 UNTIL AFTER CLEANING IN FREON.

3. MOUNT R20, R21 & R22 .06" OFF OF BOARD
2. MARK SERIAL NO. WHERE SHOWN USING .12 HIGH PERMANENT CHARACTERS.

△ MARK REVISION LETTER OF PARTS LIST TO WHICH PART WAS MANUFACTURED, APPROX. WHERE SHOWN, IDENTIFICATION TO BE .12 HIGH CHARACTERS & PERMANENT.

NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
LOGIC DIAGRAM --- 91D0359
PHOTOMASTER --- 97D0703
BOARD DETAIL --- 40D0508

SOLDERMASK --- 97D0705
SILKSCREEN --- 97D0704

MODEL NO. 620/L-100
TEST UNIT DIA/277

GENERAL
SPECIFIC
FRESH

DESIGNER'S USE
REVISIONS
TOLERANCES
UNLESS OTHERWISE SPECIFIED
X = .1
X = .25
X = .5
X = .75
X = 1.0
X = 1.5
X = 2.0
X = 2.5
X = 3.0
X = 3.5
X = 4.0
X = 4.5
X = 5.0
X = 5.5
X = 6.0
X = 6.5
X = 7.0
X = 7.5
X = 8.0
X = 8.5
X = 9.0
X = 9.5
X = 10.0

OR C. VALERIE 10-12-71
CHKD BY 10/1/72
DRAWN BY 10/1/72
ENGR BY 10/1/72
APPROVED BY 10/1/72
APPROVED BY 10/1/72

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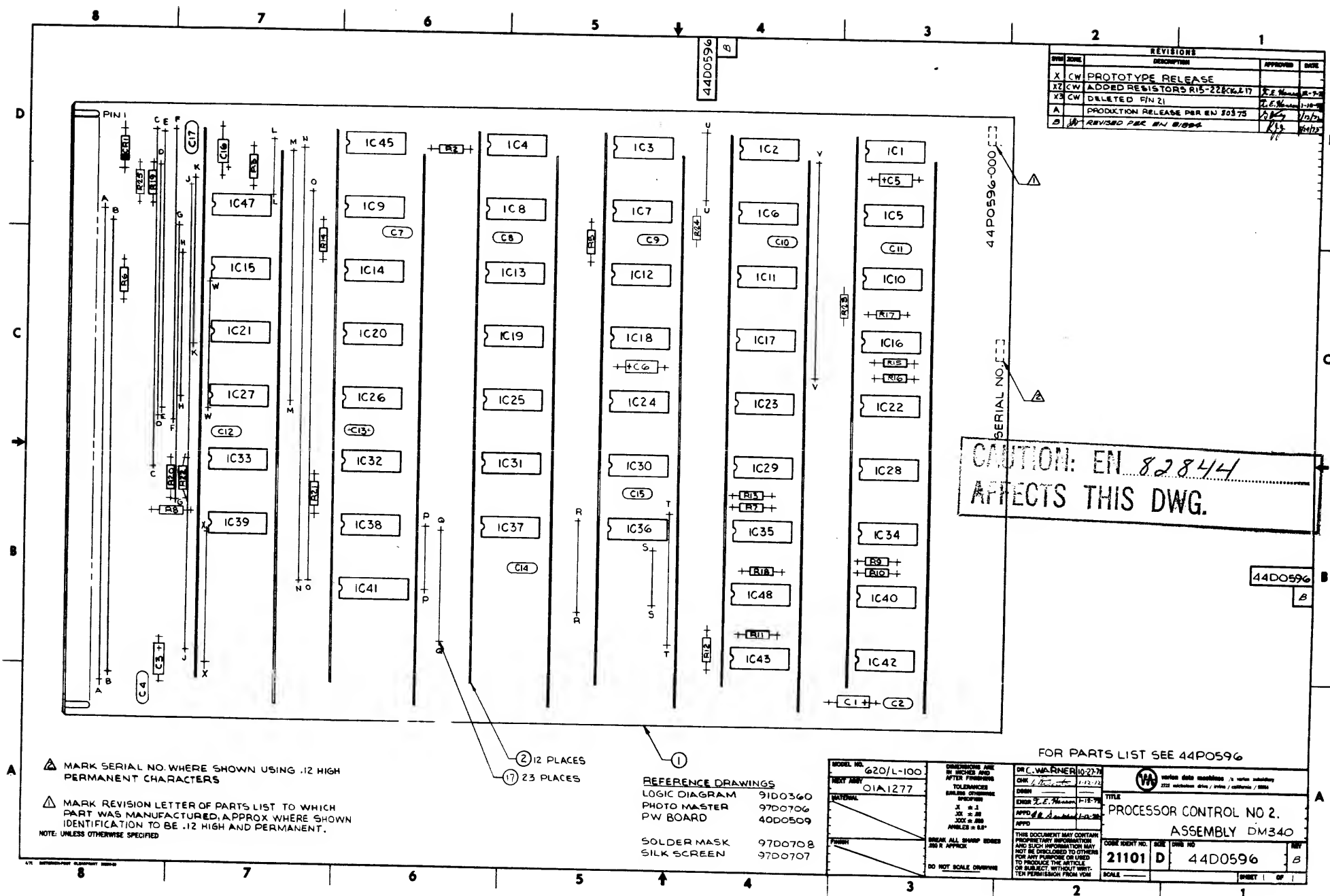
FOR PARTS LIST SEE 44D0595

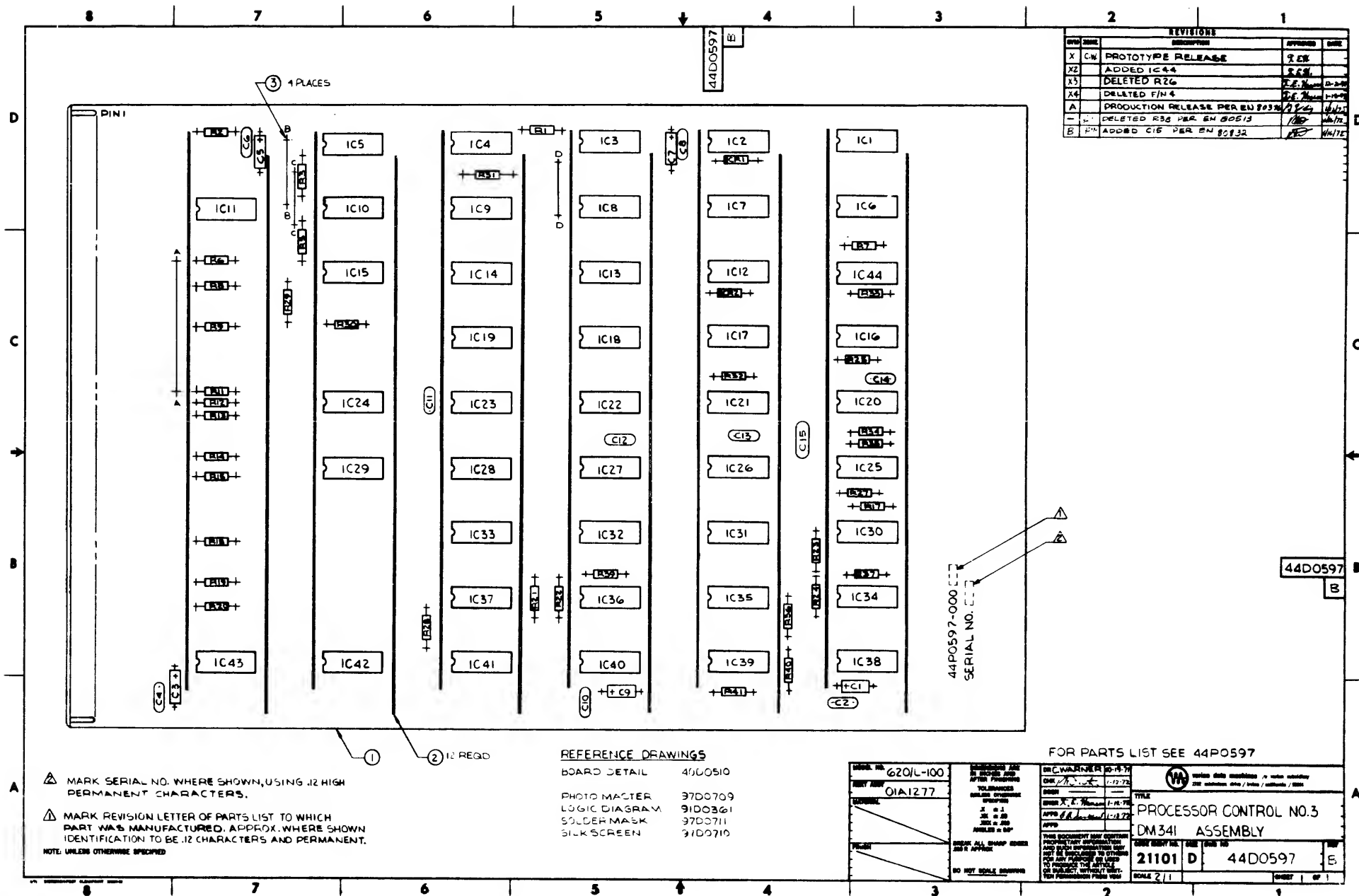
PROCESSOR CONTROL NO. 1
DM 339-ASSEMBLY

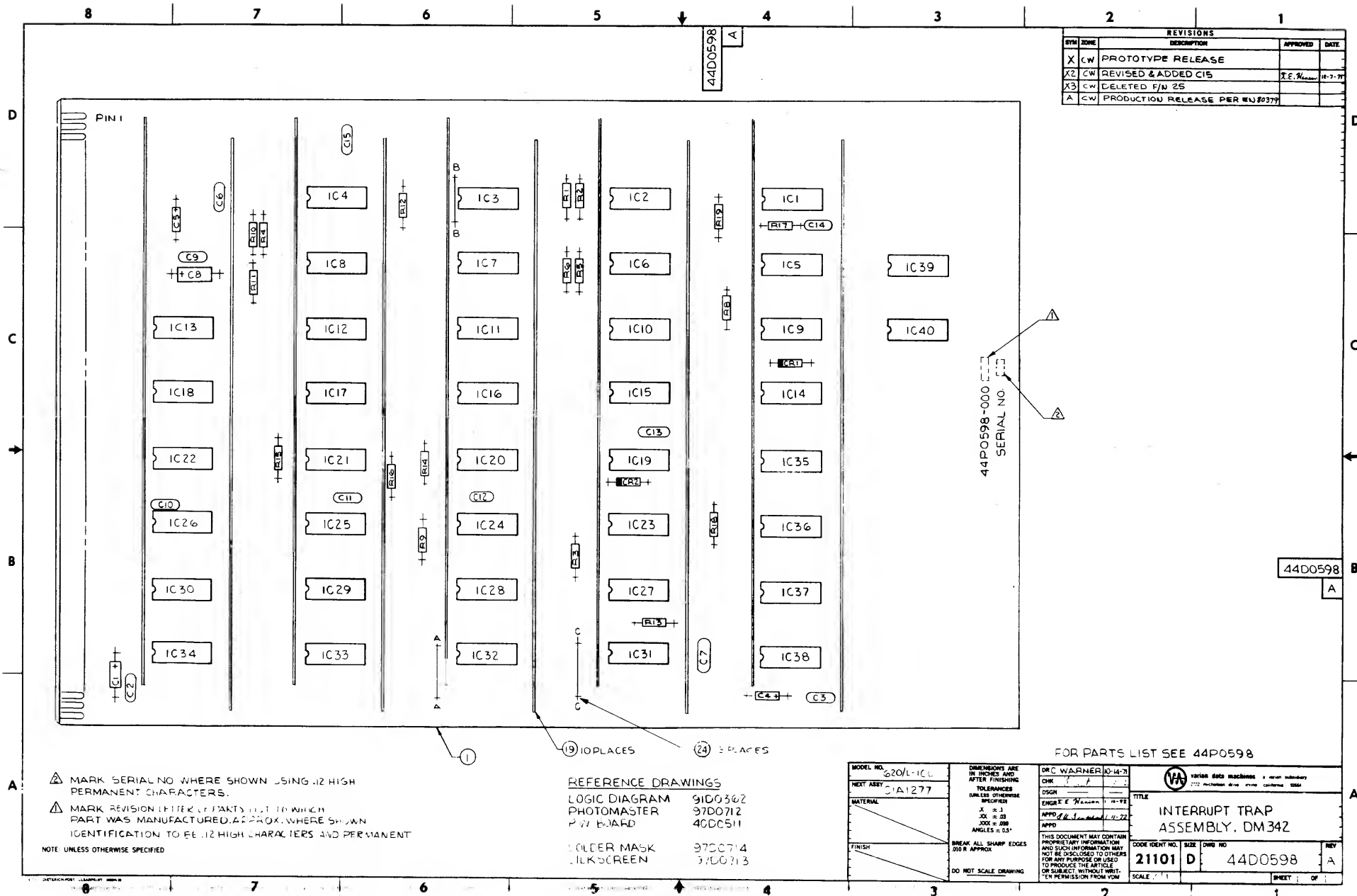
CASE IDENT NO. 21101 D
DATE 44D0595

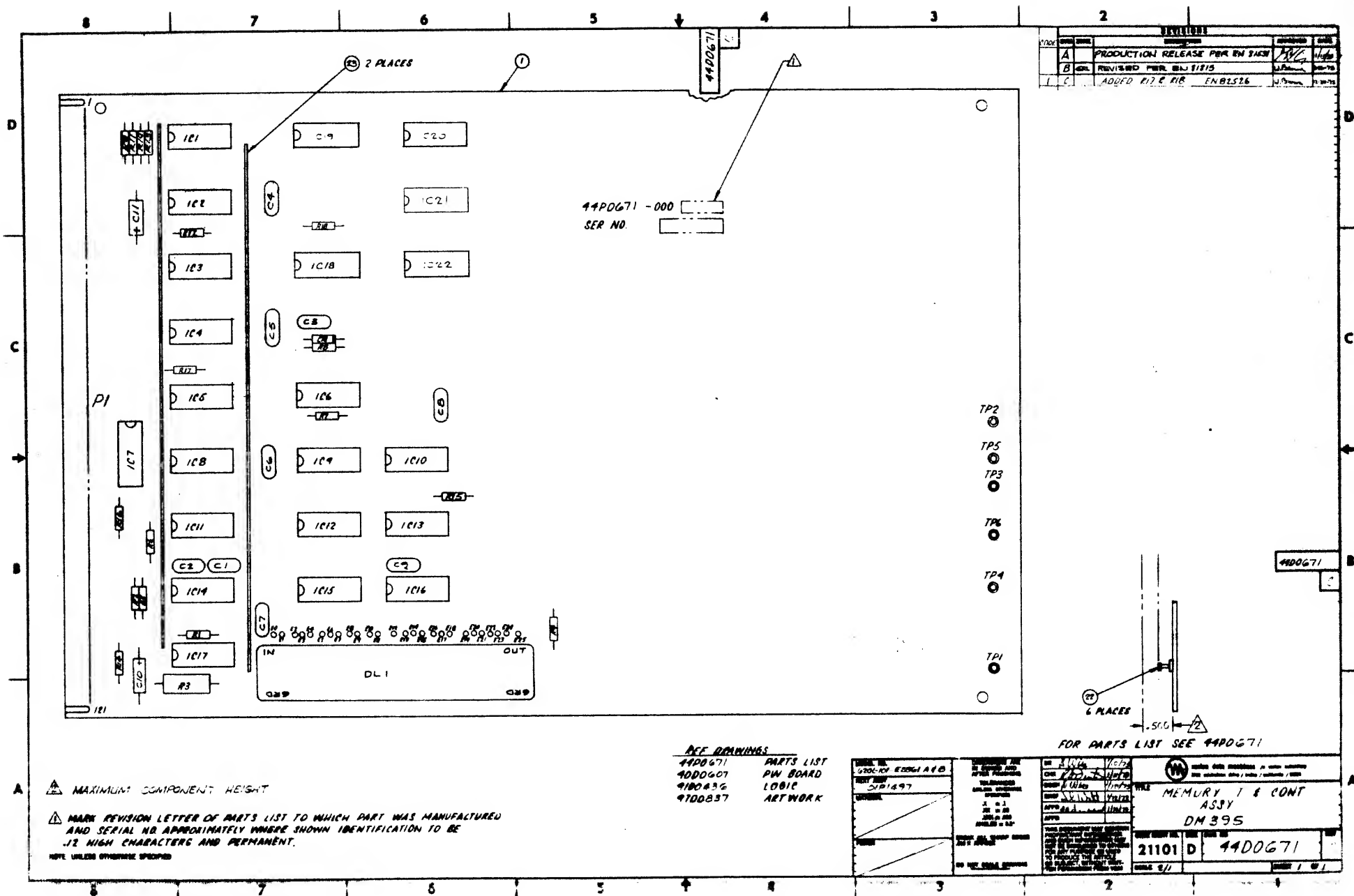
SCALE 2/1

SHEET 1 OF 1

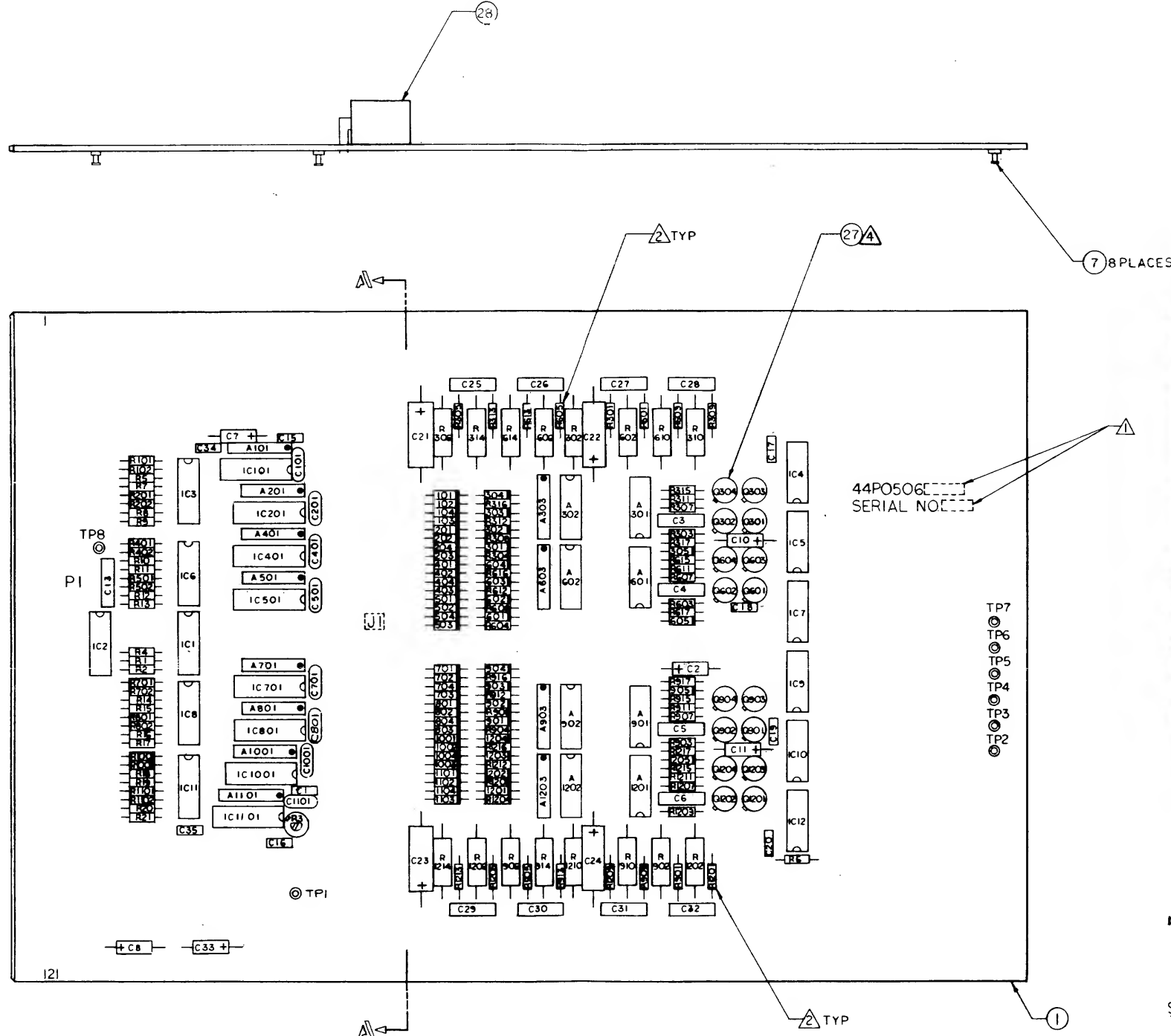








REVISIONS			
REV	ZONE	DESCRIPTION	DATE
X2		PROTOTYPE RELEASE	
A		PRODUCTION RELEASE EN 5210	8/1/72
B		RELOCATED F-F & E-E EN 5210	8/1/72
C	811	REMOVED CONNECTOR MTG HARDWARE AND STANDOFFS (6 HARDWARE), ADD WIRE 3 PER EN 5210	8/1/72
D	114	ADJUSTED EDGE OF B3K2 AND TP2-7 PER EN 5210	8/1/72
E	814	ADDED DASH NO CHART TO F/D PER EN 50415	8/1/72
F		REMOVED ALL UPPER WIRES PER EN 50793	
G	CW	ADDED G/N4 PER EN 81135	8/2/72
H	115	REV. SEL. PLF EN 51712	8/2/72



△ USE OF TRANSISTOR PADS OPTIONAL.

3. TOP SOLDER FILLET NOT REQUIRED ON INACCESSIBLE COMPONENT OR CONNECTOR.

△ VALUE FOR THE FOLLOWING COMPONENTS TO BE SELECTED AT FINAL TEST: R301, 305, 309, 313, 601, 605, 609, 613, 901, 905, 909, 913, 1201, 1205, 1209, 1213.

△ MARK APPLICABLE 44P0506 DASH NUMBER AND REVISION LETTER TO WHICH PART WAS MANUFACTURED AND SERIAL NUMBER USING 12 HIGH CHARACTERS APPROXIMATELY WHERE SHOWN
NOTE: UNLESS OTHERWISE SPECIFIED

DASH NUMBER CHART	
PART NUMBER	DESCRIPTION
44P0506-000	SENSE & INHIBIT ASSY
44P0506-001	SENSE & INHIBIT ASSY

FOR PARTS LIST SEE 44P0506

REFERENCE DRAWINGS	
40D0454	P.W. BOARD
44P0506	PARTS LIST
91D0285	LOGIC DIAGRAM
97D0533	ARTWORK
97D0555	SILKSCREEN
97D0556	SOLDER MASK

DESG. NO. 629/L Q1F-034 DATE 8/1/72 BY J. H. H. / J. H. H. CHECKED J. H. H. / J. H. H. APPROVED J. H. H. / J. H. H. NO NOT SCALE DRAWING	THIS DOCUMENT MAY CONTAIN PROPRIETARY INFORMATION AND SUCH INFORMATION MAY NOT BE DISCLOSED OR USED FOR ANY PURPOSE OR IN ANY MANNER WITHOUT THE WRITTEN PERMISSION OF THE COMPANY.	TITLE SENSE/INHIBIT ASSY DM288 DRAWING NO. 21101 REV. E SCALE 2/1 SHEET 1 OF 1	PART NO. 44P0506 REV. E
--	---	--	----------------------------

DWG NO. 44P0671

REVISIONS

REV	EN	CHG CODE	DESCRIPTION	DR	APPD
F	B2526	1	UPDATED REV LTR OF REF DWGS. WAS: B, UPDATED REV LTR OF FIND N° 1, WAS: C, REVISED QTY OF PIN 16, WAS: 12, REVISED WFT PART N° FOR FIND N° 26, WAS: 49A0308-000	W.E.	1-2-72

NEXT ASSEMBLY

DIAM 97

MODEL NO.

620/L-101-E2861
A&B



varian data machines / a varian subsidiary
2722 michelson drive / irvine / california / 92664

BY D. WISE / 1/10/73

CHK M.A.D.

CODE IDENT NO. 21101

TITLE
PARTS LIST - MEMORY
T&C CONT ASSY, DM395

DESN

ENGR

APPD

APPD

THIS DOCUMENT MAY CONTAIN
PROPRIETARY INFORMATION AND
SUCH INFORMATION MAY NOT BE
DISCLOSED TO OTHERS FOR ANY
PURPOSE OR USED TO PRODUCE
THE ARTICLE OR SUBJECT, WITH-
OUT PERMISSION FROM VDM

SIZE DWG NO. REV

A 44P0671 F

SHEET 1 OF 3

PARTS LIST

CODE IDENT 21101

QUANTITY REQ'D PER DASH NO	000	FIND NO	PART NUMBER	DESCRIPTION	REMARKS	ZONE
REF -			44DOG71 C	ASSEMBLY		
REF -			71DOG56 C	LOGIC DIAGRAM		
REF -			97DO837	ARTWORK		
REF -			99A093E	BOOTSTRAP LOADER PROTECT (E-2047)	ENGR'S DESCRIPTION	
1			4000007-000 D	RW. BOARD		
2			49A0039-000	INTEGRATED CIRCUIT	IC6,11	
4			49A0023-000		IC7,13,18,19	
1			49A0041-000		IC16	
2			49A0654-001		IC10,22	
1			49A0003-000		IC9	
1			49A0056-000		IC12	
1			49A0142-001		IC14	
2			49A0079-000		IC8,15	
1			49A0019-000	INTEGRATED CIRCUIT	IC17	
2			69N1500-101	CAP, 100pf 500V	CI,2	
7			71A0004-100	CAP, .1uf 50V	C3-9	
2			71N1000-475	CAP, 4.7uf 10V 10%	CI0,11	
1			71S1017-000	DIODE	CR1	
1			80A0039-001	DELAY LINE	DL1	

NEXT ASSY 01A1497		MODEL NO 6201-101-0001		APD 044 1-12-73		TITLE: PARTS LIST	
REV	A	B	C	D	E	MEMORY T & CONT	
EN NO	8153181815	8153181815	8153181815	8153181815	8153181815	ASSY DM 395	
DATE	1/10/73	1/10/73	1/10/73	1/10/73	1/10/73	DWG NO 44POG71	
DR	WILLIAM GRL	WILLIAM GRL	WILLIAM GRL	WILLIAM GRL	WILLIAM GRL	REV F	
CHK	WILLIAM GRL	WILLIAM GRL	WILLIAM GRL	WILLIAM GRL	WILLIAM GRL	SHEET 2 OF 3	

DWG NO. 44POG71

QUANTITY BOM IN DASH NO			PARTS LIST		CODE IDENT: 21101		REMARKS	ZONE
QTY	UNIT	QTY	PART NUMBER	DESCRIPTION				
14	16	1	65N2500-102	RES, 1K, 1/4W, ±5%			R1, 2, 5, 7, 9-16, 17, 18	
1	17	1	65N2500-101	RES, 180Ω, 1/4W, ±5%			R4	
1	18	1	65N2500-202	RES, 2K, 1/4W, ±5%			R6	
1	19							
1	20	1	65N2500-103	RES, 10K, 1/4W, ±5%			R3	
1	21	1	65N1010-680	RES, 68Ω, 1/4W, ±5%			R5	
6	22	6	58A0066-002	TERMINAL			TP1 THRU TP6	
2	23	2	53C0194-000	BUS, POWER				
1	24	1	49A0060-000	INTEG CIRCUIT			IC20	
1	25	1	49A0007-000				IC2	
1	26	1	49A0042-000				IC4	
2	27	2	49A0510-000				IC1, 5	
1	28	1	49A0516-000	INTEG CIRCUIT			IC3	
1	29	1	49A0128-001	INTEG CIRCUIT			IC21	

NOTES:

DWG NO. 44POG71
REV E


SHEET 2 OF 3

NOTES: (UNLESS OTHERWISE SPECIFIED)

1. ALL RESISTORS ARE 1/4W, $\pm 5\%$
2. THIS DRAWING CONSISTS OF SHEETS 1.0, 2.0, 3.0, 4.0, 5.0, 6.0, 7.0, 8.0, 9.0, 10.0
3. ON IC'S 1, 3, 4, 6, 7, 8 & 10 PIN 14 IS CONNECTED TO +5 AND PIN 7 IS CONNECTED TO GRD.
4. PREFIX COMPONENT REFERENCE DESIGNATION NO. WITH CIRCUIT NO, EXAMPLE: CKT 1, R101; CKT 2, R202

REFERENCE DESIGNATIONS	
LAST USED	NOT USED
C24	C18, 19
CR15, CR_24	
L2	
Q26	
RS1, R_09	
RT 2	
IC 11	
A_04	

REFERENCE DRAWINGS	
44D0578	ASSEMBLY
44P0578	PARTS LIST
40D0497	BOARD DETAIL
97D0673	ARTWORK
97D0672	SILKSCREEN
97D0671	SOLDERMASK

DR: <i>[Signature]</i> 11/7/71 CHK: <i>[Signature]</i> 11/7/71 DESN: <i>[Signature]</i> 11/7/71 ENGR: <i>[Signature]</i> 11/7/71 APPD: <i>[Signature]</i> 11/7/71 APPS: <i>[Signature]</i> 11/7/71		 varian data machines / a varian subsidiary 225 mchabon dr / irvine / california / 92614	
TITLE LOGIC DIAGRAM DR/SK SW			
CODE IDENT NO. 21101	SIZE C	DWS NO 91C0346	REV C
SCALE —		6204-100	SHEET 1.0

REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
X	02	PROTOTYPE RELEASE	
X	02	ADDED DIODES IN CKTS 1-4	
A	02	PRODUCTION RELEASED BY 80250	8/1/74
B		C23 & 24 WAS .33 UF 50VDC EN 80374	4/24/75
C	74	ADDED CRIS PER EN 81003	5/24/75

TABLE OF CONTENTS

DESCRIPTION	SHEET NO.
COVER	1.0
REVISION, TABLE OF CONTENTS &	2.0
CONNECTOR PIN ASSIGNMENTS	
DECOUPLING CAPS. & DRIVER SW'S, POSITIVE	3.0
DRIVER SWITCHES, NEGATIVE	4.0
SINK SWITCHES, NEGATIVE	5.0
SINK SWITCHES, POSITIVE	6.0
DRIVER LOGIC	7.0
SINK LOGIC	8.0
PREDRIVER	9.0
CURRENT SOURCES	10.0

CONNECTOR PI

PINS FUNCTION SHEET

1	GRD	
2		
3	GRD	
4	-12V	
5	GRD	
6	TCRX-	9.0
7	YS6B	8.0
8	YS6A	8.0
9	YS7B	8.0
10	YS7A	8.0
11	YS4B	8.0
12	YS4A	8.0
13	YS5B	8.0
14	YS5A	8.0
15	YS2B	8.0
16	YS2A	8.0
17	YS3B	8.0
18	YS3A	8.0
19	YSOB	8.0
20	YSOA	8.0
21	YSIB	8.0
22	YSIA	8.0
23	CAGB	7.0
24	CA1B	7.0
25	CA7A	7.0
26	CAOB	7.0
27	CA4A	7.0
28	CA3B	7.0
29	CABA	7.0
30	CA2B	7.0
31	CA2A	7.0
32	CA7B	7.0
33	CA3A	7.0
34	CAGB	7.0
35	CAOA	7.0
36	CASB	7.0
37	CAIA	7.0
38	CA4B	7.0
39	SPARE	7.0
40	SPARE	7.0
41	GRD	

PINS FUNCTION SHEET

42	GRD	
43	LIIX+	8.0
44	LIIX+	8.0
45	GRD	
46	LC9X+	8.0
47	GRD	
48	RSTX-	9.0
49	GRD	
50	WESTX-	9.0
51	GRD	
52	SELX-	9.0
53	GRD	
54	LC2X+	9.0
55	GRD	
56	LO5X+	7.0
57	GRD	
58	SDIS	7.0
59	GRD	
60	RYXX-	7.0
61	NOT AVAILABLE	
62	NOT AVAILABLE	
63	GRD	
64	LO4X+	7.0
65	GRD	
66	LOIX+	7.0
67	GRD	
68	LOOX+	7.0
69	GRD	
70	LC3X+	7.0
71	GRD	
72	RWT2-	9.0
73	GRD	
74	LI2X+	9.0
75	GRD	
76	RWT1-	9.0
77	GRD	
78	LC8X+	8.0
79	LC7X+	8.0
80	LC6X+	8.0
81	GRD	
82	GRD	

PINS FUNCTION SHEET

83	SPARE	7.0
84	CC1B	7.0
85	CC6A	7.0
86	CCOB	7.0
87	CC7A	7.0
88	CC3B	7.0
89	CC4A	7.0
90	CC2B	7.0
91	CC5A	7.0
92	CC5B	7.0
93	CC2A	7.0
94	CC4B	7.0
95	CC3A	7.0
96	CC7B	7.0
97	CCOA	7.0
98	CC6B	7.0
99	CC1A	7.0
100	XS6A	8.0
101	XS6B	8.0
102	XS7A	8.0
103	XS7B	8.0
104	XS4A	8.0
105	XS4B	8.0
106	XS5A	8.0
107	XS5B	8.0
108	XS2A	8.0
109	XS2B	8.0
110	XS3A	8.0
111	XS3B	8.0
112	XSOA	8.0
113	-12V	
114	XS1A	8.0
115	XSOB	8.0
116	+5V	
117	XS1B	8.0
118		
119	+12V	
120	+12V	
121	GRD	
122	GRD	

CONNECTOR FUNCTIONS

DATE	REV	DATE	REV
21101	C	91C0346	C
SCALE			

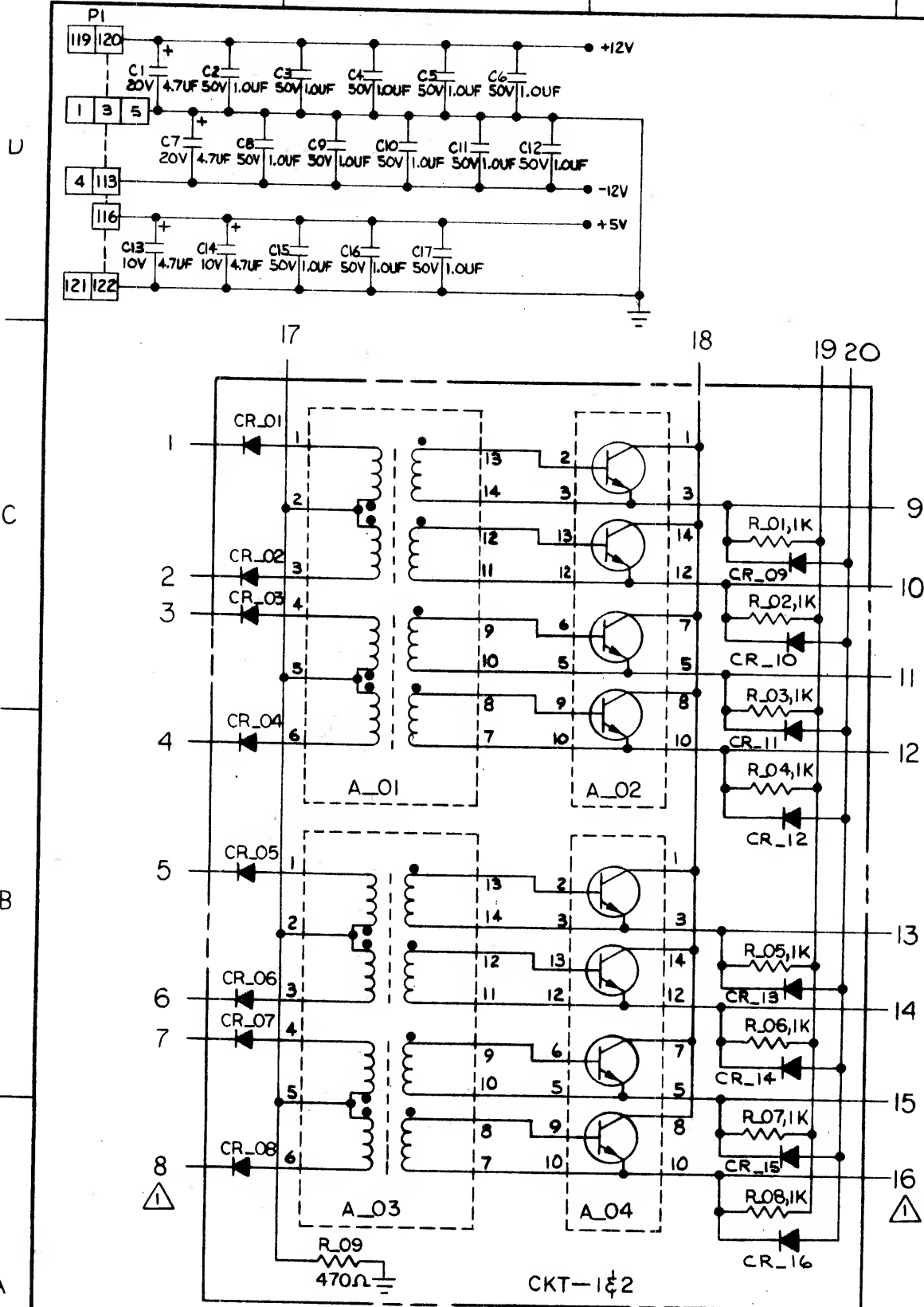
SHEET 2.0

4

3

2

1



⚠ NUMBERS OUTSIDE PHANTOM LINES CORRESPOND TO
CIRCUIT BLOCKS 1 & 2 ON SHT 7

DECOUPLING CAPS. & DRIVER SW'S, POSITIVE

DATE	REV	DATE	REV
21101	C	91C0346	C
SCALE	—	SHEET	3.0

4

3

2

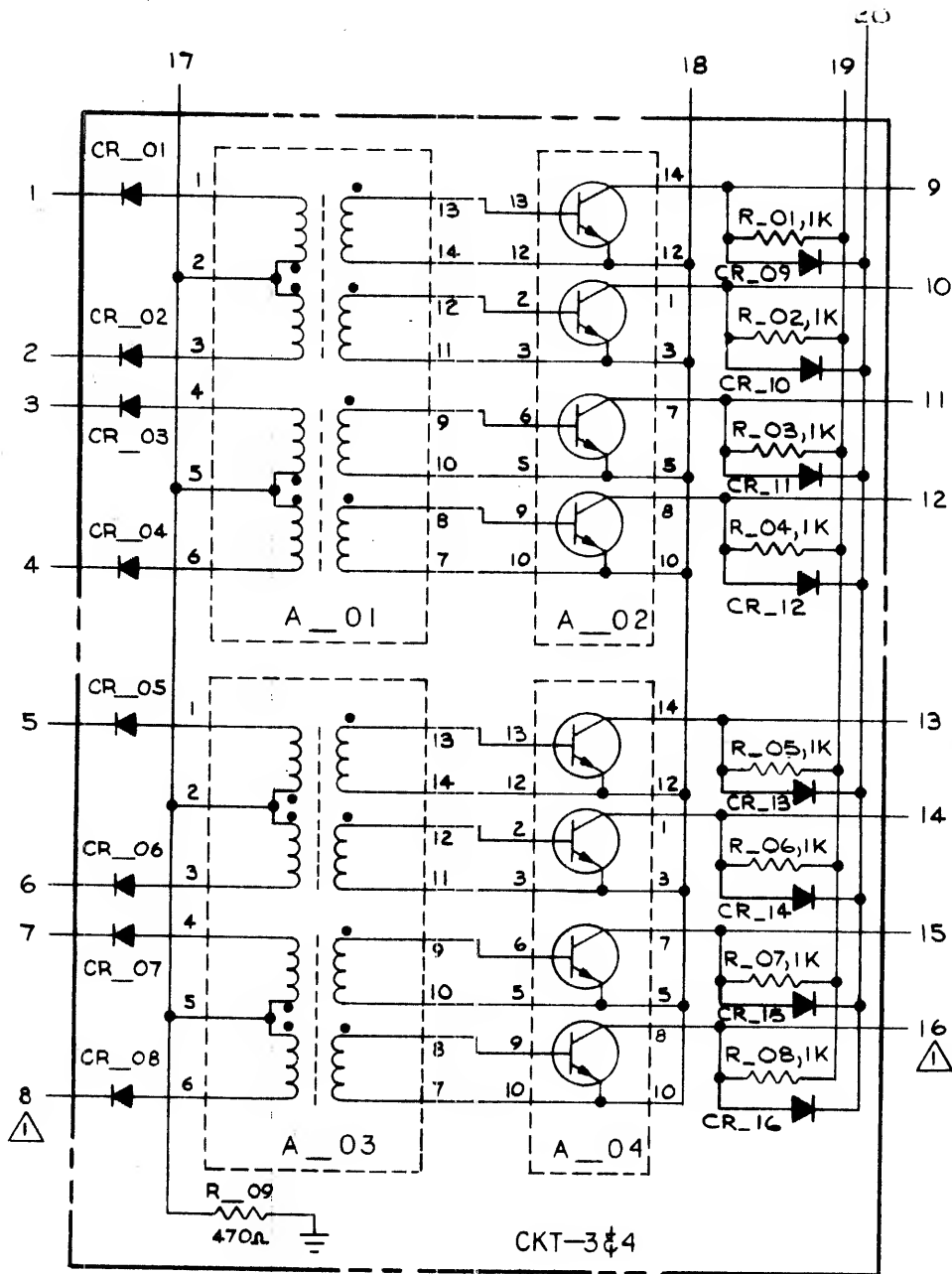
1

D

C

B

A



DRIVER SWITCHES, NEGATIVE

CODE	REV	DATE	BY
21101	C	91C0346	C
SCALE			

SHEET 4.0

4

3

2

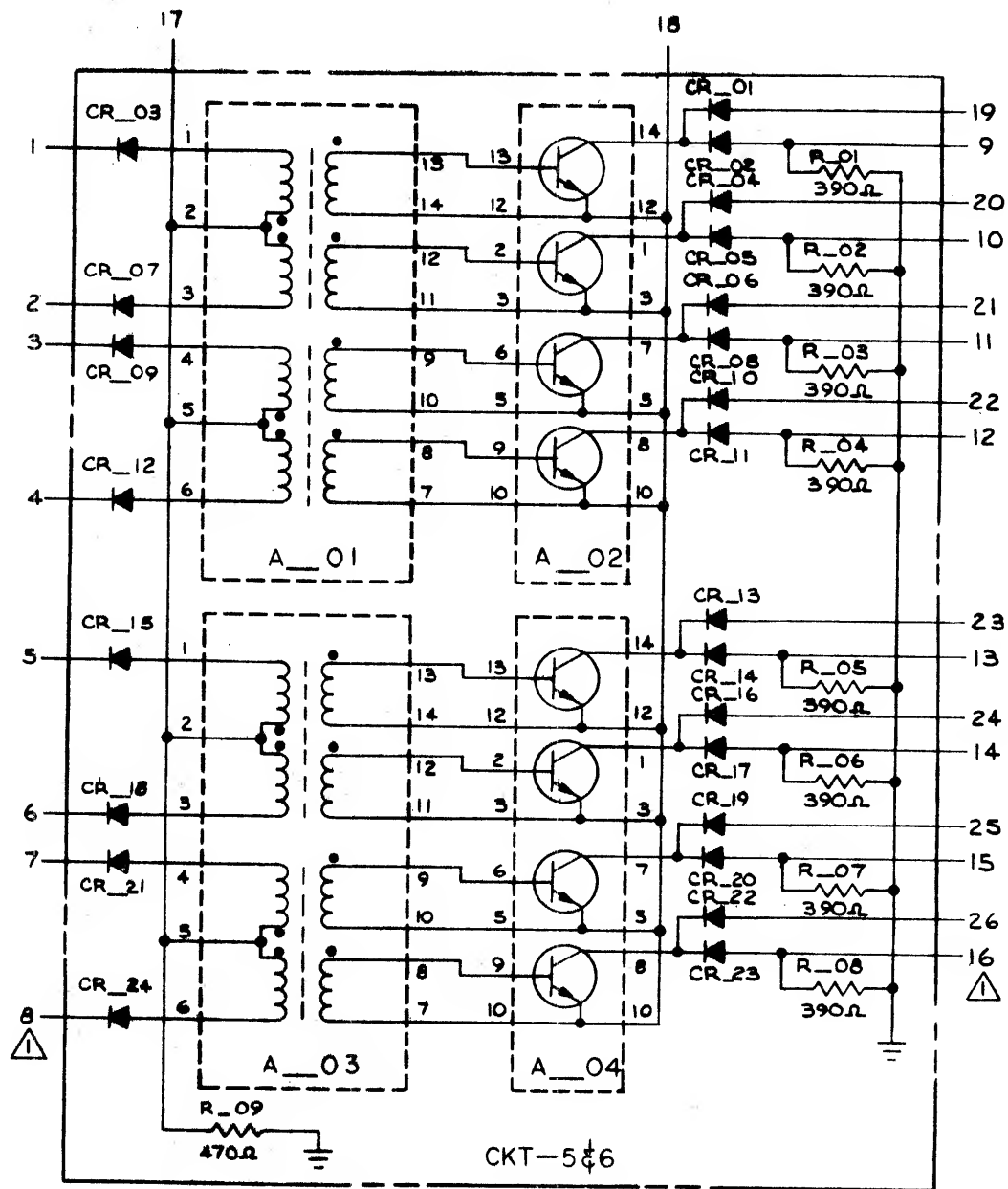
1

D

C

B

A



⚠ NUMBERS OUTSIDE PHANTOM LINES CORRESPOND TO CIRCUIT BLOCKS 5 & 6 ON SHT 8

SINK SWITCH, NEGATIVE

CODE	IDENT NO.	REV	DATE	NO
21101	C	91C0346		C
SCALE	—			
				SHEET 5.0

4

3

2

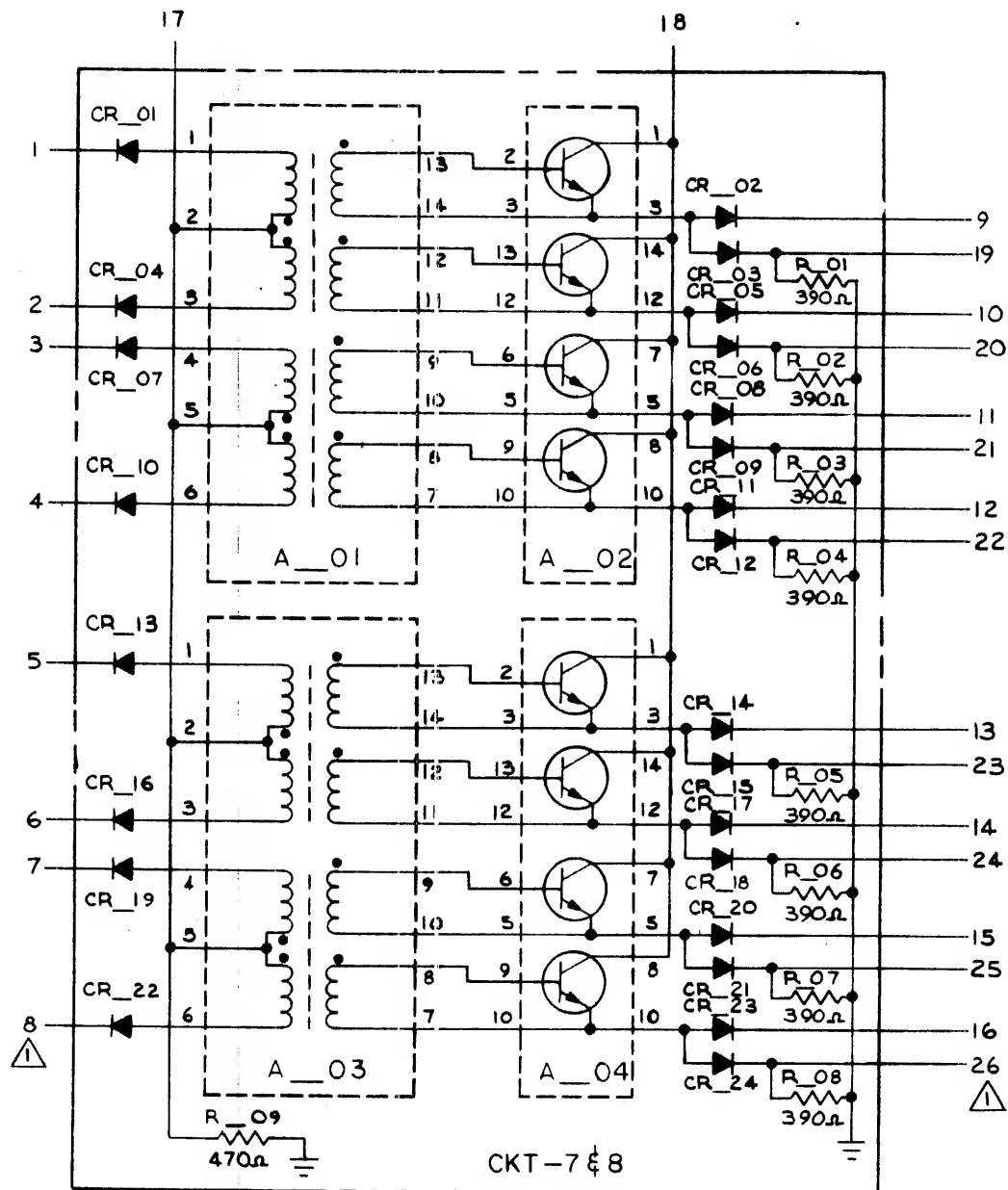
1

D

C

B

A



⚠ NUMBERS OUTSIDE PHANTOM LINES CORRESPOND TO CIRCUIT BLOCKS 7 & 8 ON SHT 8

SINK SWITCH POSITIVE

DATE	REV	DATE	REV
21101	C	91C0346	C
SCALE		SHEET	6.0

4

3

2

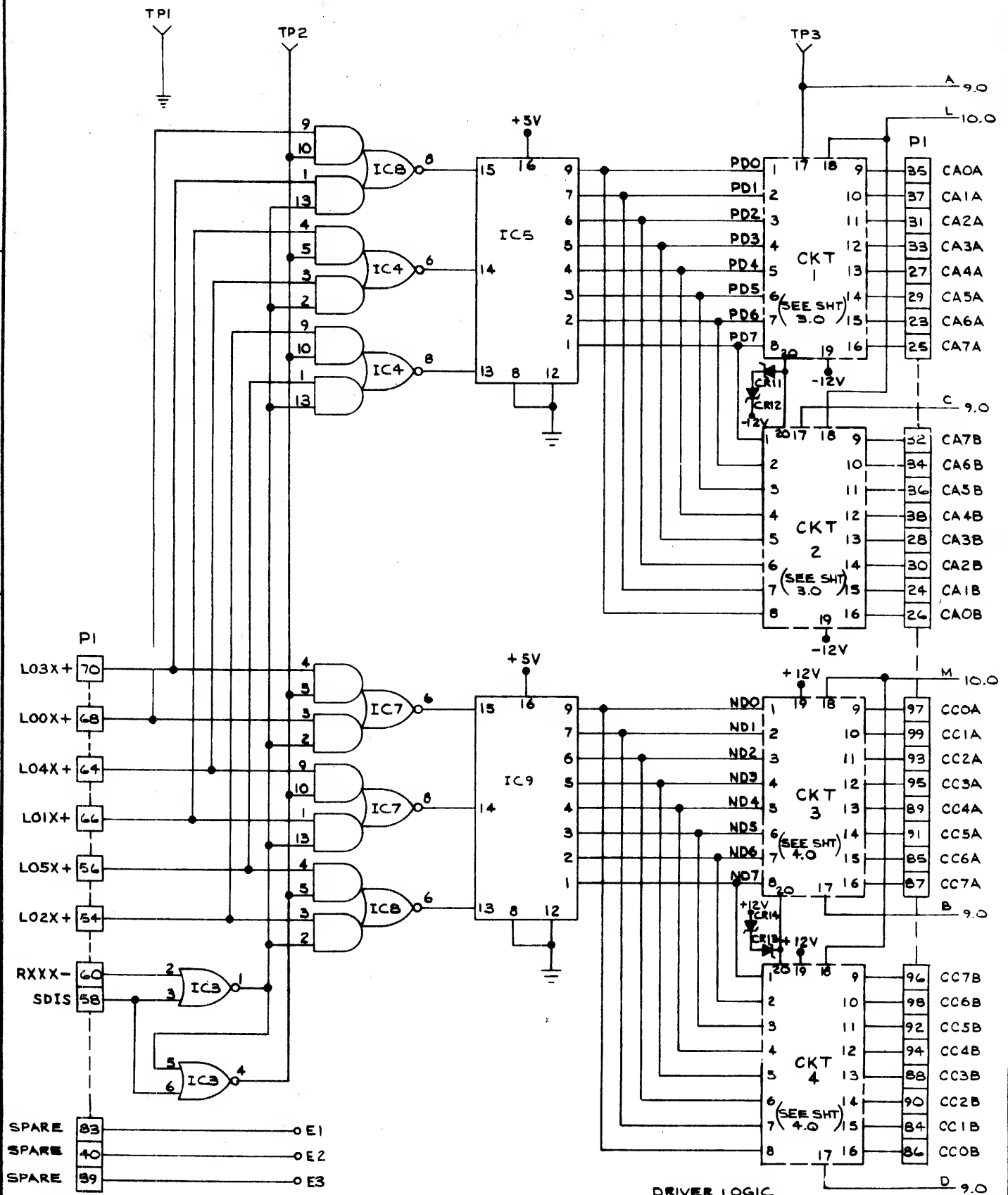
1

D

C

B

A



DRIVER LOGIC

CODE ENTRY NO.	REV	DATE	NO	REV
21101	C	91C0346		C
SCALE				

SHEET 7.0

4

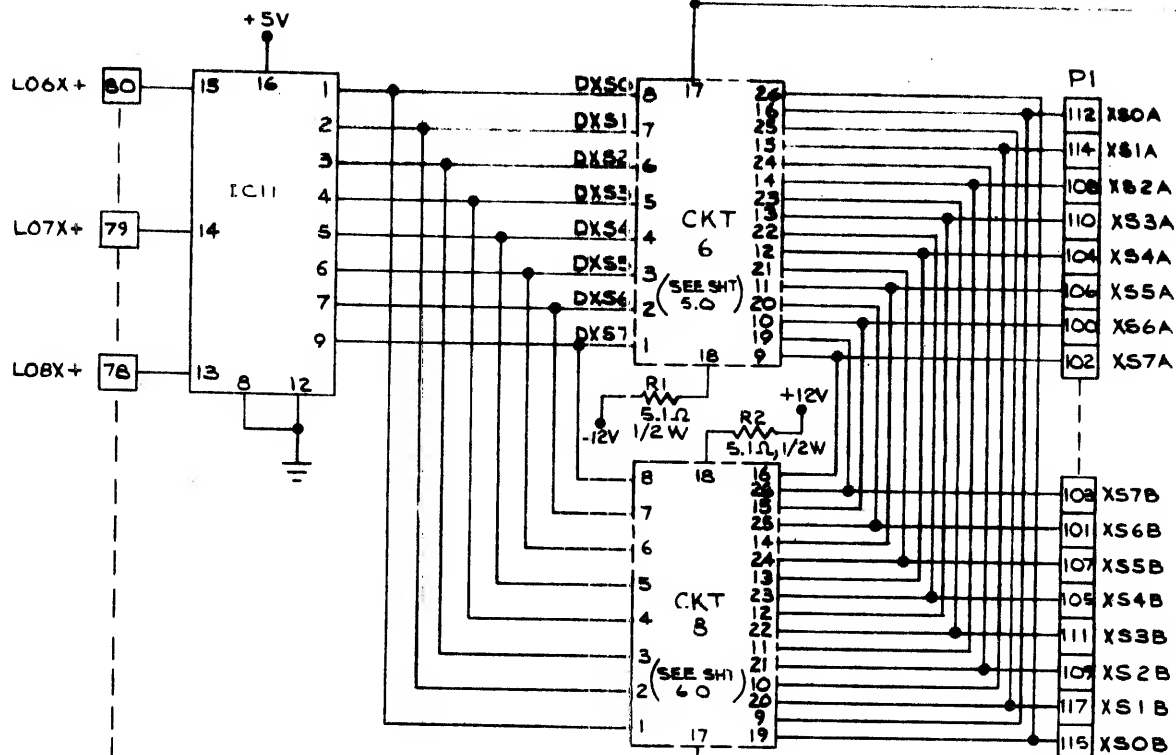
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2

1

D

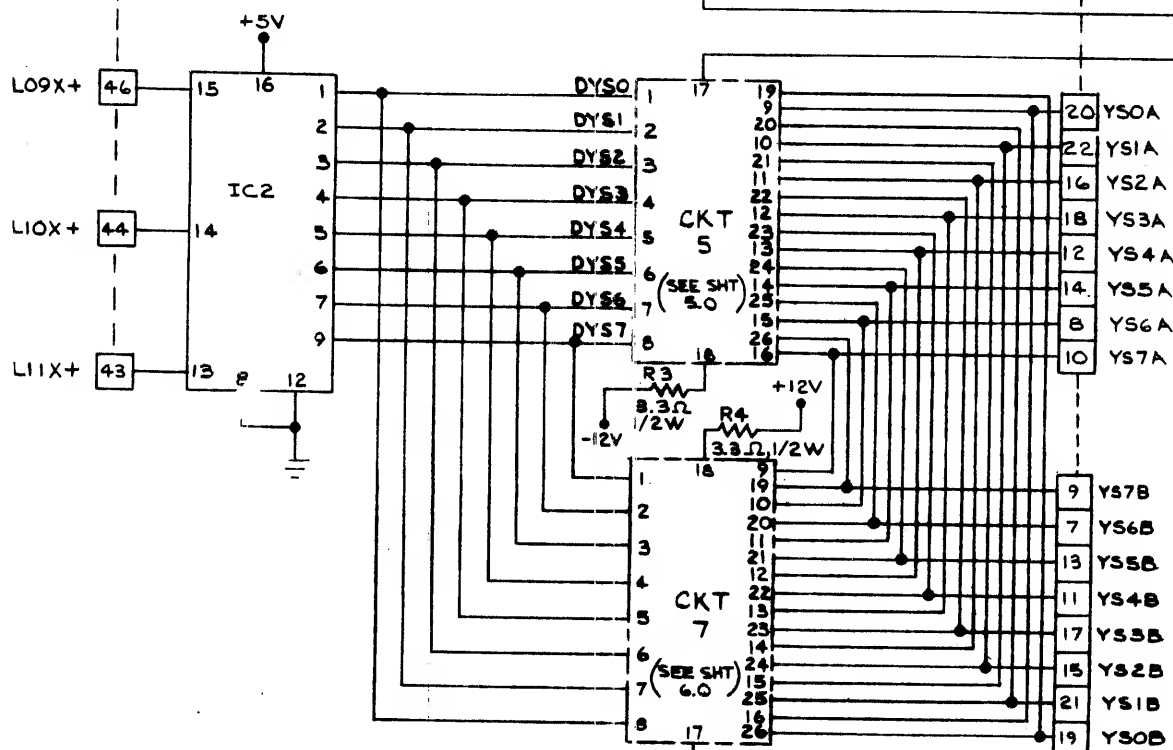
11 4.0



C

B

A



SINK LOGIC CIRCUITS

DATE: 10/10/70

21101 C 91C0346

SCALE: 1/8" = 1"

SHEET 8.0

4

3

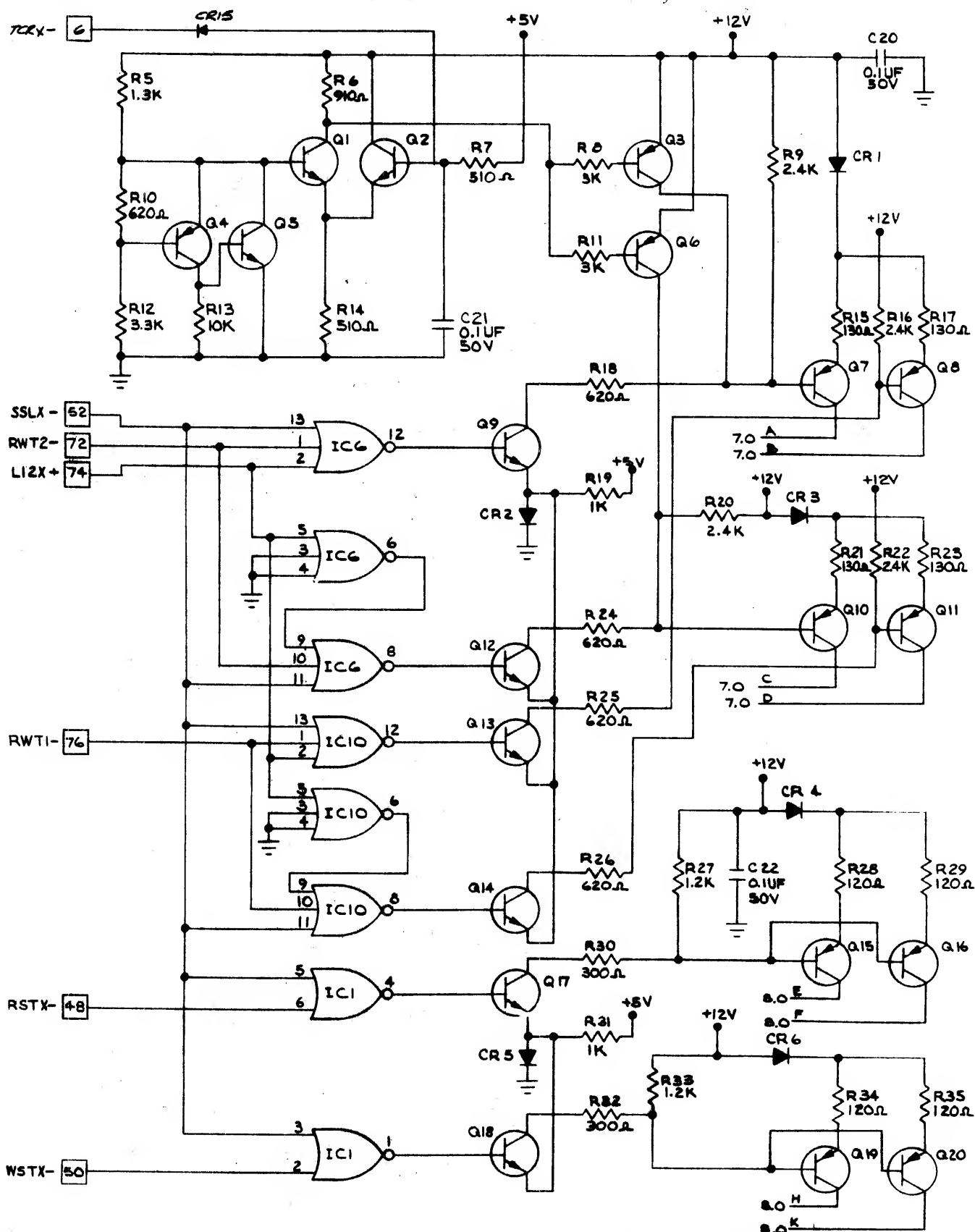
2

1

C

B

A



PREDRIVER CIRCUITS

DATE DESIGNED: 10/10/10	DATE TESTED: 10/10/10	REV: C
21101	C	91C0346
SIZE: —	QUANTITY: 20	

4

3

2

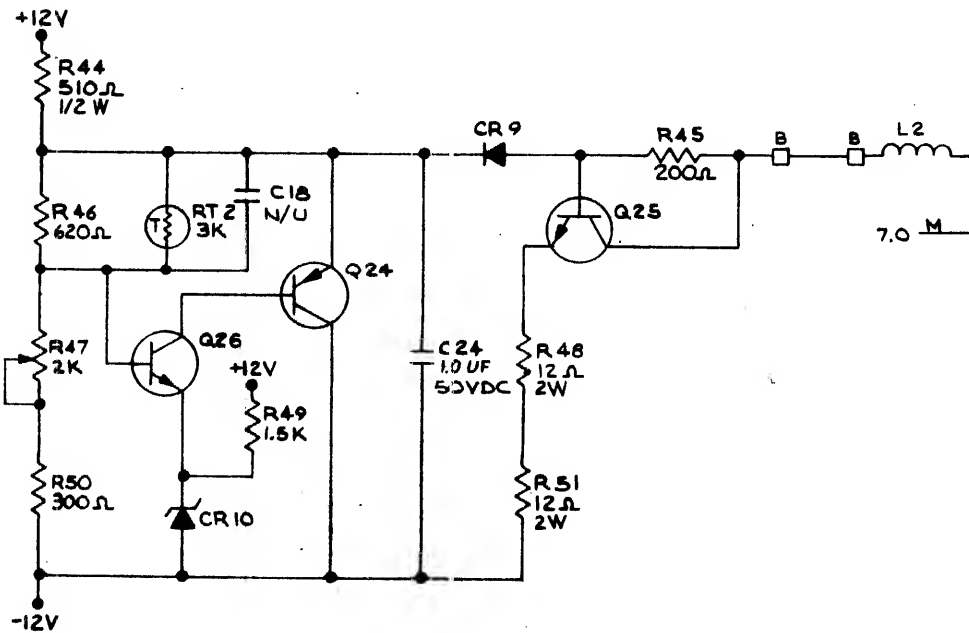
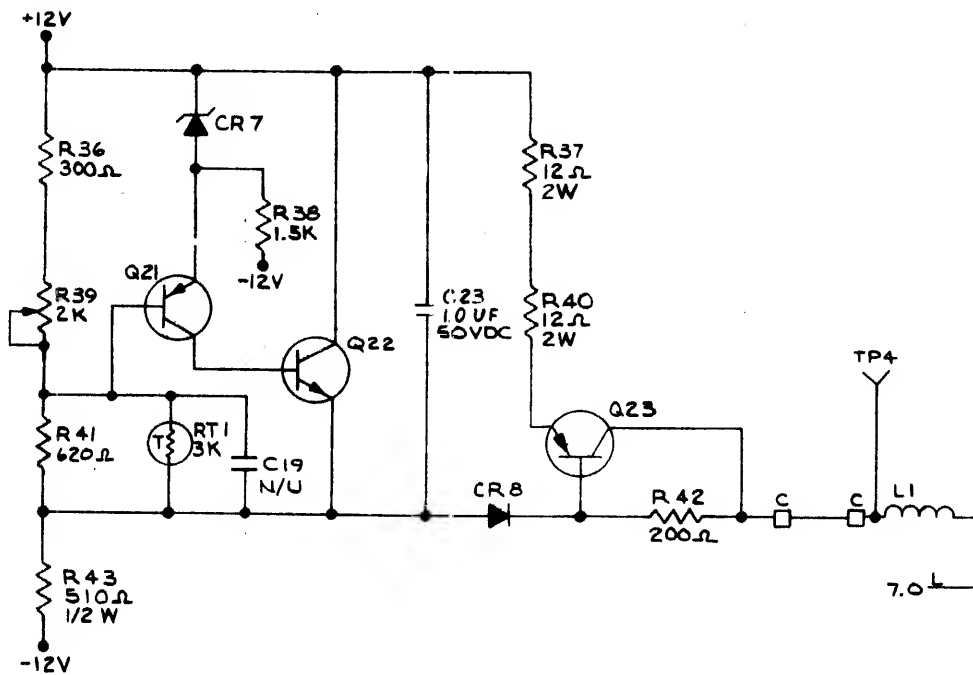
1

D

C

B

A

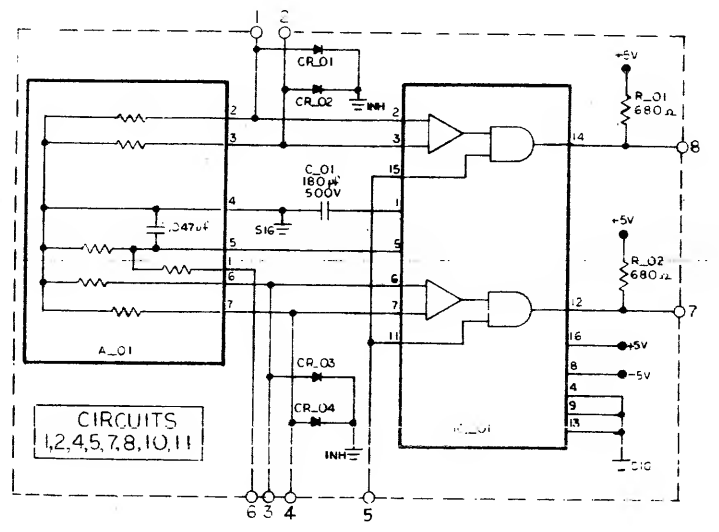
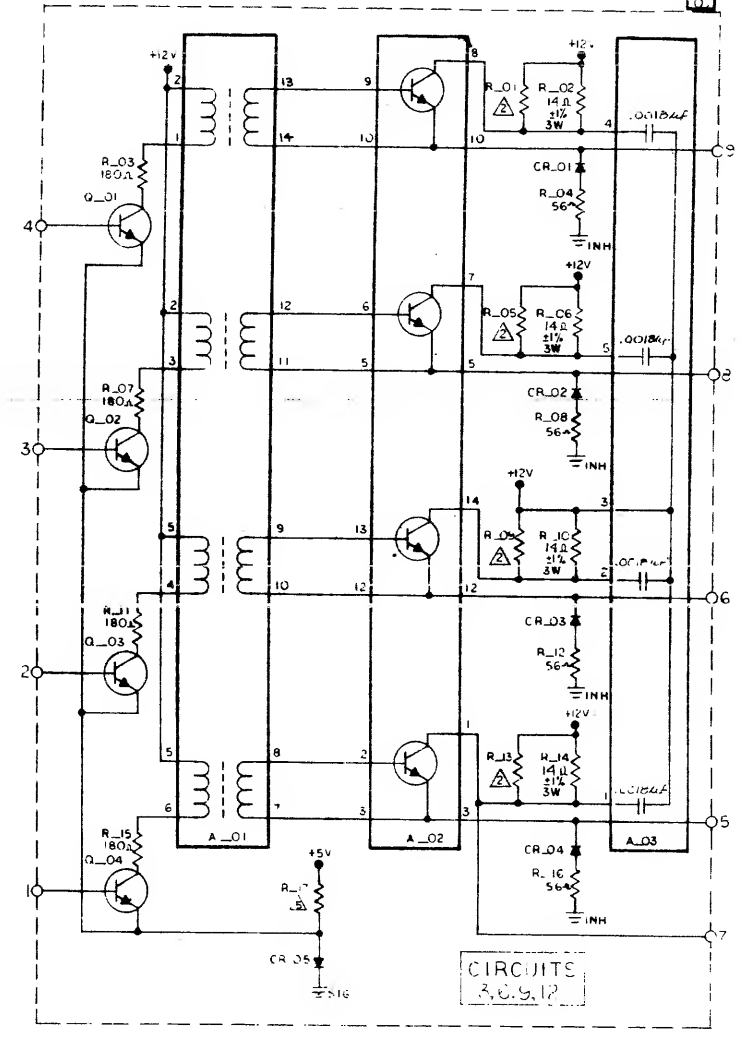


CURRENT SOURCES

CODE	IDENT	REL.	DATE	DATE	REV
21101	C		91C0346		C
SCALE					INCHES 10.0

91D0285
E

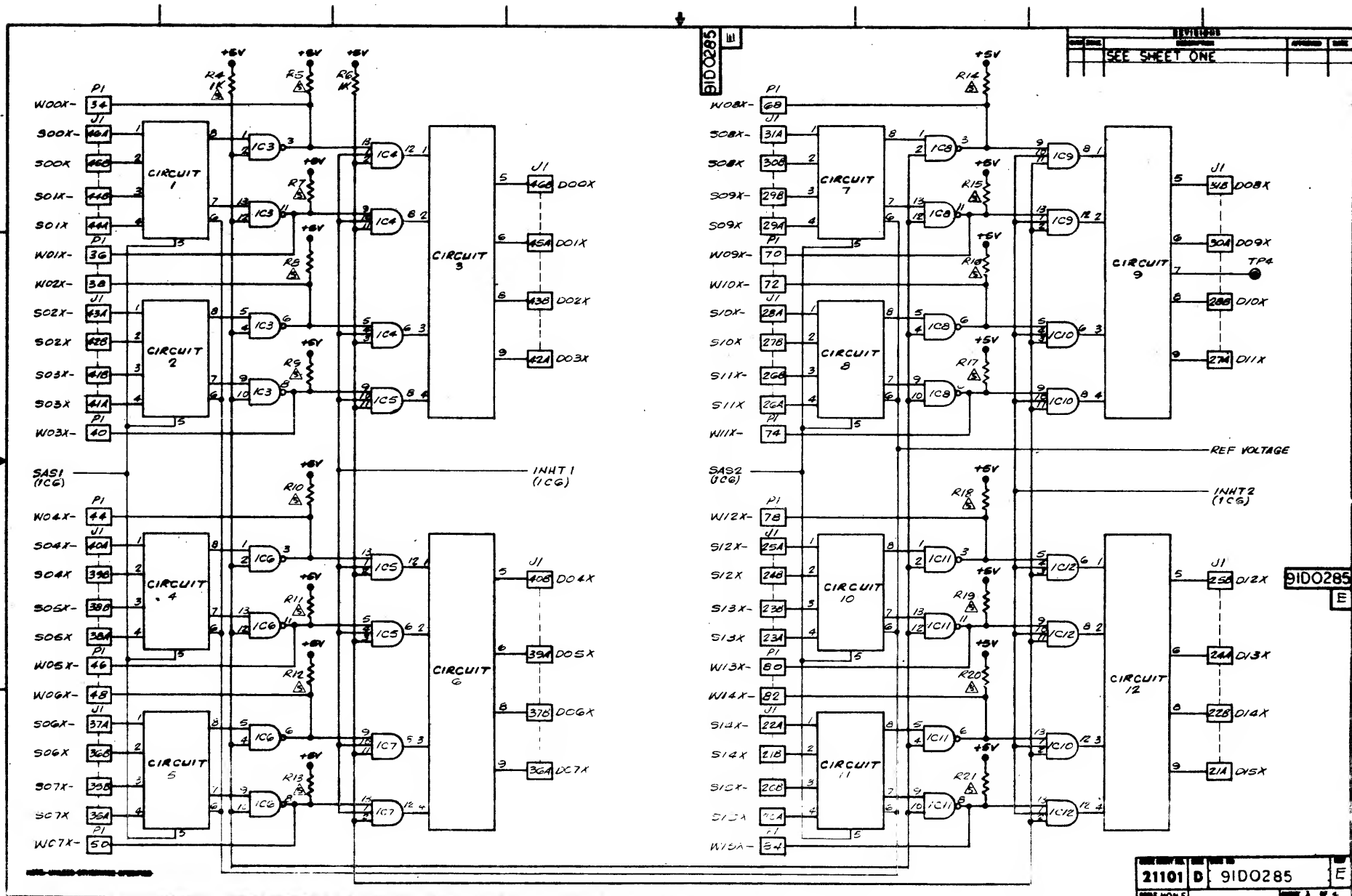
REVISIONS		APPROVED	DATE
SYM	DESC		
SEE SHEET ONE			



91D0285
E

NOTE: UNLESS OTHERWISE SPECIFIED

CODE	ISSUE NO.	REV	DATE
21101	D	91D0285	E
SCALE NONE		SHEET 2 OF 4	

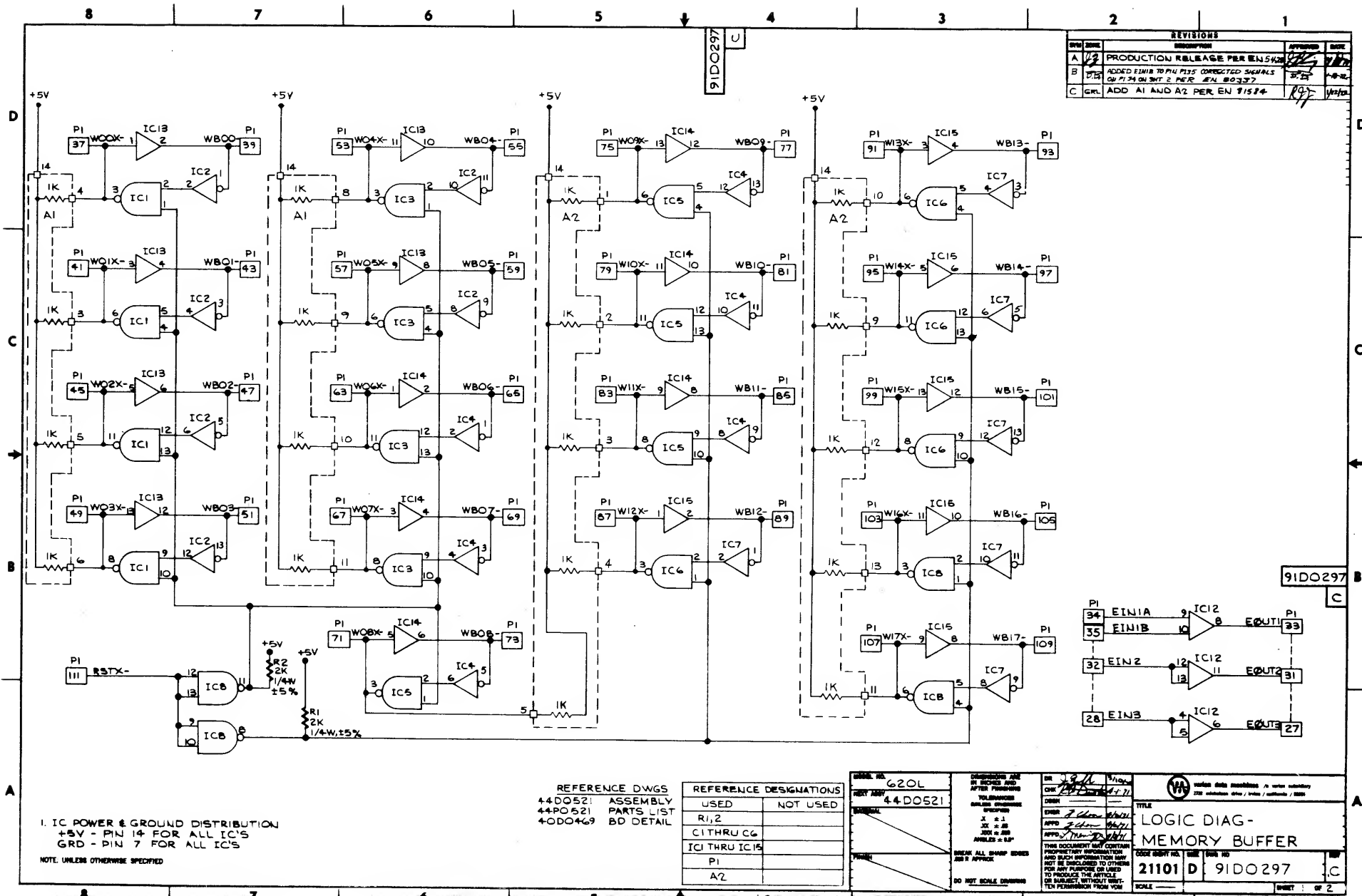


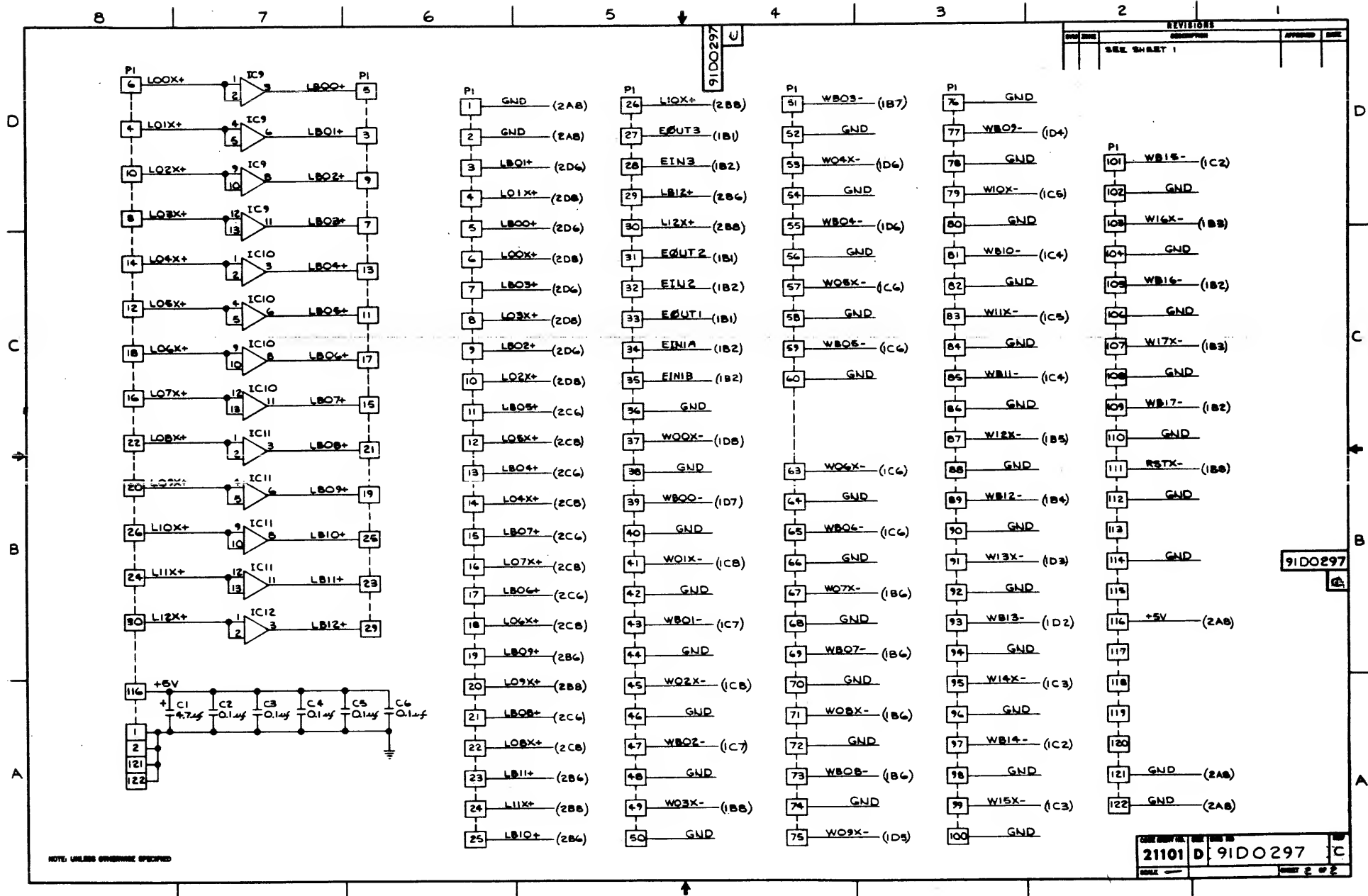
91D0285
E

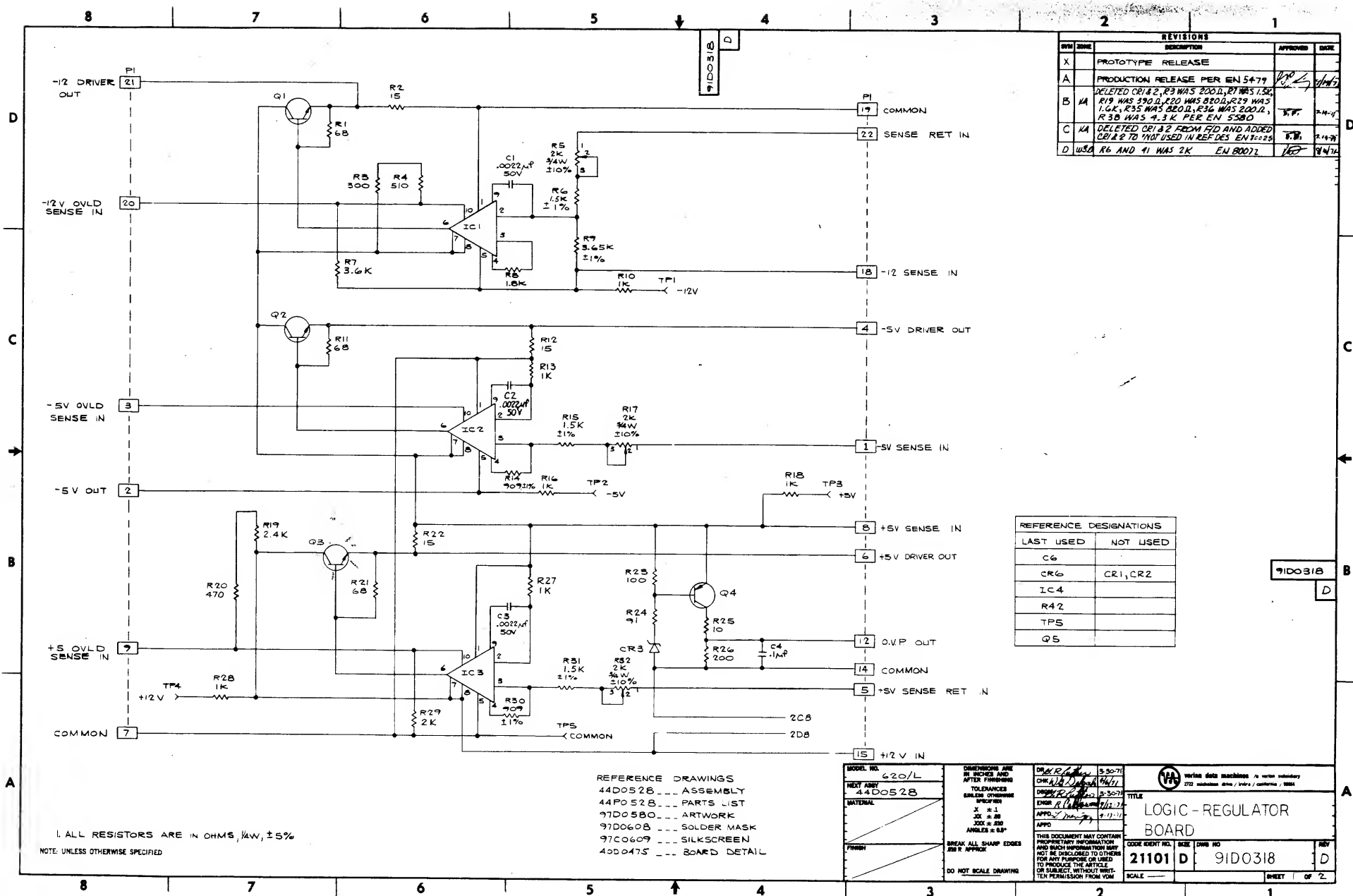
REVISIONS			
NO.	DATE	DESCRIPTION	APPROVED
		SEE SHEET ONE	

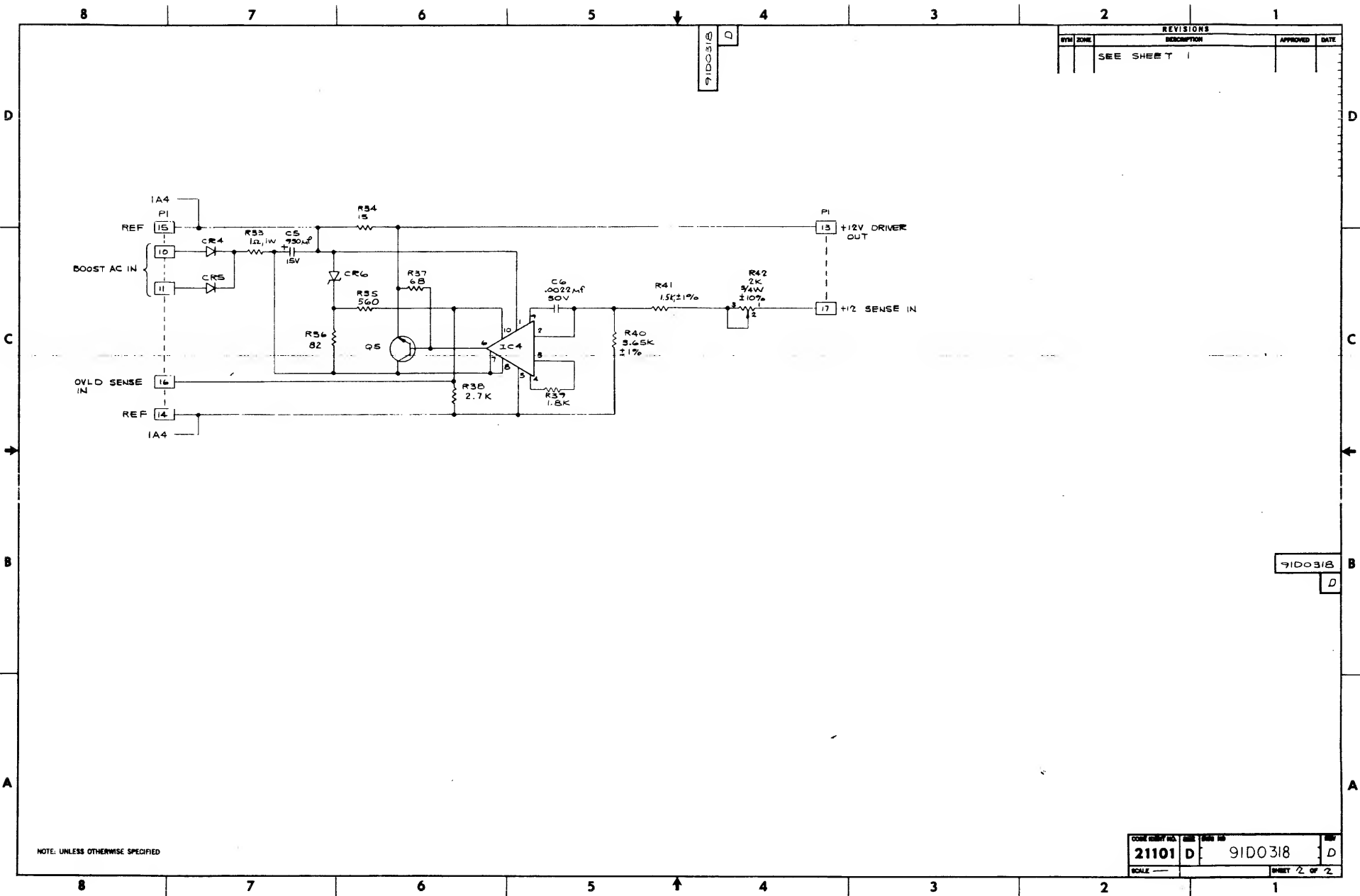
CONNECTOR PI			CONNECTOR PI			CONNECTOR PI			CONNECTOR J1			CONNECTOR J1			CONNECTOR J1			CONNECTOR J1		
PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET	PIN	FUNCTION	SHEET
1	G SIG		42	+5V		85	G SIG		1A	G SIG		20A	S15X	3	39A	005X	3	58A	CCGX-X	
2	G SIG		43	G SIG		86	SASX-		1B	G SIG		20B	S15X-	3	39B	S04X	3	58B	CCGX-X	
3	G SIG		44	W04X-	3	87	G SIG		2A	CA1X-X		21A	D15X	3	40A	S04X-	3	59A	CAGX-X	
4	G SIG		45	G SIG		88	TS4X		2B	CA1X-X		21B	S14X	3	40B	D04X	3	59B	CAGX-X	
5	G SIG		46	W05X-	3	89	G SIG		3A	CC1X-X		22A	S14X-	3	41A	S03X	3	60A	CCGX-X	
6	CA0X-X		47	G SIG		90	TS4X		3B	CC1X-X		22B	D14X	3	41B	S03X-	3	60B	CCGX-X	
7	CC0X-X		48	W06X-	3	91	G SIG		4A	CA3X-X		23A	S13X	3	42A	003X	3	61A	CA2X-X	
8	CA2X-X		49	G SIG		92	X50X-X		4B	CA3X-X		23B	S13X-	3	42B	S02X	3	61B	CA2X-X	
9	CC2X-X		50	W07X-	3	93	G SIG		5A	CC3X-X		24A	D13X	3	43A	S02X-	3	62A	CC2X-X	
10	CA4X-X		51	G SIG		94	X51X-X		5B	CC3X-X		24B	S12X	3	43B	002X	3	62B	CC2X-X	
11	CC4X-X		52	W10X-		95	G SIG		6A	CASX-X		25A	S12X-	3	44A	S01X		63A	CA0X-X	
12			53	G SIG		96	X52X-X		6B	CASX-X		25B	D12X	3	44B	S01X-		63B	CA0X-X	
13	CCGX-X		54	W17X-		97	G SIG		7A	CC5X-X		26A	S11X	3	45A	D01X	3	64A	CC0X-X	
14			55	G SIG		98	X53X-X		7B	CC5X-X		26B	S11X-	3	45B	S00X		64B	CC0X-X	
15	CAGX-X		56	INHT-	1	99	G SIG		8A	CA7X-X		27A	D11X	3	46A	S00X-		65A	G SIG	
16			57	G SIG		100	X54X-X		8B	CA7X-X		27B	S10X	3	46B	D00X	3	65B	G SIG	
17	G SIG		58	9SLX-	1	101	G SIG		9A	CC7X-X		28A	S10X-	3	47A	G SIG				
18	YS6X-X		59	G SIG		102	X55X-X		9B	CC7X-X		28B	D10X	3	47B	G SIG				
19	G SIG		60	9ASX-	1	103	G SIG		10A			29A	S09X	3	48A	G SIG				
20	YS7X-X		63	G SIG		104	X56X-X		10B			29B	S09X-	3	48B	G SIG				
21	G SIG		64	INHE-X	1	105	CA7X-X		11A			30A	D09X	3	49A	G SIG				
22	YS4X-X		65	G SIG		106	X57X-X		11B			30B	S08X	3	49B	G SIG				
23	G SIG		66	G SIG		107	CASX-X		12A			31A	S08X-	3	50A	YS1X-X				
24	YS5X-X		67	G SIG		108			12B			31B	D08X	3	50B	YS0X-X				
25	G SIG		68	W08X-	3	109	CA3X-X		13A	X5GX-X		32A	S17X		51A	YS3X-X				
26	YS2X-X		69	G SIG		110	CC7X-X		13B	X57X-X		32B	S17X-		51B	YS2X-X				
27	G SIG		70	W09X-	3	111	CC1X-X		14A	X54X-X		33A	D17X		52A	YS5X-X				
28	YS3X-X		71	G SIG		112	CC5X-X		14B	X55X-X		33B	S16X		52B	YS4X-X				
29	G SIG		72	W10X-	3	113	CA1X-X		15A	X52X-X		34A	S16X-		53A	YS7X-X				
30	YS0X-X		73	G SIG		114	CC3X-X		15B	X53X-X		34B	D16X		53B	YS6X-X				
31	G SIG		74	W11X-	3	115	-5V		16A	X50X-X		35A	S07X	3	54A					
32	YS1X-X		75	G SIG		116	+5V		16B	YS1X-X		35B	S07X-	3	54B					
33	G SIG		76	G SIG		117	+12V		17A	TS4X		36A	D07X	3	55A					
34	W00X-	3	77	G SIG		118	+12V		17B	TS4X		36B	S06X	3	55B					
35	G SIG		78	W12X-	3	119	+12V		18A	G SIG		37A	S06X-	3	56A					
36	W01X-	3	79	G SIG		120	+12V		18B	G SIG		37B	D06X	3	56B					
37	G SIG		80	W13X-	3	121	G SIG		19A	TS4X		38A	S05X	3	57A	CAGX-X				
38	W02X-	3	81	G SIG		122	G SIG		19B	TS4X		38B	S05X-	3	57B	CAGX-X				
39	G SIG		82	W14X-	3															
40	W03X-	3	83	G SIG																
41	G SIG		84	W15X-	3															

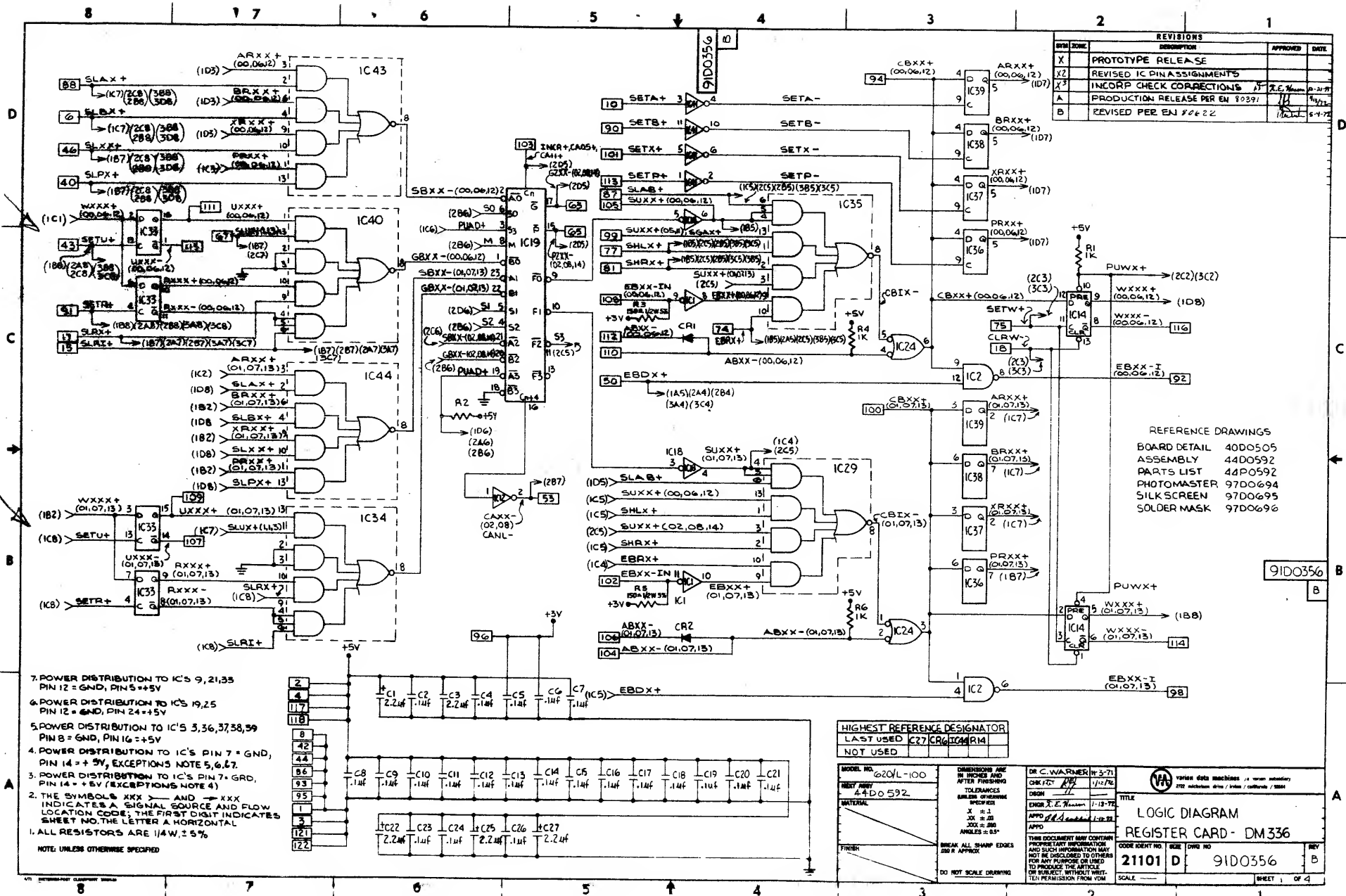
NOTE: UNLESS OTHERWISE SPECIFIED









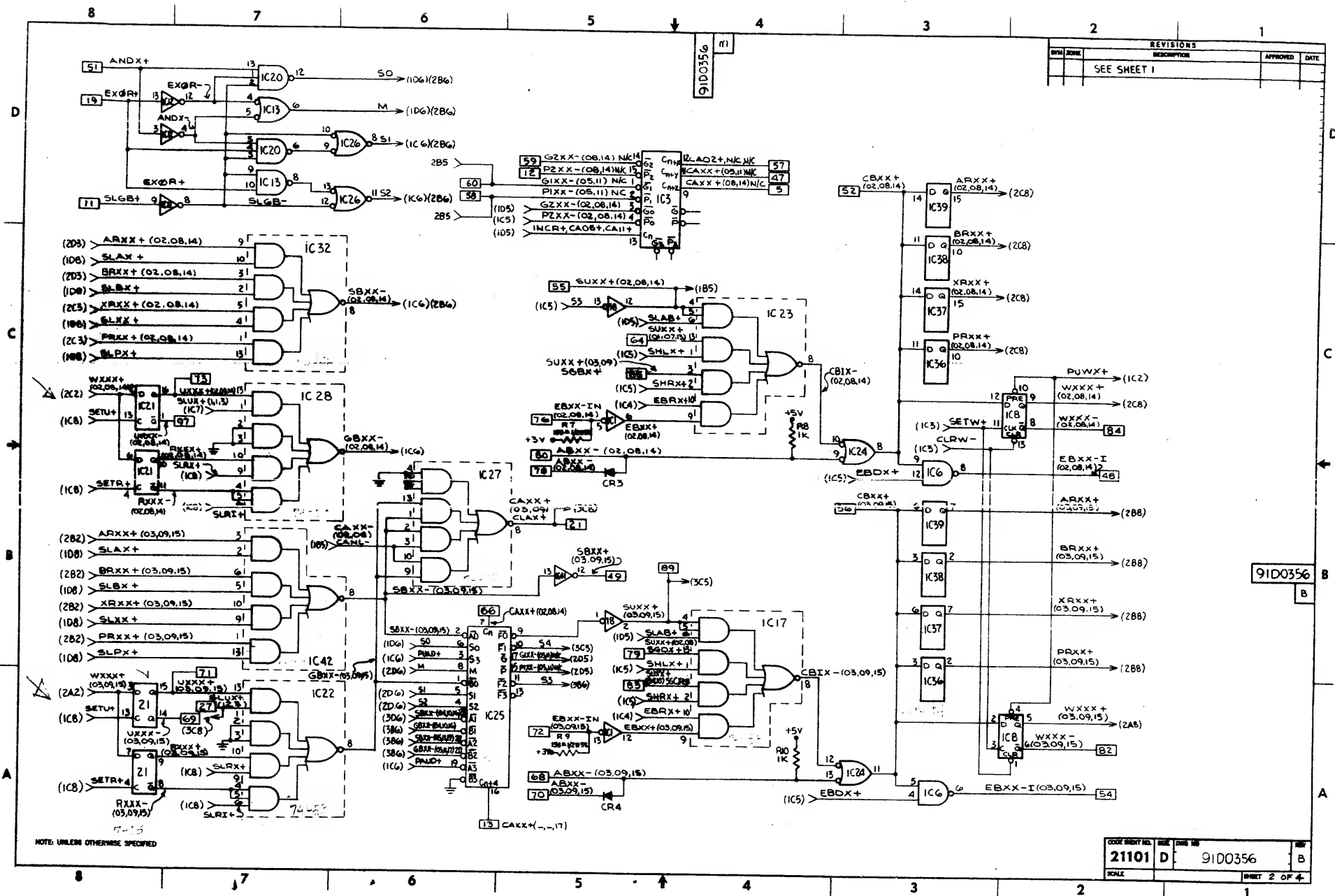


REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
X		PROTOTYPE RELEASE	
X2		REVISED IC PIN ASSIGNMENTS	
A		INCORP CHECK CORRECTIONS	
B		PRODUCTION RELEASE PER EN 10391	
B		REVISED PER EN 10422	

REFERENCE DRAWINGS
BOARD DETAIL 40D0505
ASSEMBLY 44D0592
PARTS LIST 44D0592
PHOTOMASTER 97D0694
SILK SCREEN 97D0695
SOLDER MASK 97D0696

HIGHEST REFERENCE DESIGNATOR
LAST USED C27 C104 C104 R14
NOT USED

MODEL NO. 620/L-100	DATE 4-4-60	DESIGNER J.E. HARRIS	DATE 1-13-72	TITLE LOGIC DIAGRAM REGISTER CARD - DM 336
MATERIAL	FINISH	DO NOT SCALE DRAWING	DO NOT SCALE DRAWING	DO NOT SCALE DRAWING
21101 D	91D0356			



8

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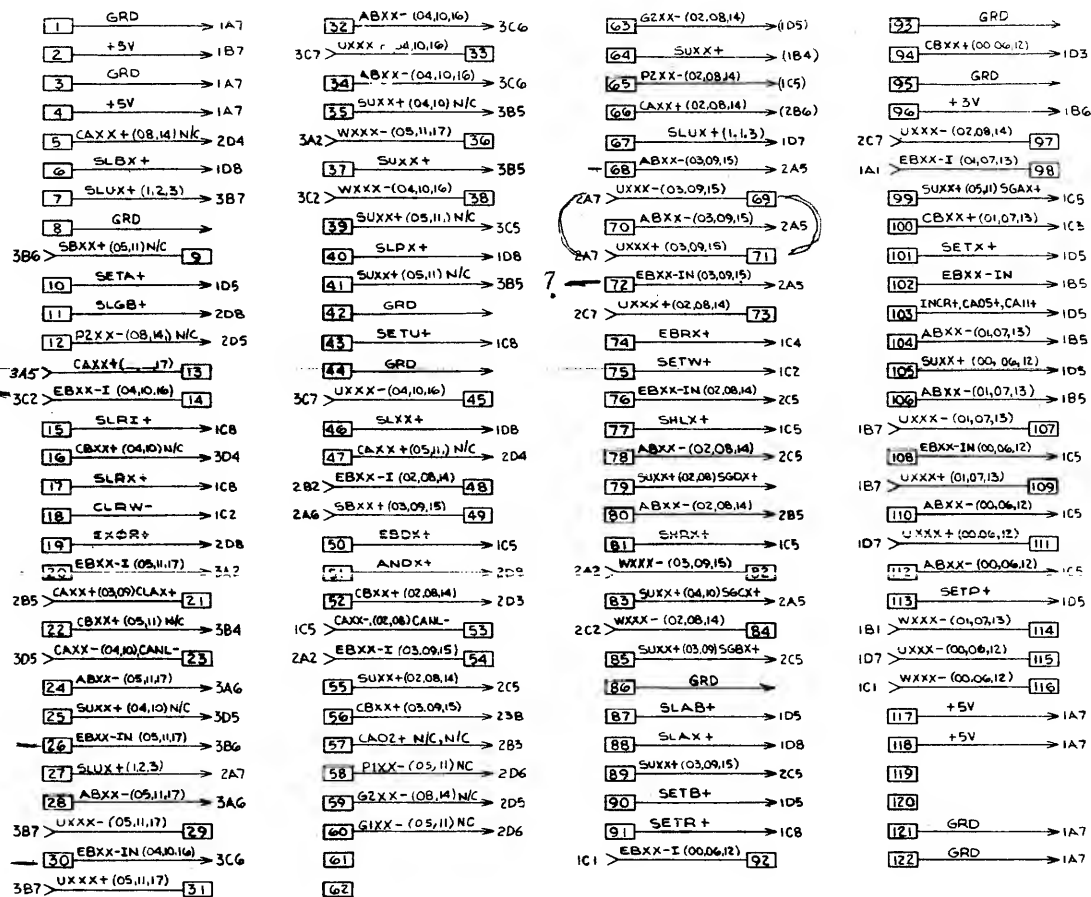
3

2

1

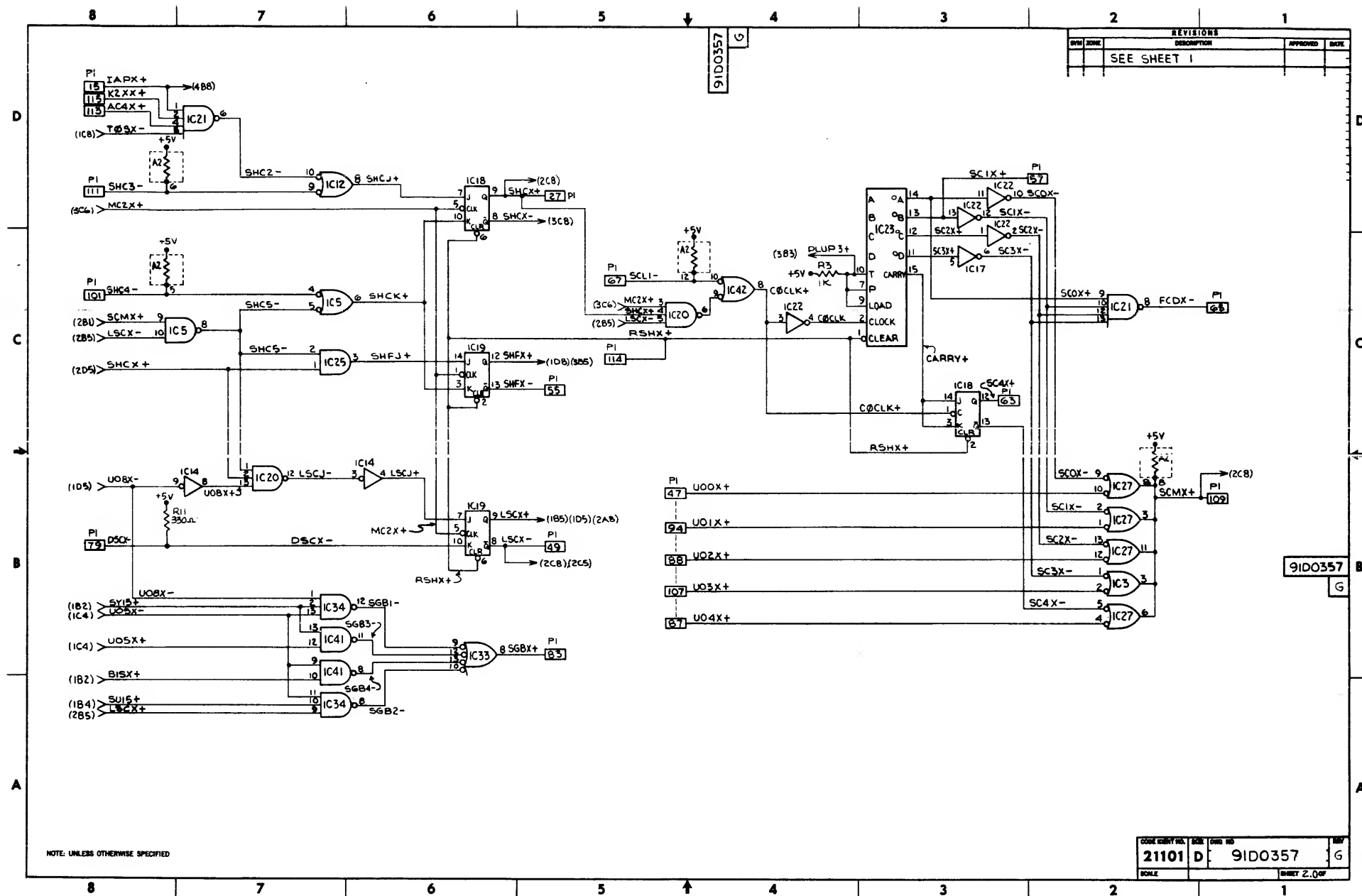
91D0356

REVISIONS			
REV	ZONE	DESCRIPTION	DATE
SEE SHEET 1			

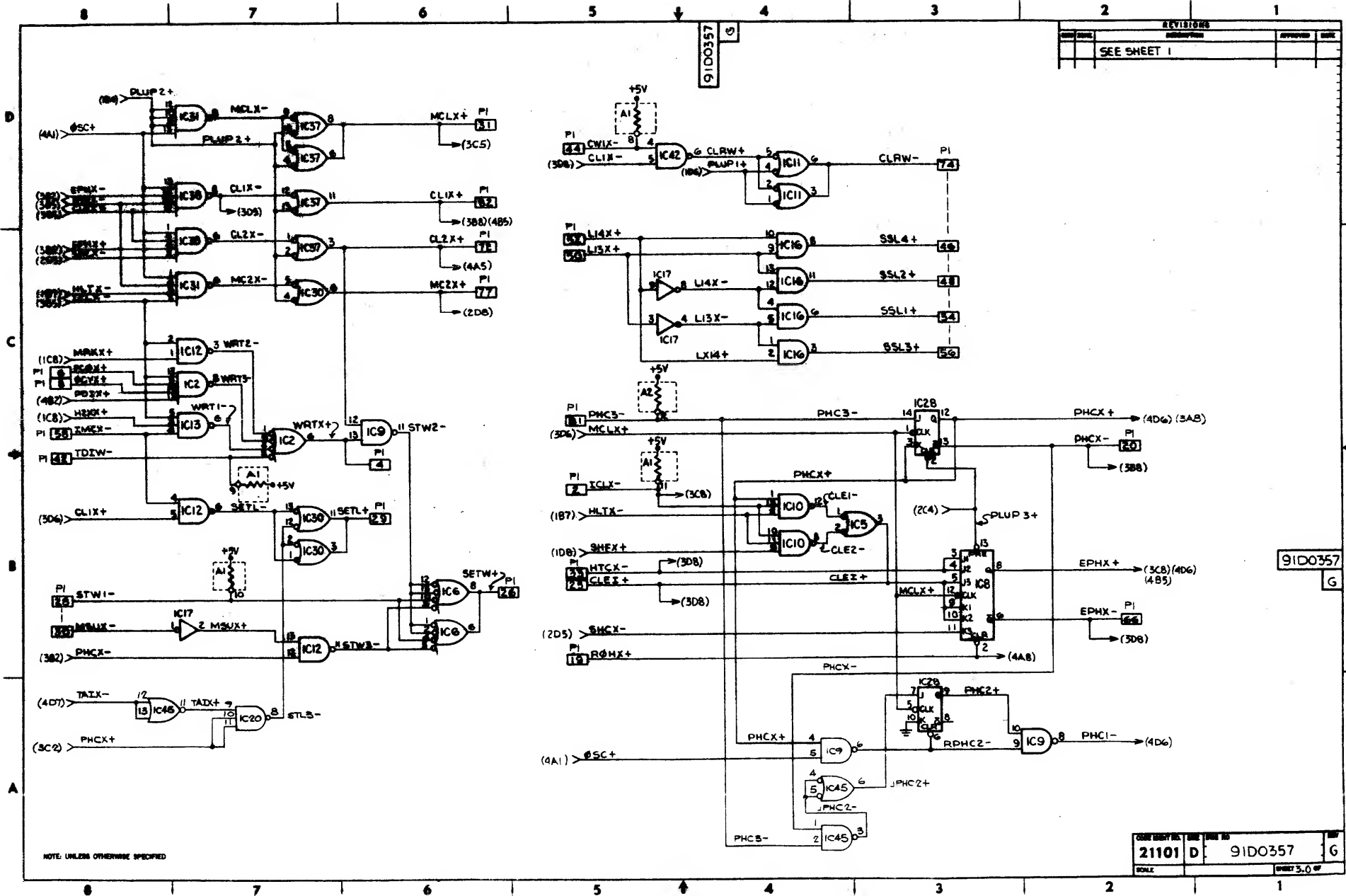


NOTE: UNLESS OTHERWISE SPECIFIED

CODE IDENT NO.	REV	DATE	BY
21101	D	91D0356	B
SCALE	SHEET 4 OF 4		



REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
1		SEE SHEET 1	

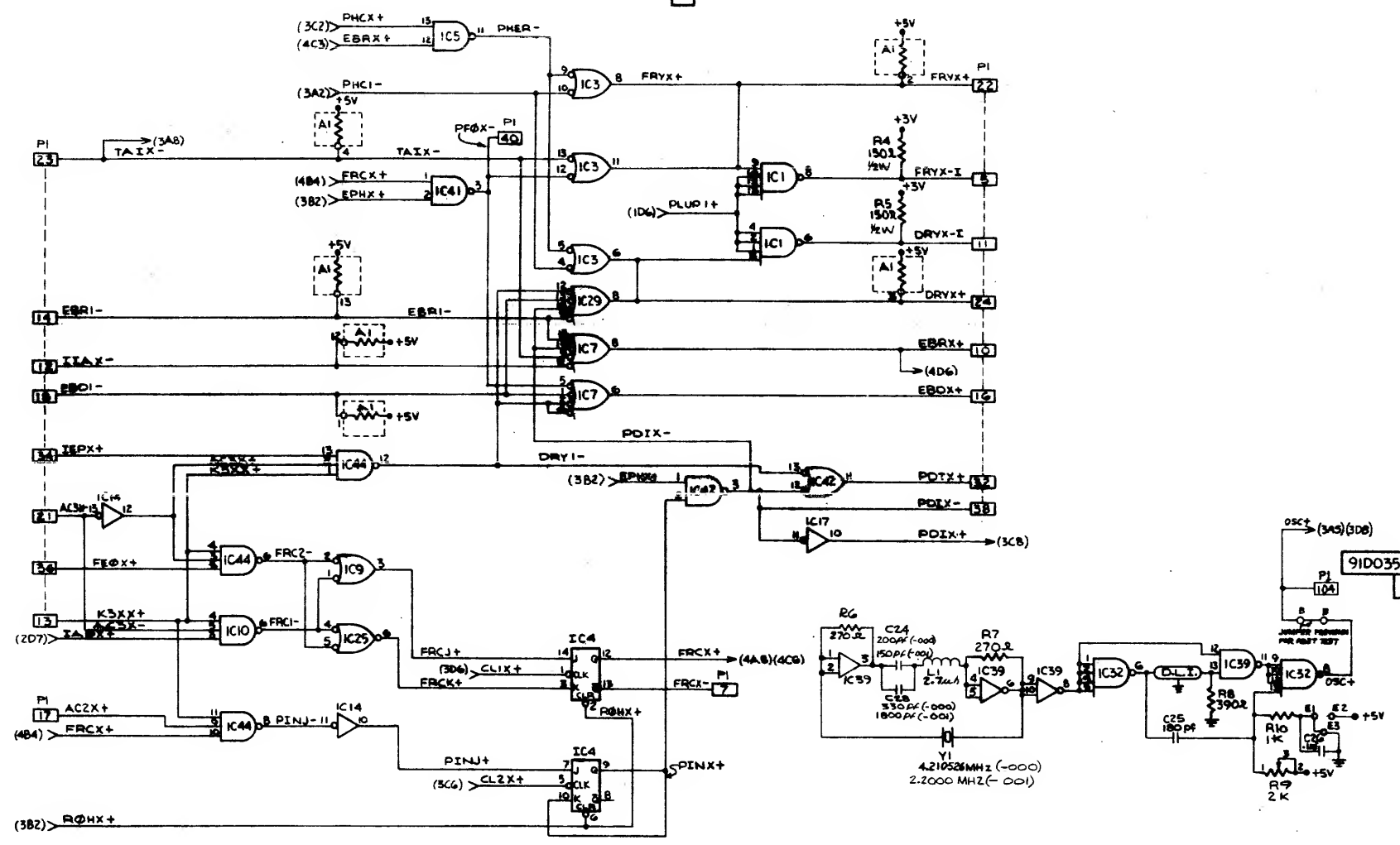


NOTE: UNLESS OTHERWISE SPECIFIED

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SCALE			SHEET 3.0 OF

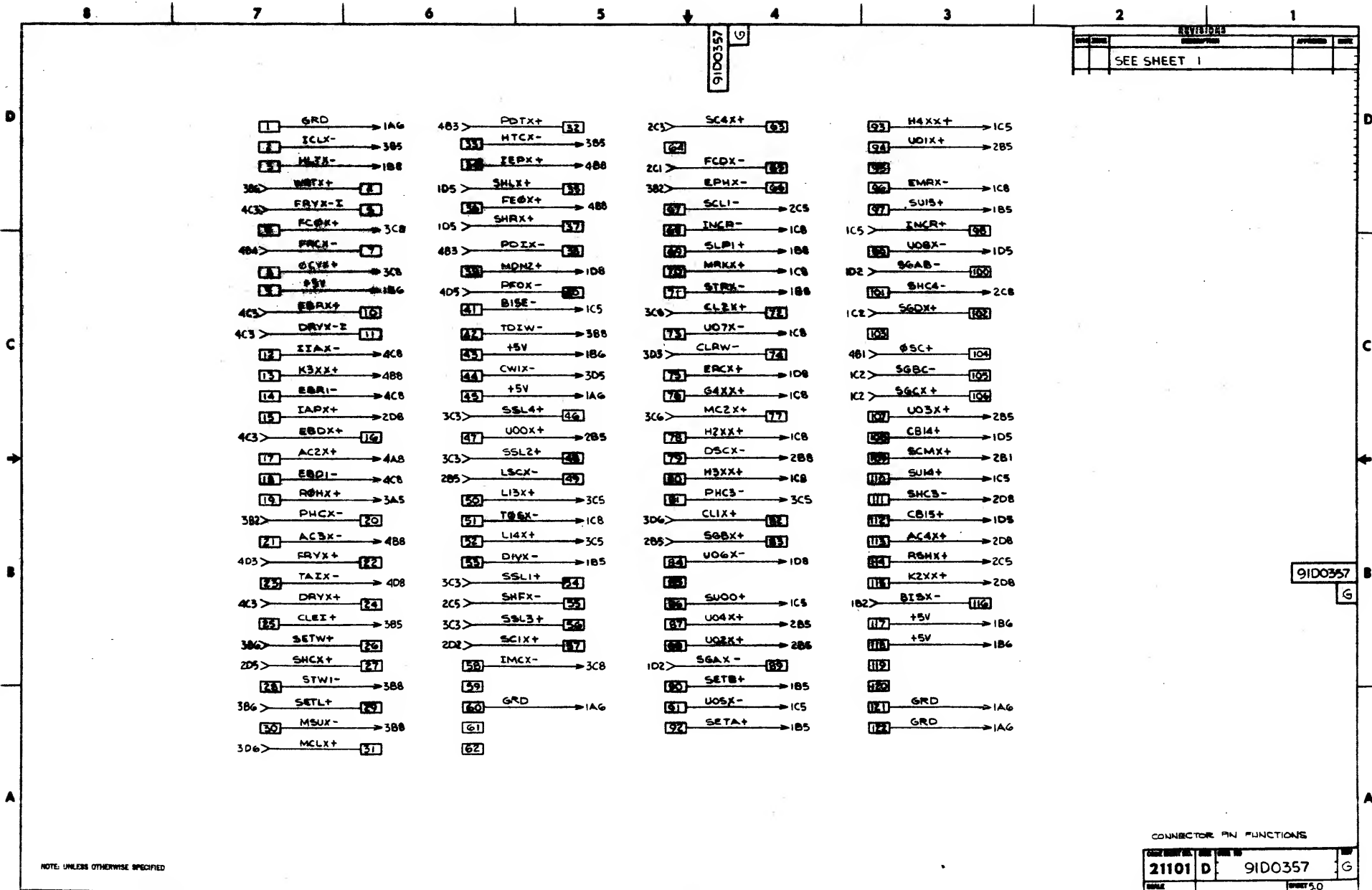
REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
1		SEE SHEET 1	

91D0357



NOTE: UNLESS OTHERWISE SPECIFIED

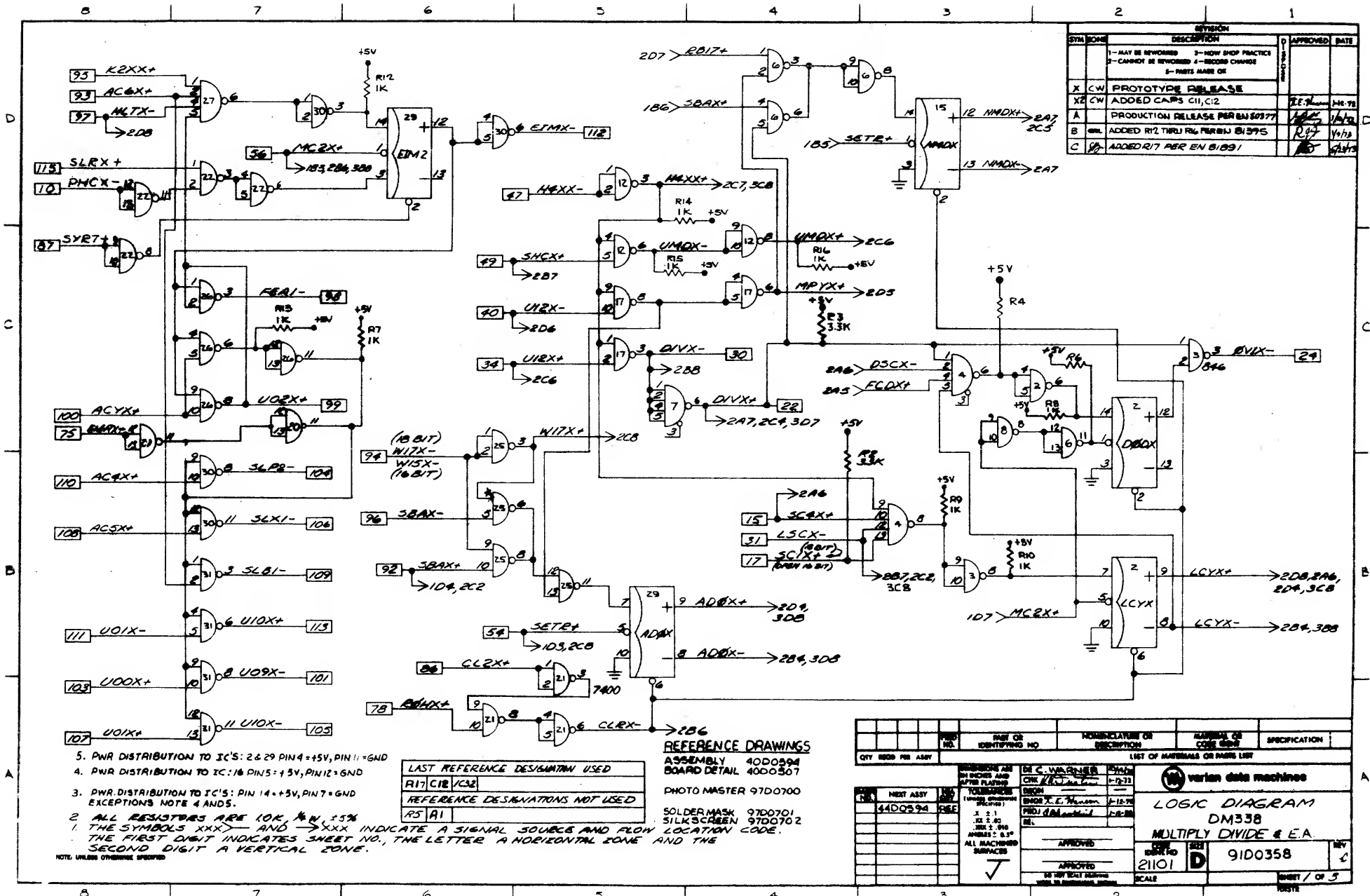
21101	D	91D0357	G
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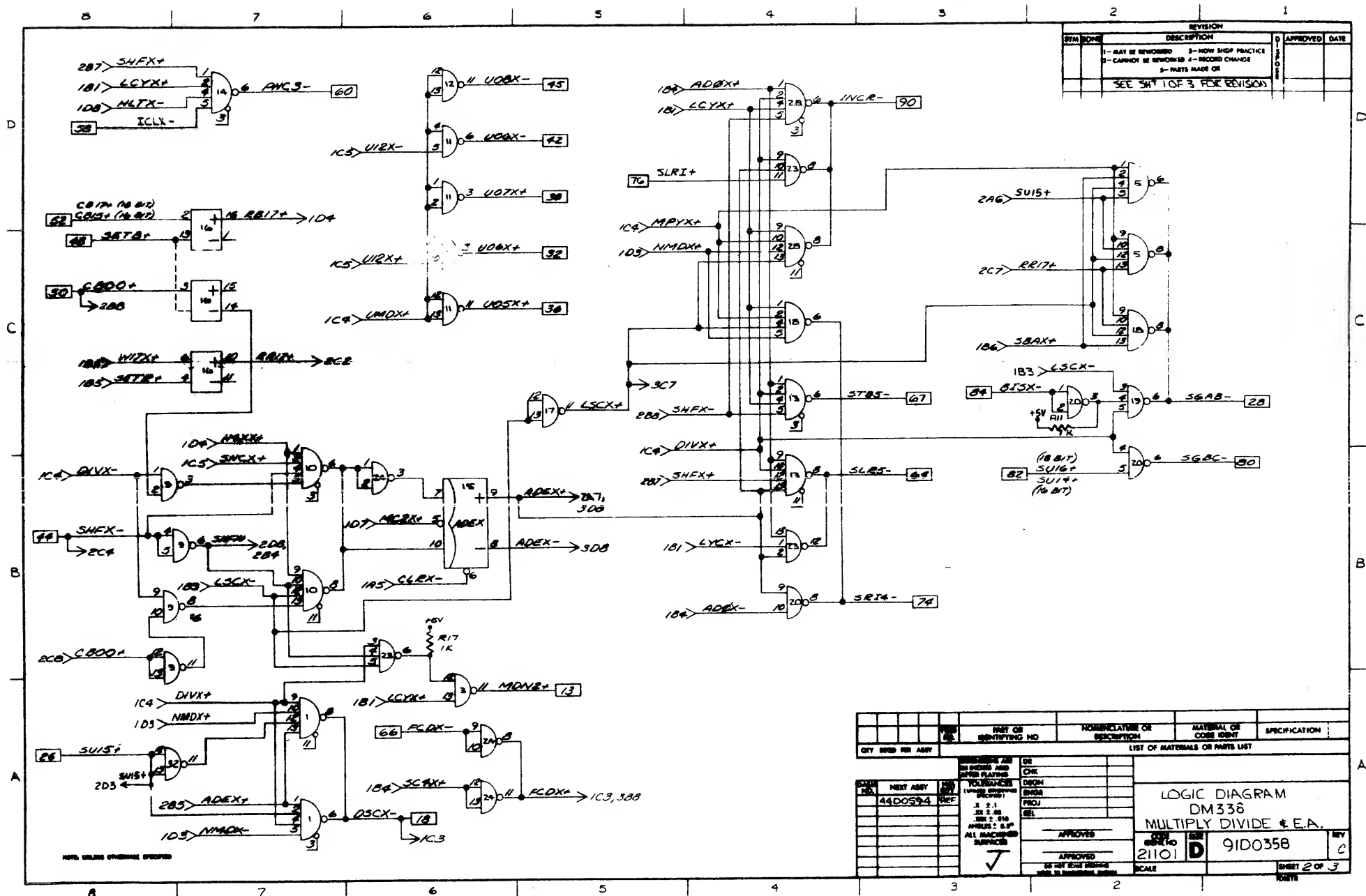


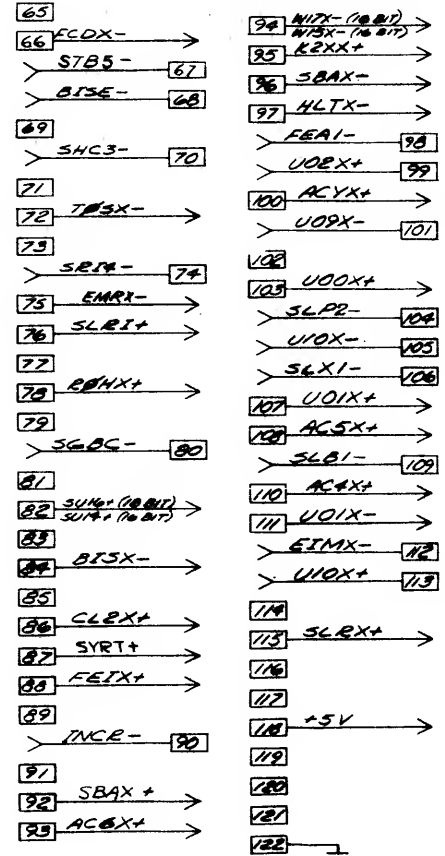
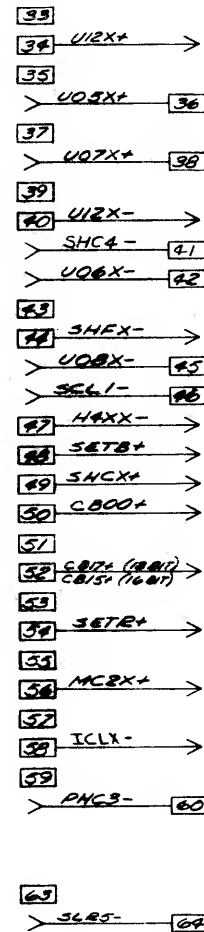
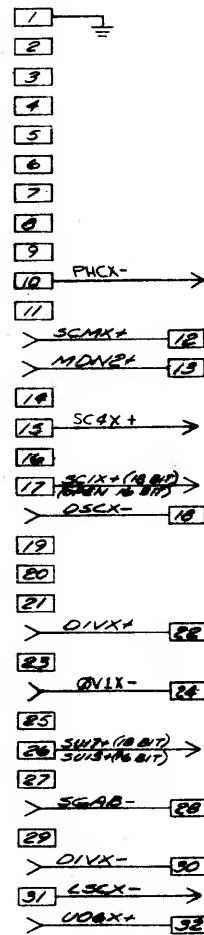
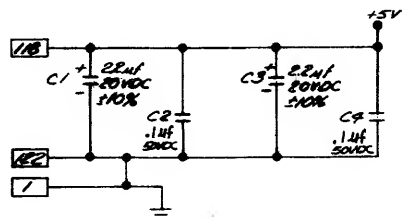
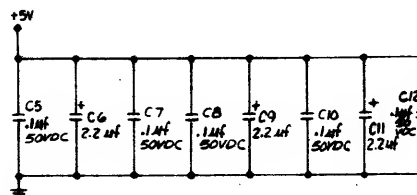
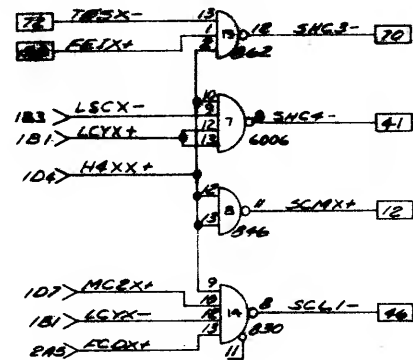
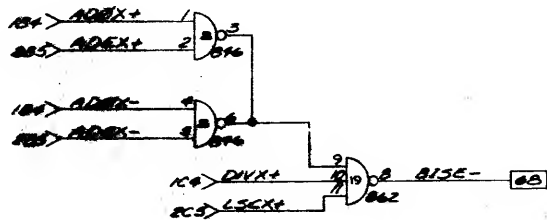
NOTE: UNLESS OTHERWISE SPECIFIED

CONNECTOR PIN FUNCTIONS

21101	D	91D0357	G
SCALE		SHEET 5.0	



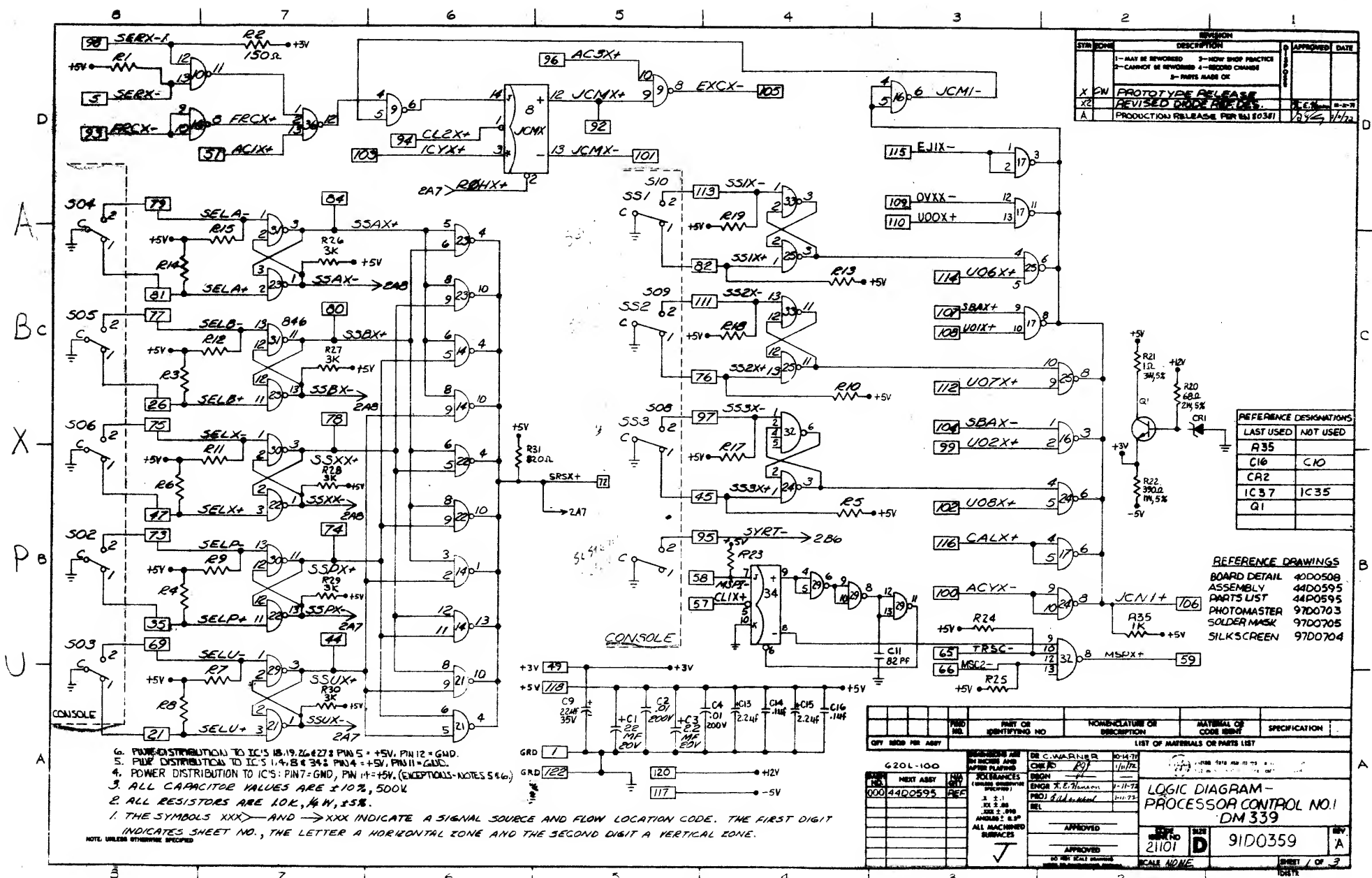




SYMBOL	DESCRIPTION	APPROVED	DATE
1	1-MAY BE REWORKED		
2	2-NOW SHOP PRACTICE		
3	3-CANNOT BE REWORKED		
4	4-REWORK CHANGE		
5	5-REWORK MADE OK		
6	6-SEE SHIT 10F3 FOR REVISION		

PART NO.		REVISION NO.		NON-REPLACEMENT OR DESCRIPTION		MATERIAL OR CODE IDENT		SPECIFICATION	
QTY. REQD. FOR ASSY		LIST OF MATERIALS OR PARTS LIST							
NEXT ASSY		PART NO.		REVISION NO.		MATERIAL OR CODE IDENT		SPECIFICATION	
44D0594		21101		D		91D0358		1.0	
APPROVED		APPROVED		APPROVED		APPROVED		APPROVED	
DO NOT SIGN OFF		DO NOT SIGN OFF		DO NOT SIGN OFF		DO NOT SIGN OFF		DO NOT SIGN OFF	

NOTE: UNLESS OTHERWISE SPECIFIED



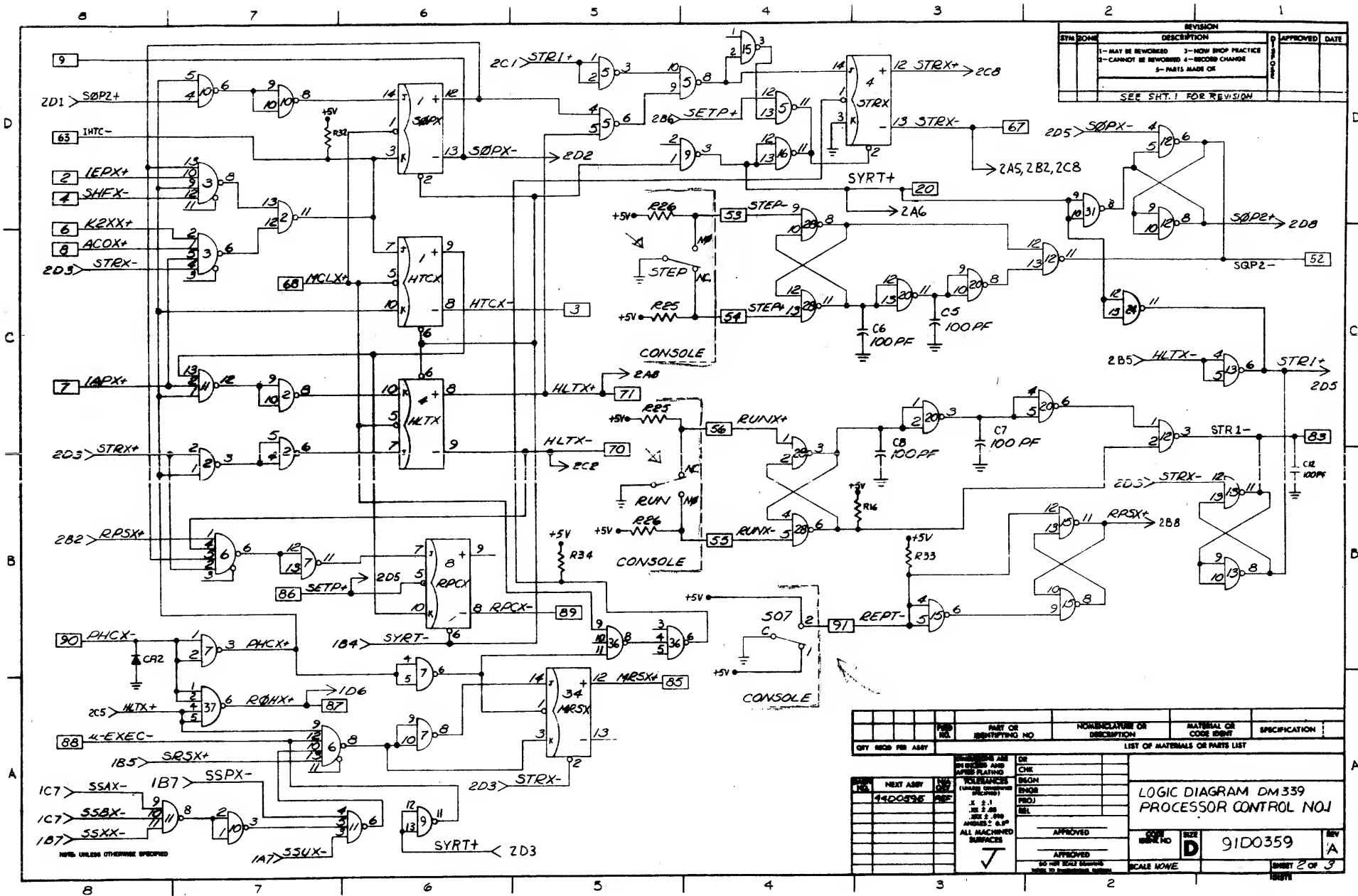
SYMBOL		DESCRIPTION	APPROVED	DATE
1	MAY BE REWORKED	3-HOW SHOP PRACTICE		
2	CANNOT BE REWORKED	4-RECORD CHANGE		
3	PARTS MADE OK			
X	DM	PROTOTYPE RELEASE		
X2		REVISED DRAWING		
A		PRODUCTION RELEASE PER RAJ 00347		

REFERENCE DESIGNATIONS	
LAST USED	NOT USED
A35	
C10	C10
CA2	
IC37	IC35
Q1	

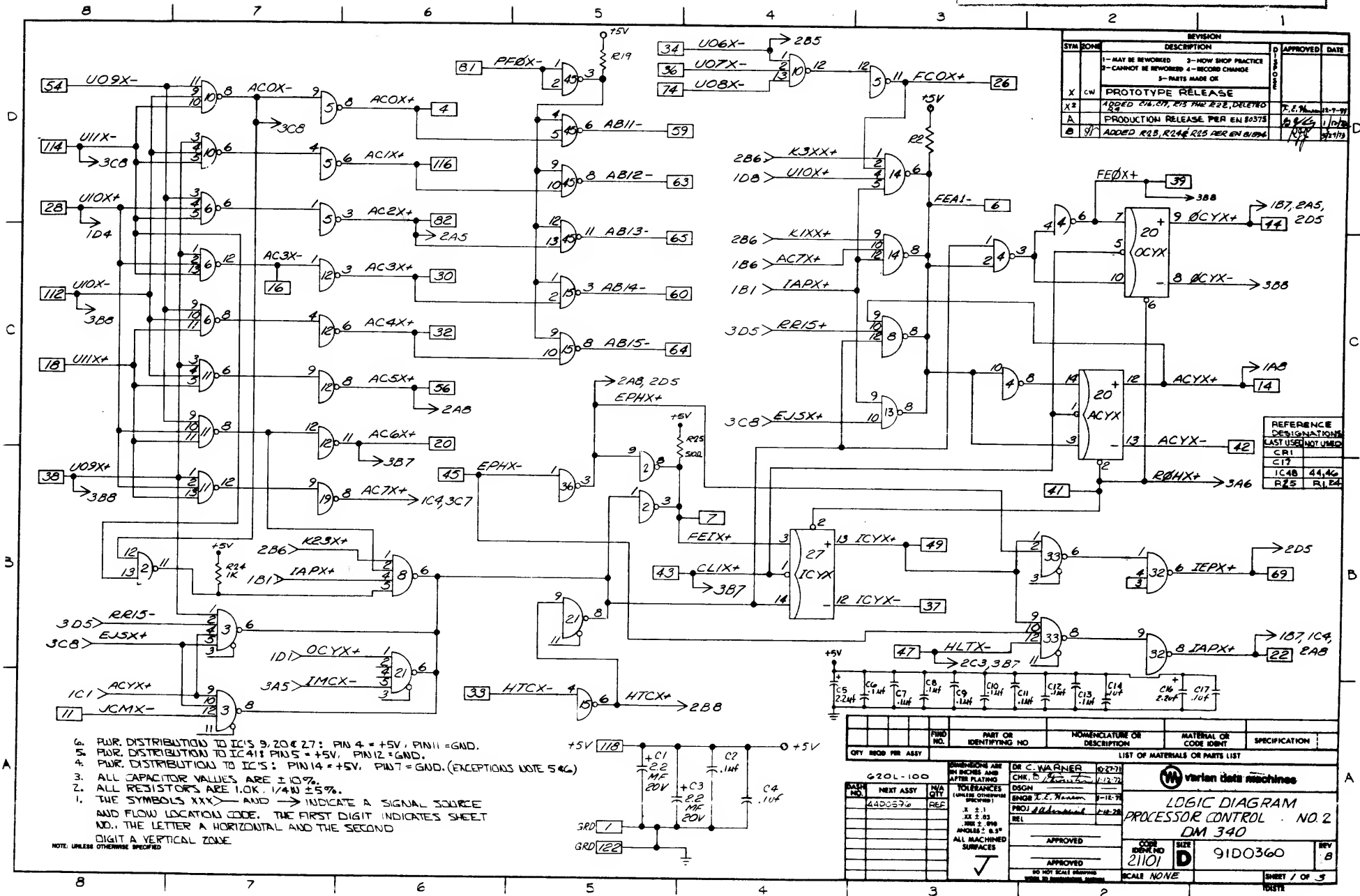
REFERENCE DRAWINGS	
BOARD DETAIL	44D0508
ASSEMBLY	44D0595
PARTS LIST	44D0595
PHOTOMASTER	9700703
SOLDER MASK	9700705
SILKSCREEN	9700704

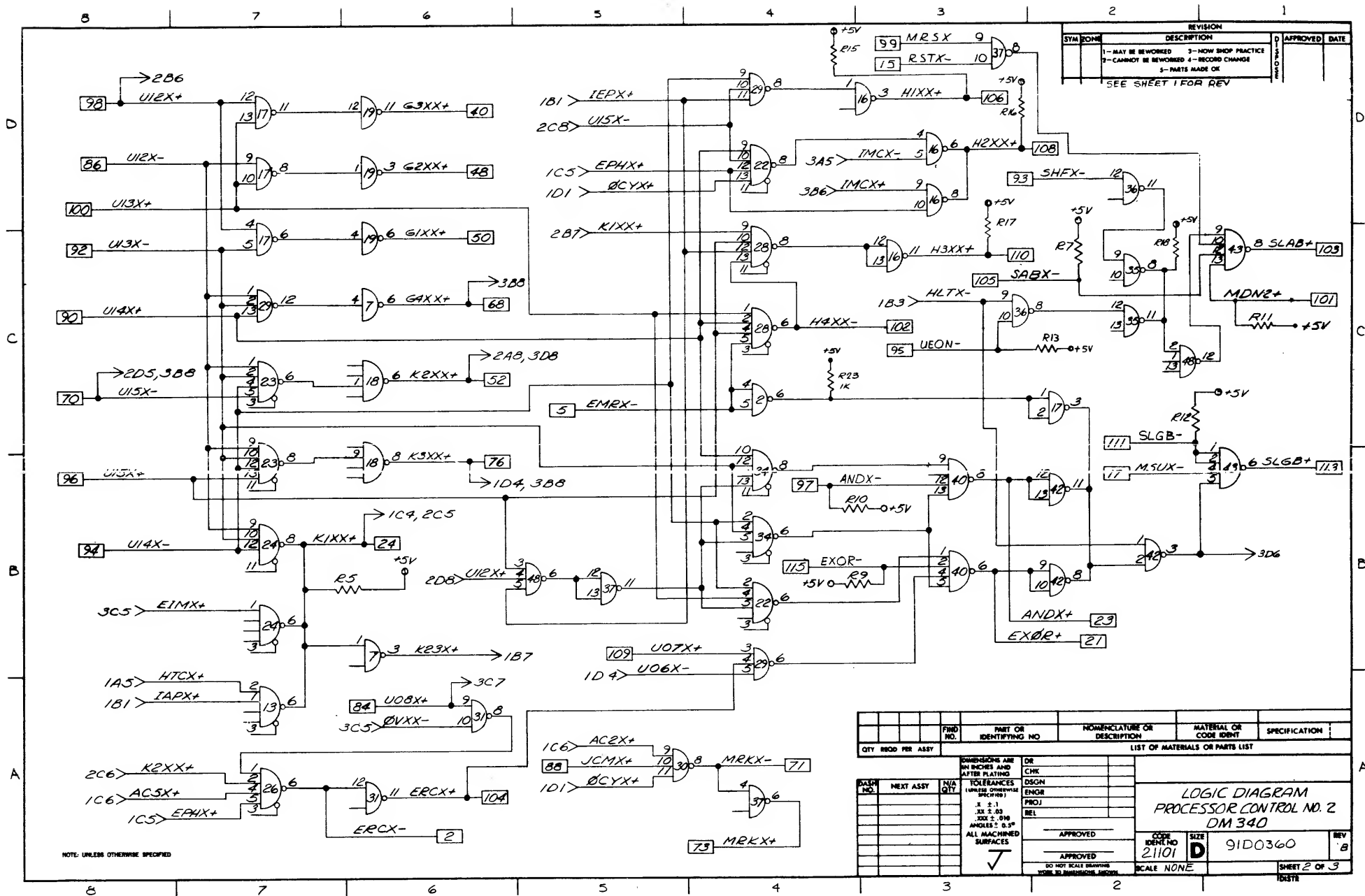
QTY	MOD	PER	ASBY	PART OR IDENTIFYING NO	NOMENCLATURE OR DESCRIPTION	ANALYST OR CODE NAME	SPECIFICATION
LIST OF MATERIALS OR PARTS LIST							
G20L-100							
NO.	NEXT ASBY	QTY	MOD	PER	DESIGN	DATE	
000	44D0595	REC			ENGR R.E. HARRIS	11-17-72	
					PROJ 811111111	11-17-72	
					REL		
					APPROVED		
					APPROVED		
					DO NOT SCALE DRAWING		
					SCALE NONE		
					SHEET 1 OF 3		

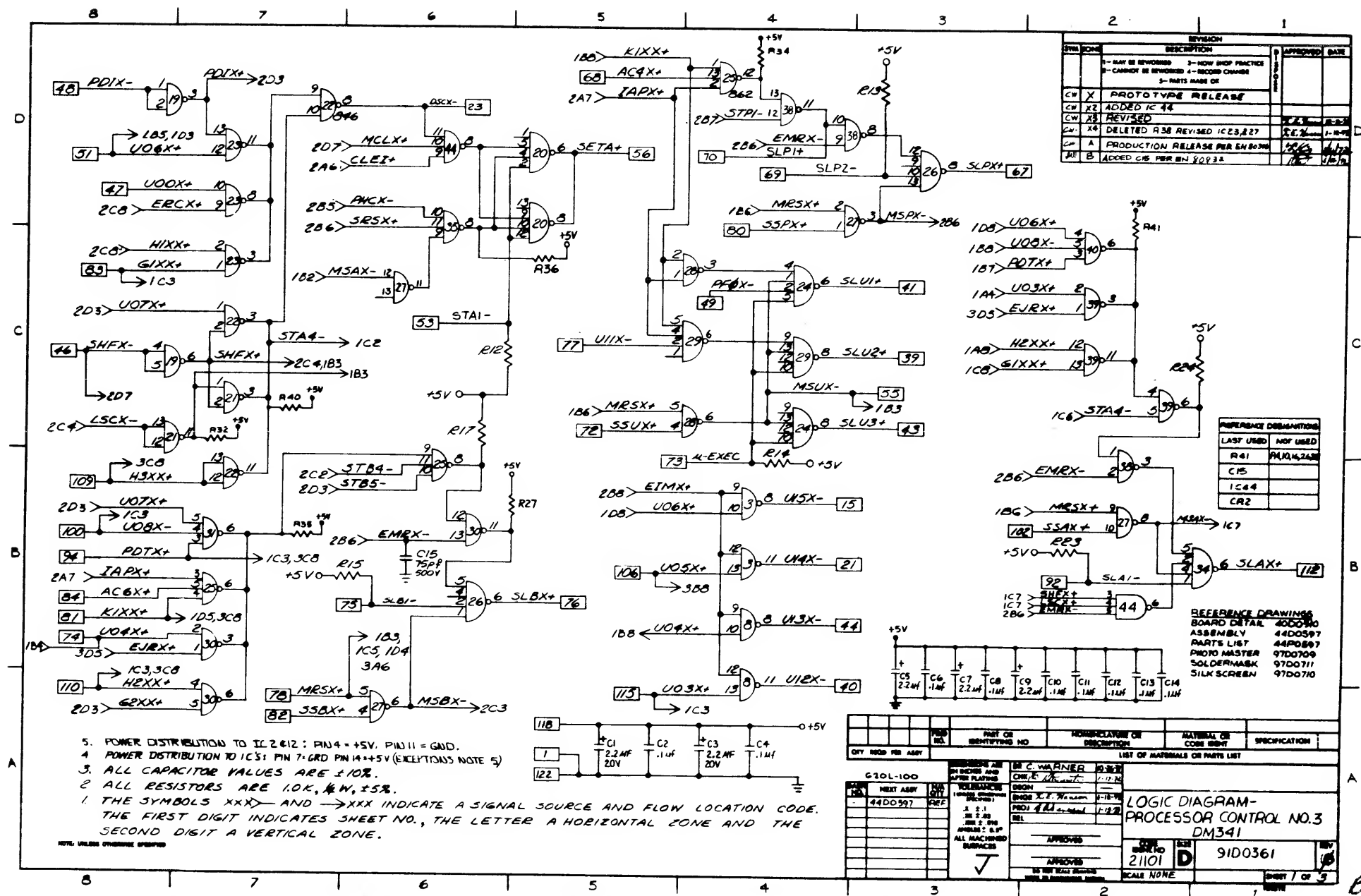
6. POWER DISTRIBUTION TO IC'S 18, 19, 26, 27 & PIN 5 = +5V, PIN 12 = GND.
5. POWER DISTRIBUTION TO IC'S 1, 4, 8 & 34 & PIN 4 = +5V, PIN 11 = GND.
4. POWER DISTRIBUTION TO IC'S: PIN 7 = GND, PIN 14 = +5V, (EXCEPTIONS - NOTES 5 & 6).
3. ALL CAPACITOR VALUES ARE $\pm 10\%$, 500V.
2. ALL RESISTORS ARE 10K, $\frac{1}{4}W$, 15%.
1. THE SYMBOLS XXX AND XXX INDICATE A SIGNAL SOURCE AND FLOW LOCATION CODE. THE FIRST DIGIT INDICATES SHEET NO., THE LETTER A HORIZONTAL ZONE AND THE SECOND DIGIT A VERTICAL ZONE.
NOTE: UNLESS OTHERWISE SPECIFIED

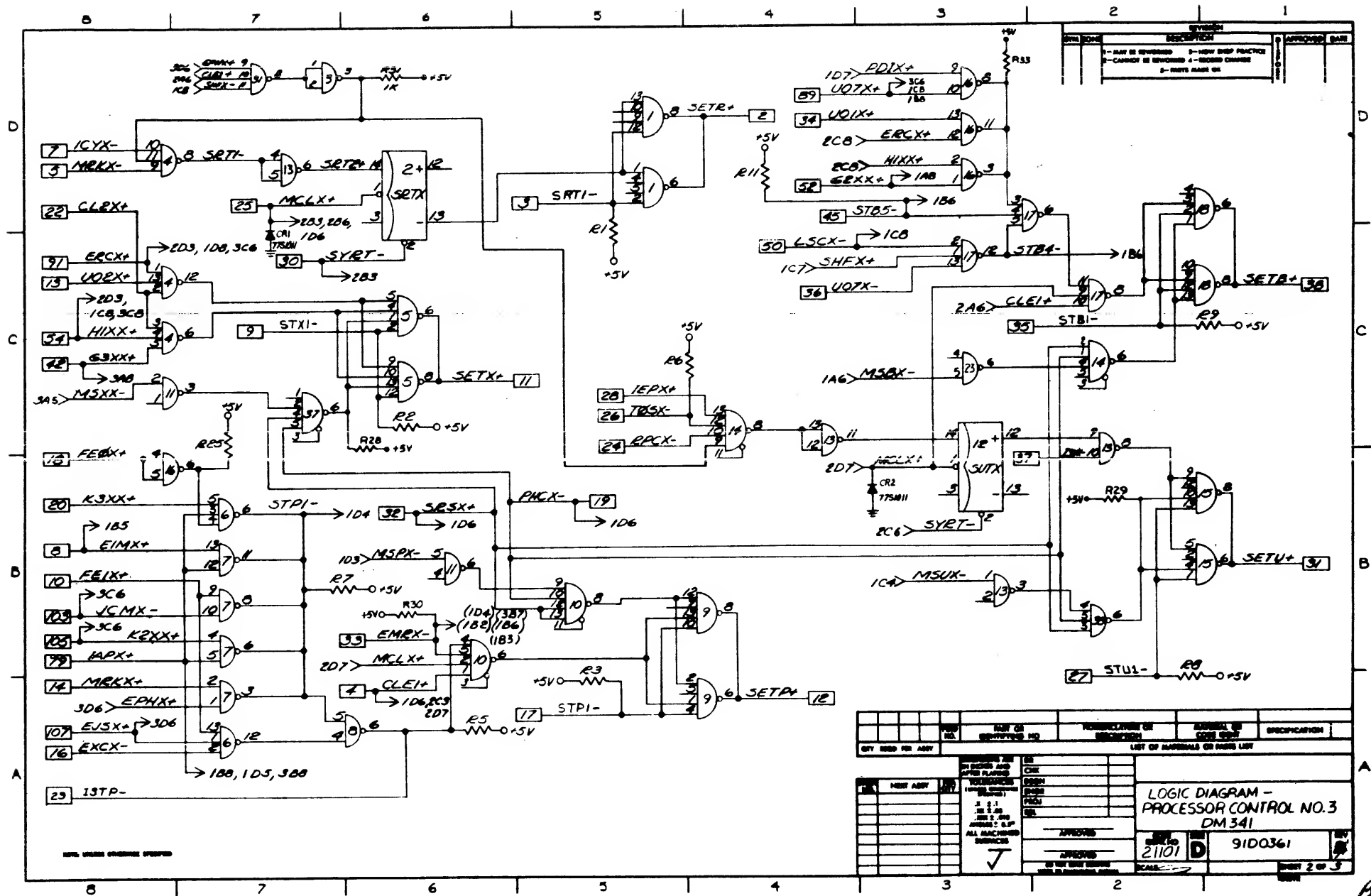


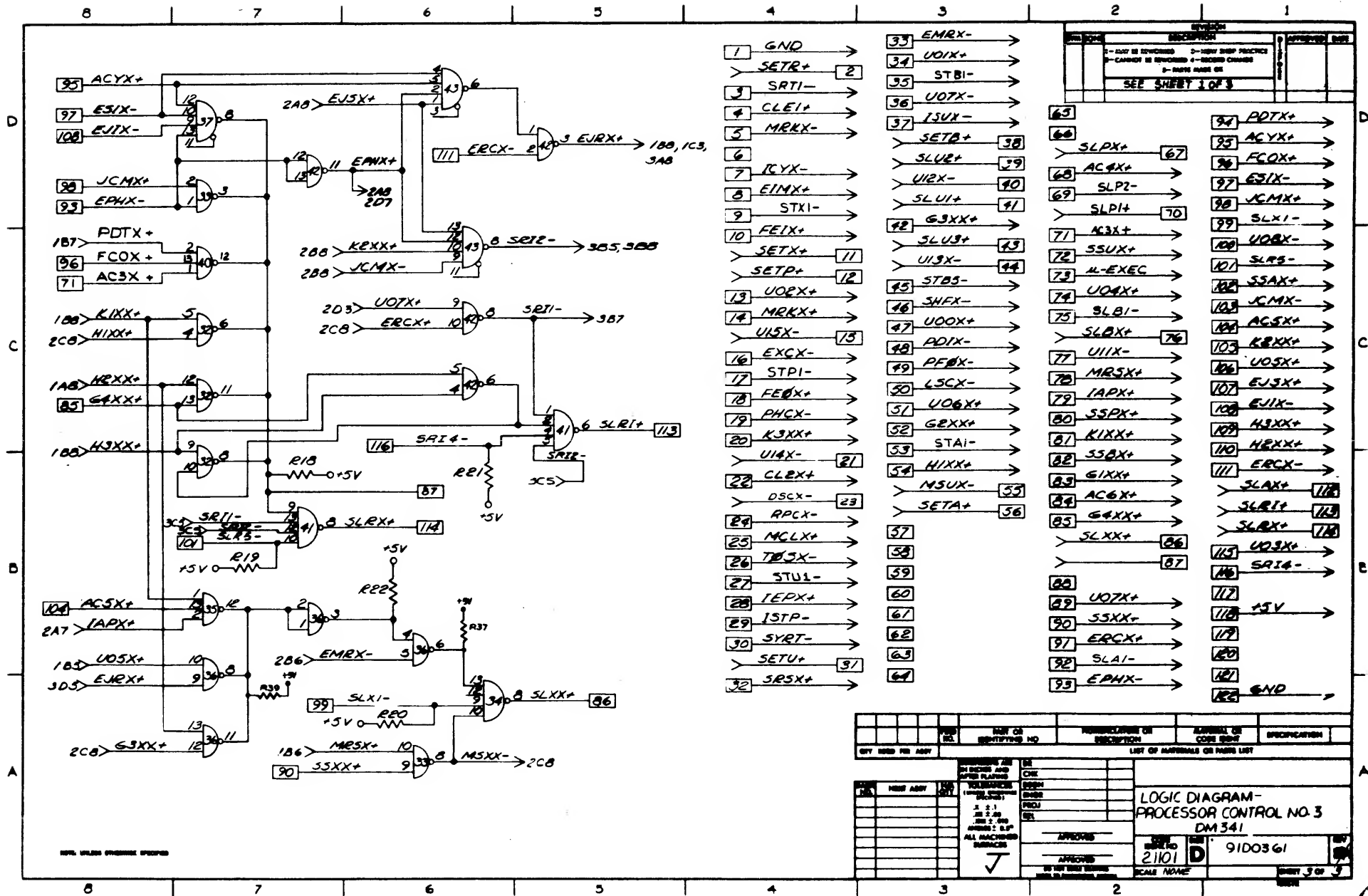
CAUTION: EN 82844
AFFECTS THIS DWG.

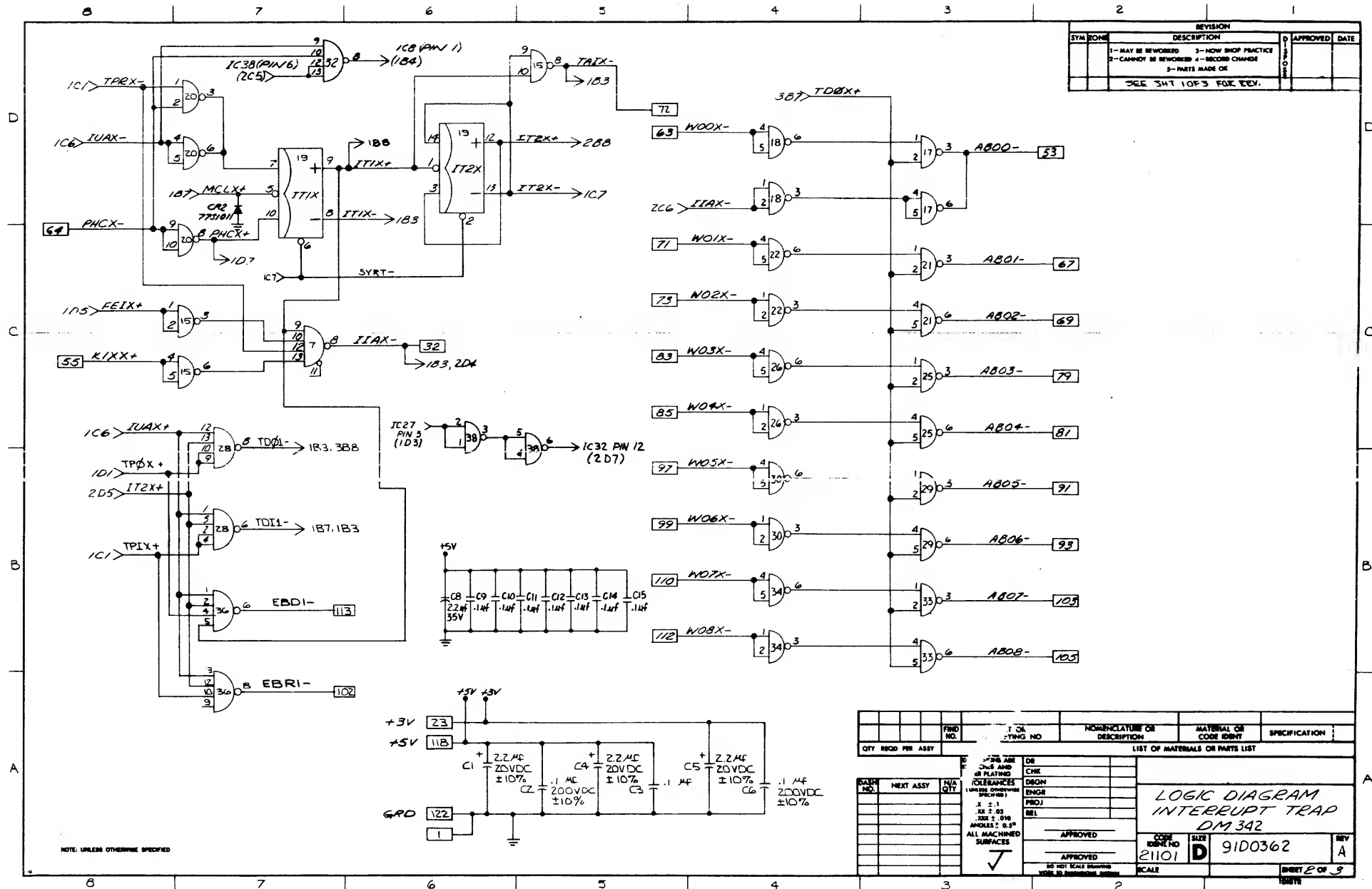


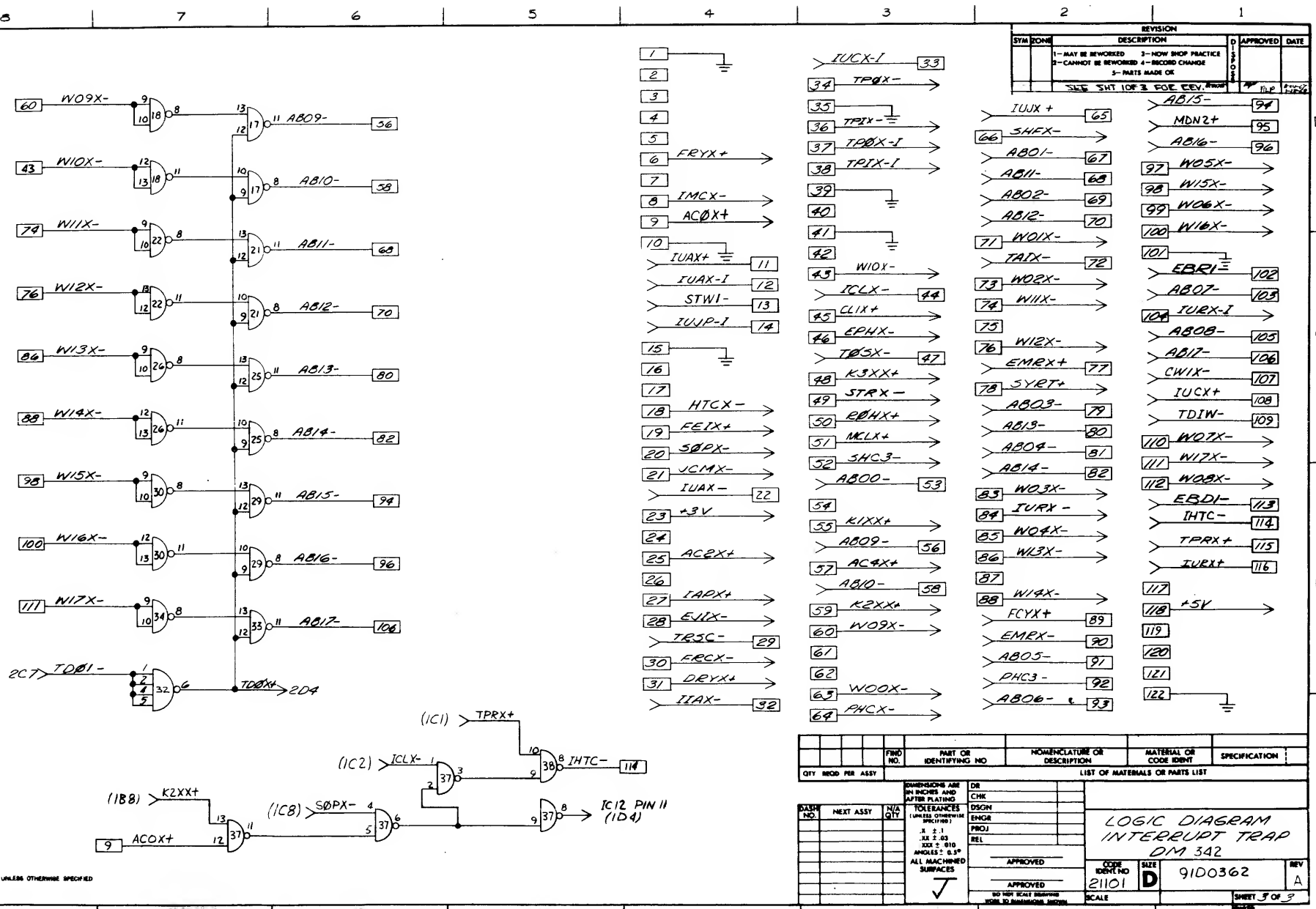










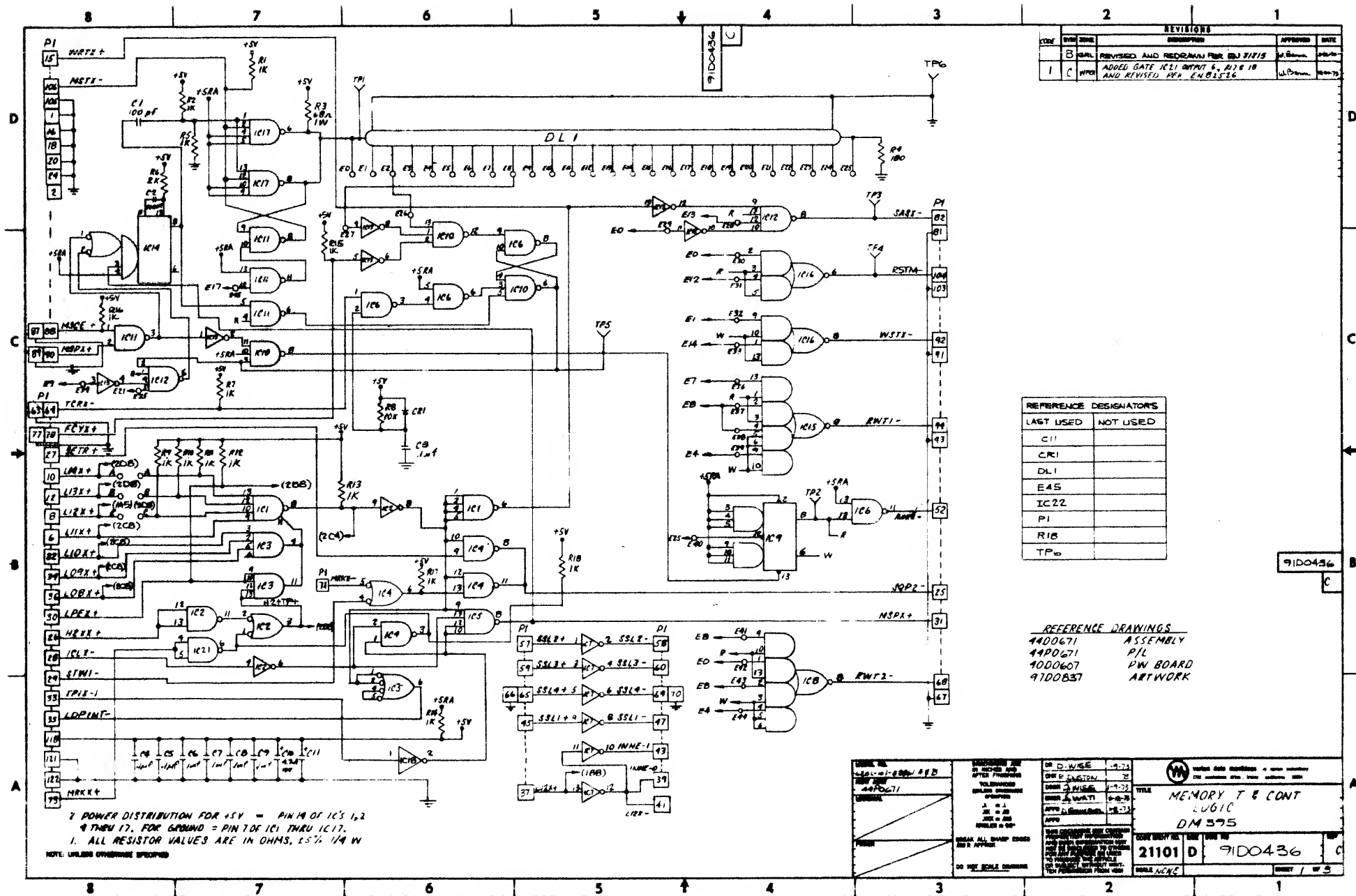


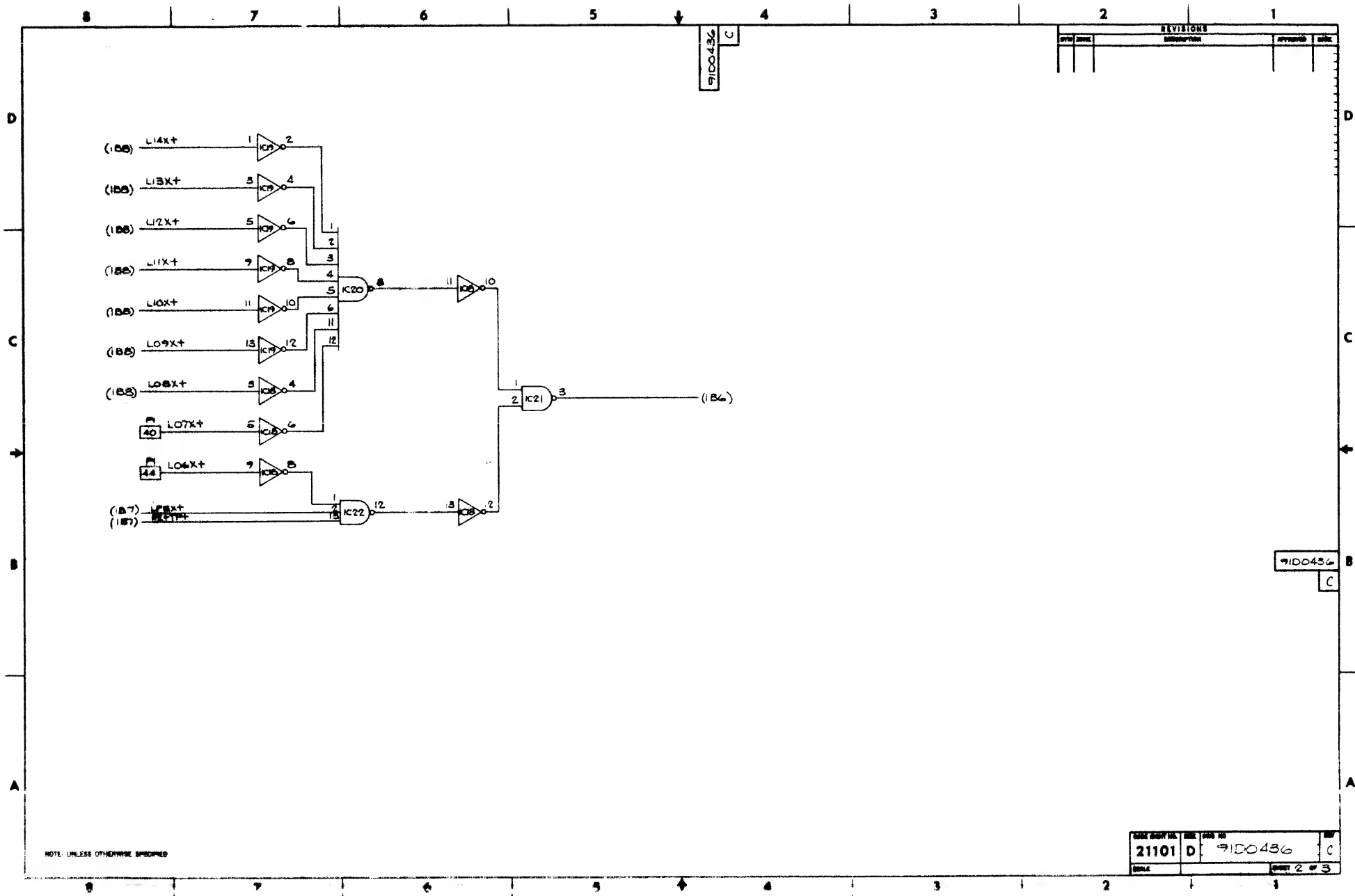
SYMBOL		REVISION	APPROVED	DATE
DESCRIPTION				
1 - MAY BE REWORKED		2 - NOW SHOP PRACTICE		
3 - CANNOT BE REWORKED		4 - RECORD CHANGE		
5 - PARTS MADE OK				
SEE SHIT 1 OF 3 FOR REV. 100				

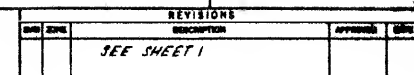
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LOGIC DIAGRAM
INTERRUPT TRAP
DM 342

CODE IDENT NO 21101
SIZE D
9100362
REV A







9100436
C

8

7

6

5

4

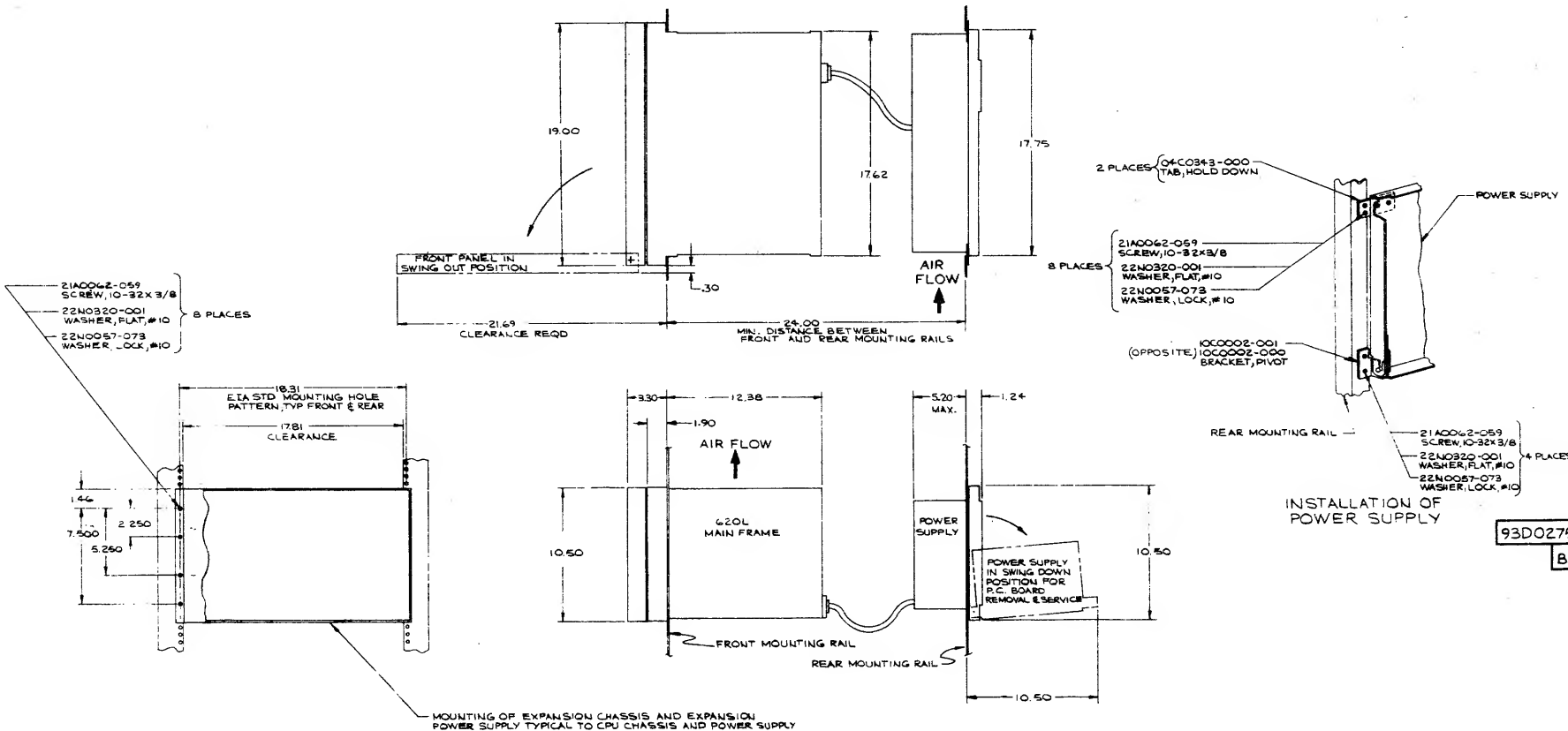
3

2

1

93D0276

REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
A	1/1/74	PRODUCTION RELEASE EN 5412	
B	1/1/74	REVISE CPU TOP VIEW, ADDED DIM CPU INSIDE VIEW PER EN 5412	



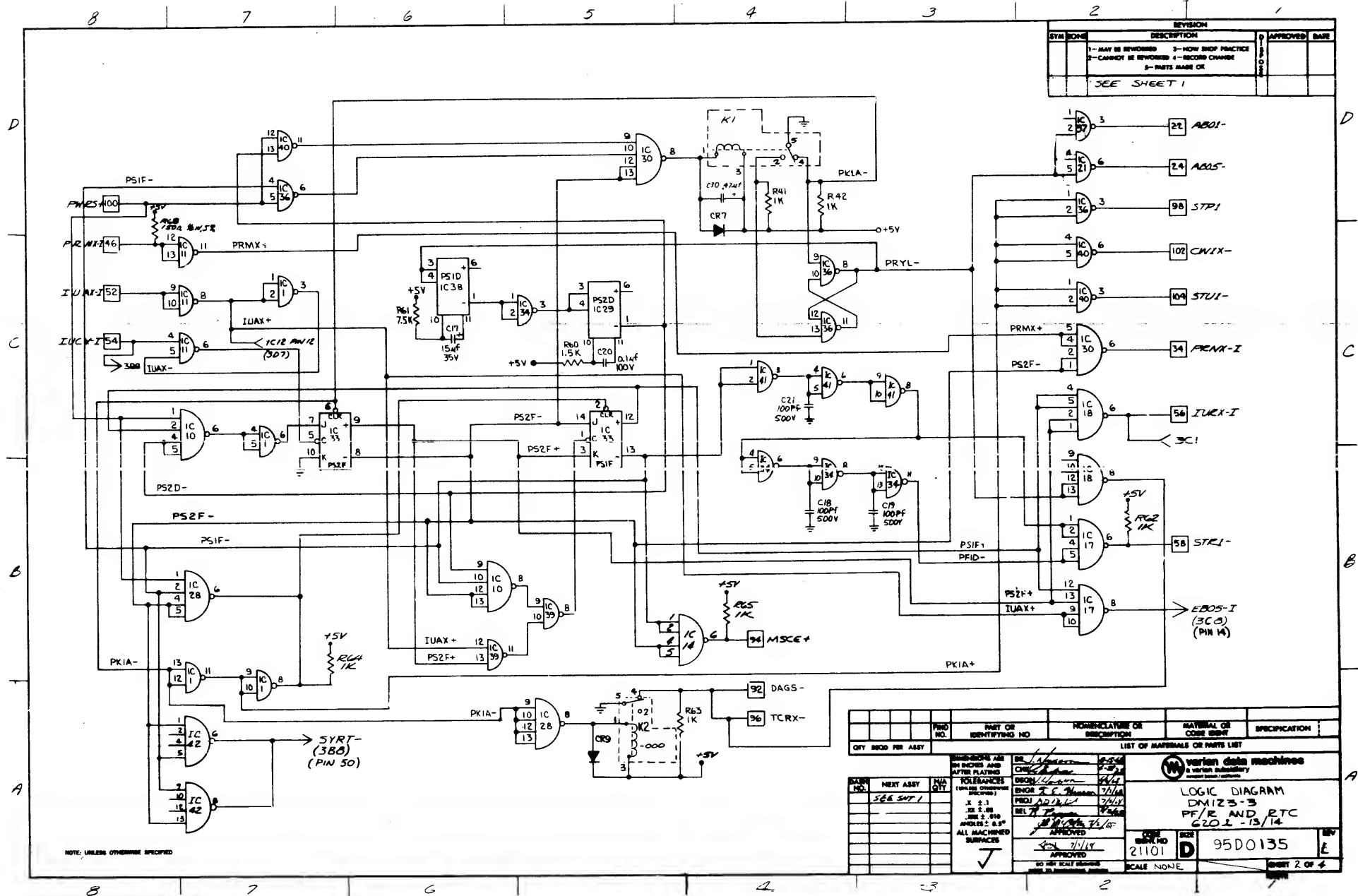
INSTALLATION OF POWER SUPPLY

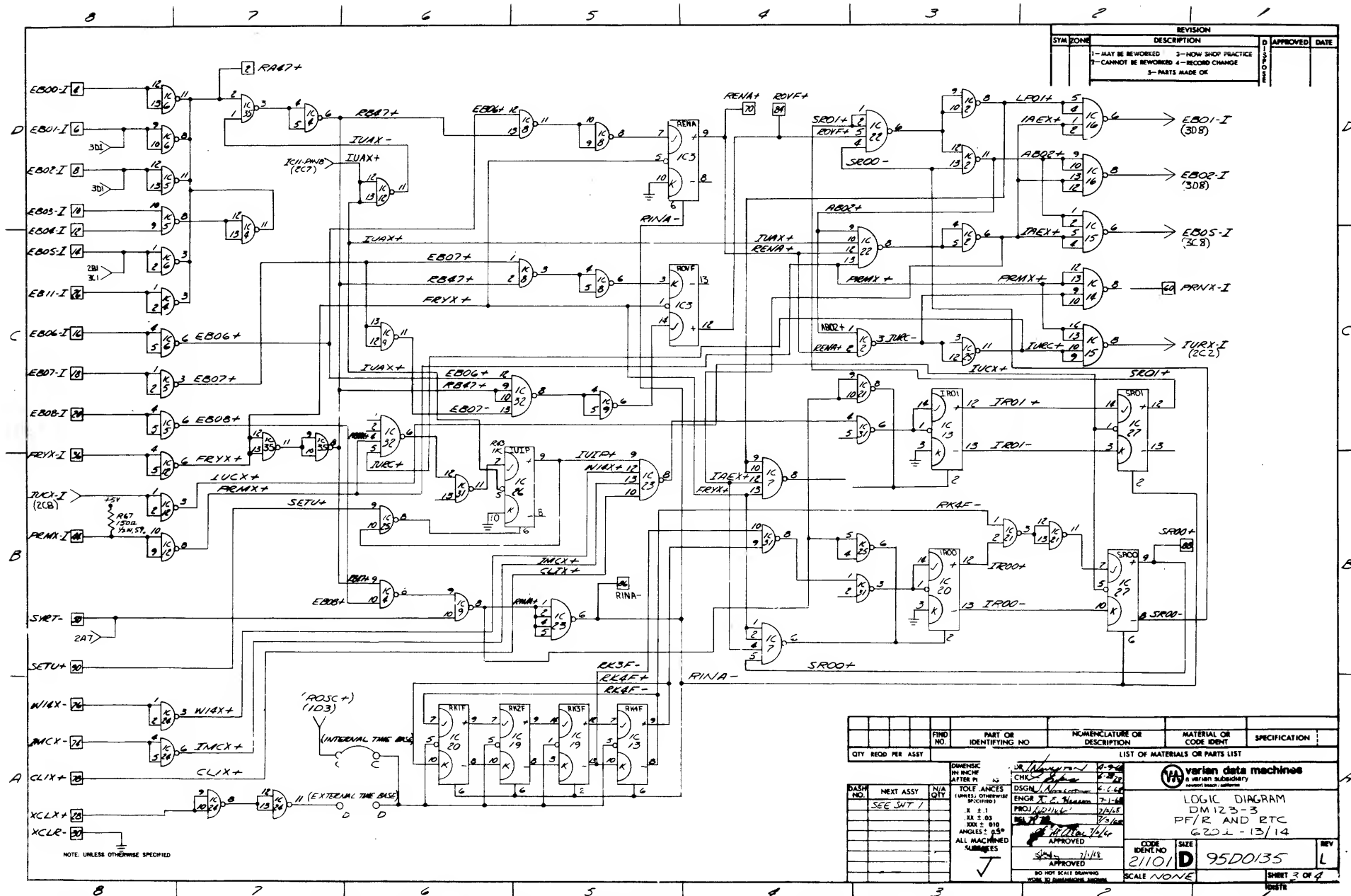
93D0276

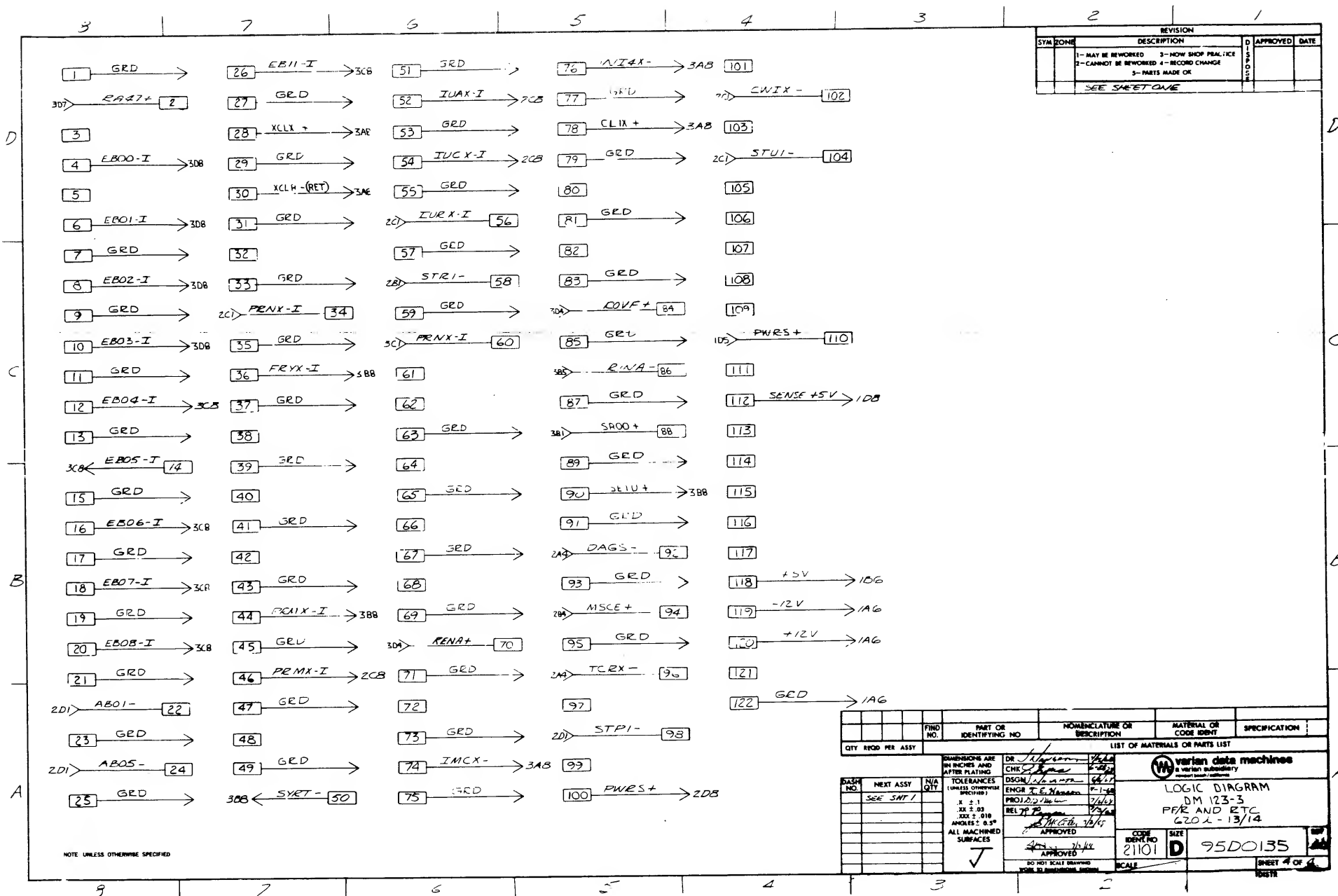
B

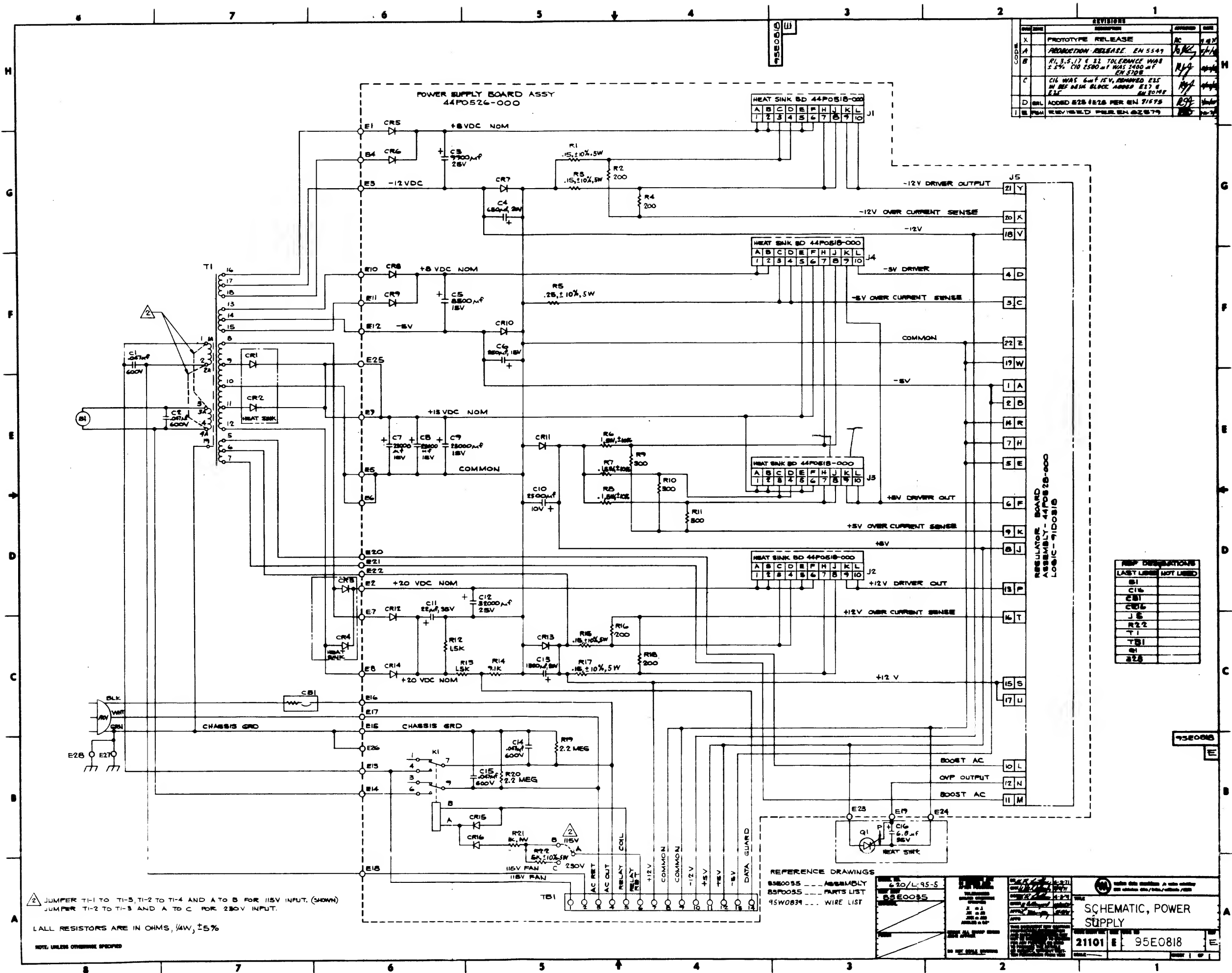
NOTE: UNLESS OTHERWISE SPECIFIED

MODEL NO. 620/L	DESIGNER J. MARSHALL	DATE 1/1/74	REVISED BY J. MARSHALL	DATE 1/1/74
FOR ARMY MULTIPLE USAGE	TOLERANCES UNLESS OTHERWISE SPECIFIED X ± .1 XX ± .05 XXX ± .02	THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND SUCH INFORMATION MAY NOT BE DISCLOSED TO OTHERS FOR ANY PURPOSE OR USED TO PRODUCE THE ARTICLE OR SUBJECT, WITHOUT WRITTEN PERMISSION FROM VON		
DATE 1/1/74	BREAK ALL SHARP EDGES SEE R. APPENDIX	DO NOT SCALE DRAWING		
TITLE INSTL DWG, PWR SUP, 620L BSC		CODE IDENT NO. 21101 D	REV B	SHEET 1 OF 1









REVISIONS			
REV	DESCRIPTION	DATE	BY
1	PROTOTYPE RELEASE	10/1/64	10/1/64
2	PRODUCTION RELEASE EN 5549	10/1/64	10/1/64
3	RL 3, 5, 17 & 22 TOLERANCE WAS 2.5%, C10 2500M F WAS 1400 M F EN 5708	10/1/64	10/1/64
4	C16 WAS 6M F 15V, REMOVED E15 IN REF DATA BLOCK ADDED E17 & E18	10/1/64	10/1/64
5	ADDED E25 & E26 PER EN 71678	10/1/64	10/1/64
6	REVISED PER EN 82679	10/1/64	10/1/64

REF DESIGNATIONS	
LAST USED	NOT USED
B1	
C16	
E15	
E16	
J6	
R22	
T1	
TB1	
Q1	
228	

△ JUMPER T1-1 TO T1-3, T1-2 TO T1-4 AND A TO B FOR 115V INPUT. (SHOWN)
JUMPER T1-2 TO T1-3 AND A TO C FOR 230V INPUT.

ALL RESISTORS ARE IN OHMS, 1/4W, ±5%

NOTE: UNLESS OTHERWISE SPECIFIED

REFERENCE DRAWINGS
95E0055 --- ASSEMBLY
95P0055 --- PARTS LIST
95W0834 --- WIRE LIST

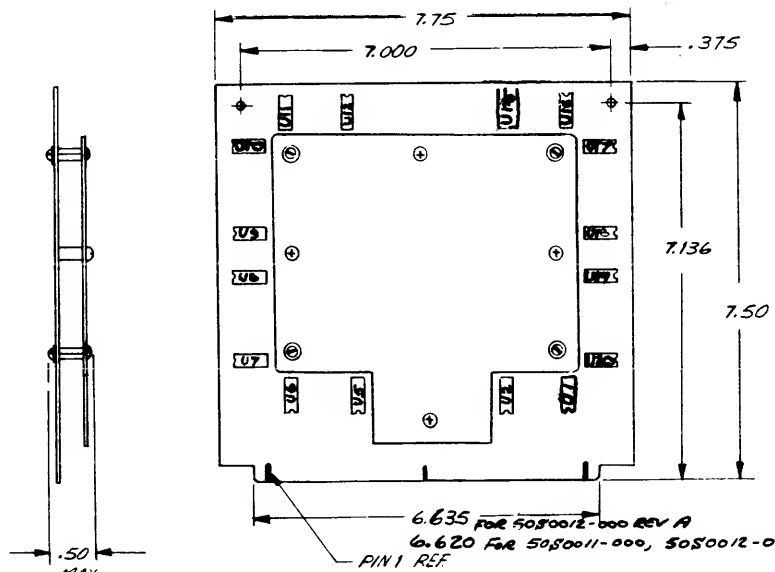
20/L-95-S
95E0035

SCHEMATIC, POWER SUPPLY
21101 E 95E0818

REVISIONS			SIGNATURE AND DATE		
LTR	ZONE	DESCRIPTION	DFT	CHK	ENGRG
A		ERN 786-C	3-2-74		3-7-74

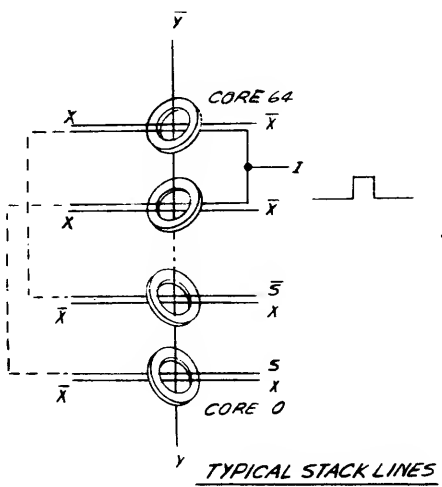
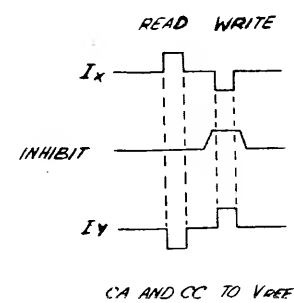
PIN ASSIGNMENT

PIN	SIDE "A"	SIDE "B"
1	GND	GND
2	CA1	CA1
3	CC1	CC1
4	CA3	CA3
5	CC3	CC3
6	CA5	CA5
7	CC5	CC5
8	CA7	CA7
9	CC7	CC7
10	SPARE	SPARE
11	SPARE	SPARE
12	SPARE	SPARE
13	X56	X57
14	X54	X55
15	X52	X53
16	X50	X51
17	RT1-1	RT1-1
18	GND	GND
19	RT1-2	RT1-2
20	S11	S11
21	I11	S6
22	S6	I6
23	S15	S15
24	I15	S9
25	S9	I9
26	S5	S5
27	I5	S7
28	S7	I7
29	S0	S0
30	I0	S4
31	S4	I4
32	SPARE	SPARE
33	SPARE	SPARE
34	SPARE	SPARE
35	S1	S1
36	I1	S12
37	S12	I12
38	S14	S14
39	I14	S10
40	S10	I10
41	S3	S3
42	I3	S13
43	S13	I13
44	S8	S8
45	I8	S2
46	S2	I2
47	SPARE	SPARE
48	GND	GND
49	SPARE	SPARE
50	Y50	Y50
51	Y53	Y52
52	Y55	Y54
53	Y57	Y56
54	SPARE	SPARE
55	SPARE	SPARE
56	SPARE	SPARE
57	CA6	CA6
58	CC6	CC6
59	CA4	CA4
60	CC4	CC4
61	CA2	CA2
62	CC2	CC2
63	CA0	CA0
64	CC0	CC0
65	GND	GND



SIDE "A" SHOWN

TYPICAL TEST PROGRAM TIMING



TYPICAL STACK LINES

DRIVE							
U20	U9	U18	U7	U19	U10	U11	U8
CC0	CC1	CC2	CC3	CC4	CC5	CC6	CC7
CA0	CA1	CA2	CA3	CA4	CA5	CA6	CA7
X50	0	2	16	18	45	47	61
X51	1	3	17	19	44	46	60
X52	4	6	20	22	41	43	57
X53	5	7	21	23	40	42	56
X54	8	10	24	26	37	39	53
X55	9	11	25	27	36	38	52
X56	12	14	28	30	33	35	49
X57	13	15	29	31	32	34	48

"X" DECODE DRIVE

DRIVE							
U1	U11	U16	U6	U2	U12	U15	U5
CC0	CC1	CC2	CC3	CC4	CC5	CC6	CC7
CA0	CA1	CA2	CA3	CA4	CA5	CA6	CA7
Y50	0	2	16	18	32	34	48
Y51	1	3	17	19	33	35	49
Y52	4	6	20	22	36	38	52
Y53	5	7	21	23	37	39	53
Y54	8	10	24	26	40	42	56
Y55	9	11	25	27	41	43	57
Y56	12	14	28	30	44	46	60
Y57	13	15	29	31	45	47	61

"Y" DIODE DECODE

WORST CASE PATTERN											
	Y0	Y1	Y2	Y3			Y31	Y32			Y63
X0	1	1	1	1			1	0	0		0
X1	0	0	0	0			0	1	1		1
X2	1	1	1	1			1	0	0		0
X3	0	0	0	0			0	1	1		1
X63	0	0	0	0			0	1	1		1

S/N K

- 6 INTEGRATED CIRCUIT U3, U4, U13 & U14 NOT USED.
 - 5 PCB TYPE: PLANAR PLUGGABLE. COMPONENTS AND CORES ON ONE SIDE.
 - 4 CAPACITY: 4KX18 18 MIL MAX. (COINCIDENT) 8KX9 18 MIL MAX. [COINCIDENT/ANTICOINCIDENT, 4K DECODE].
 - 3 WIRING: 3 WIRE - 3D X & Y - CONTINUOUSLY WIRED SENSE/INHIBIT - BOWTIE 3 POINT TERMINATION.
 - 2 DIODE TYPE: INTEGRATED CIRCUIT - DIODE ARRAY, DUAL-IN-LINE 16 DIODES PER PACKAGE. AMPEX PN 586-687.
 - 1 CONNECTION TYPE: TOTAL 130 CONTACTS, 65 CONTACTS EACH SIDE SPACED AT .100. MATING CONNECTOR TYPE IS MASTERITE IND. PN 008GR65-DR-B-X OR EQUIV.
- NOTES: UNLESS OTHERWISE SPECIFIED.

NOTICE		UNLESS OTHERWISE SPECIFIED		SIGNATURE		DATE	
THIS DRAWING SHALL NOT BE DUPLICATED, USED FOR ANY PURPOSE OTHER THAN THAT FOR WHICH PROVIDED OR DISCLOSED, IN WHOLE OR IN PART WITHOUT THE WRITTEN CONSENT OF AMPEX CORPORATION. PROVIDED, HOWEVER, THAT IF THIS DRAWING IS SPECIFIED TO BE DELIVERED TO THE GOVERNMENT, OR TO A GOVERNMENT CONTRACTOR, PURSUANT TO A GOVERNMENT PRIME OR SUBCONTRACT, THE GOVERNMENT MAY MAKE SUCH USE OF THIS DRAWING AS IS PERMITTED BY THE APPLICABLE "DATA" CLAUSE SET FORTH IN SUCH CONTRACT OR SUBCONTRACT.		DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES.		DRAWN BY: <i>A. H. H. H.</i>		2/2/74	
3256973 9403-4411		TOL: 12 PLCS PLG ANG		CHK BY: <i>A. H. H. H.</i>		2/2/74	
NEXT ASSY USED ON		REMOVE BURS AND SHARP EDGES YES <input type="checkbox"/> NO <input type="checkbox"/>		DFT APPD		ENGRG APPD	
APPLICATION		DO NOT SCALE THIS PRINT		AUTH BY:			
		MATERIAL:		SCALE: 100%		SHEET 1 OF 1	
		FINISH:		SIZE: D		CODE IDENT NO: 09150	
				COMPUTER PRODUCTS DIV		ELECTRICAL/MECHANICAL INTERFACE AND SCHEMATIC	
				Culver City, California 90230		3238441	

ENGINEERING NOTICE

E.N. NO. 83017

PAGE 1 OF 3

ISSUE DATE 6-4-74



MODEL NUMBER 620/L-100	EFFECTIVE SERIAL <u>ALL</u>	W28 6/4/74 PARTS DISPOSITION	
DEVICE DM342		<input type="checkbox"/> USE AS IS	<input type="checkbox"/> SCRAP
INTERRUPT TRAP		<input checked="" type="checkbox"/> REWORK SEE PAGE 3	<input type="checkbox"/> NOT APPLICABLE

TYPE OF INFORMATION

<input type="checkbox"/> DRAWING RELEASE	<input checked="" type="checkbox"/> DRAWING CHANGE	<input type="checkbox"/> RECORD CHANGE	<input type="checkbox"/> SUBSTITUTION OR DEVIATION	<input type="checkbox"/> STOP ORDER
--	--	--	--	-------------------------------------

REASON FOR ACTION OUTPUT CWIX - (IC34-11) IS OR-TIED ON GROUND PLANE WITH A DTL OUTPUT OF PFR BOARD. DRIVING GATES MUST BE OPEN-COLLECTOR OR DTL. DTL GATE ON PFR UNABLE TO PULL DOWN ACTIVE PULLUP GATE ON INTERRUPT TRAP BOARD RELIABLY.	CHANGE CODE: <u>1</u> OUTSTANDING CODE 2 EN S: <u>C</u>
---	---

ACTION TO BE TAKEN

44P0598 P/L-INTERRUPT TRAP ASSY DM342

1. UPDATE REVISION LETTER OF REF DRAWINGS 44D0598, 91D0362 AND F/N 1 40D0511-000 PER THIS EN.
2. F/N 3 (65N2500-102)
 - A. QTY IS: 15 WAS: 12
 - B. REMARKS IS: R8-22 WAS: R8-19
3. F/N 16 (49A0042-000)
 - A. QTY IS: 7 WAS: 6
 - B. REMARKS IS: IC13,17,21,25,29,33,34 WAS: IC17,21,25,29,33,13
4. F/N 18 (49A0039-000)
 - A. QTY IS: 5 WAS: 6
 - B. REMARKS IS: IC18,22,26,30,31 WAS: IC18,22,26,30,31,34

DOCUMENTS AFFECTED

DOCUMENT	REV.	DOCUMENT	REV.	REFERENCE DOCUMENTS
	IS WAS		IS WAS	
44D0598	B A	91C0362	B A	RECO # 05306
44P0598	E D	97D0712	C B	
40D0511	C B	97D0713	C B	
		97D0714	C B	

DRAFTSMAN	CHECKER	PROJECT ENGR.	RES. MGR.	REVIEW BOARD
J. Luther	E-2374	Brownfield	6/3/74	T.E. Hanson
			6-4-74	

ENGINEERING NOTICE

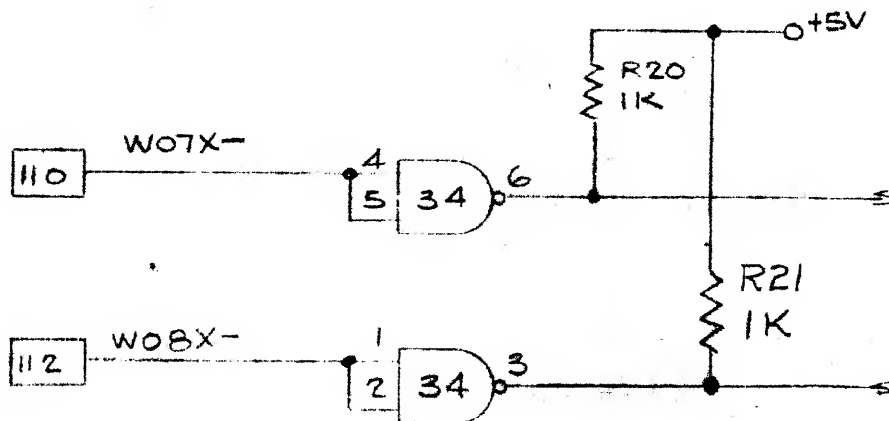


E.N. No. 83017

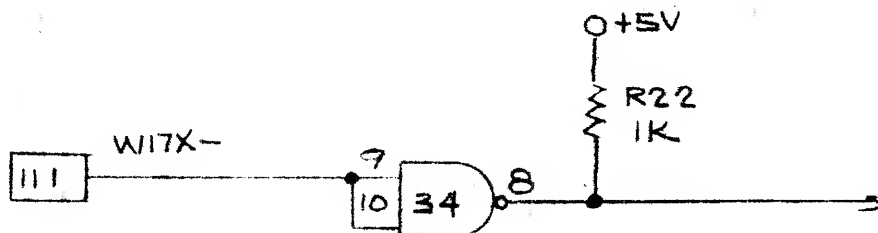
PAGE 2 OF 3

91C0362 LOGIC- INTERRUPT TRAP DM342

1. SHEET 1 REFERENCE DESIGNATION BLOCK
LAST USED IS: R22 WAS: R19
2. SHEET 2 ZONE B4 ADD R20 & R21 AS SHOWN



3. SHEET 3 ZONE B7 ADD R22 AS SHOWN



97D0712 ARTWORK DM342

REVISE TO CONFORM WITH CHANGES TO 91C0362 LOGIC

97D0713 SILKSCREEN DM342

97D0714 SOLDER MASK DM342

44D0598 ASSEMBLY DM342

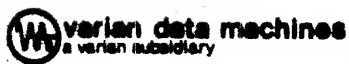
REVISE TO CONFORM WITH CHANGES TO 97D0712 ARTWORK

40D0511 P.W. BOARD DM342

1. REVISE TO CONFORM WITH CHANGES TO 97D0712 ARTWORK.

2. UPDATE REVISION LETTER OF ARTWORK MASTERS 97D0712, 97D0713 AND 97D0714 PER THIS EN.

ENGINEERING NOTICE



REWORK INSTRUCTIONS

E. N. No. 83017

PAGE 3 OF 3

Reidentify the following revision letters by stamping an "X" over the old revision letter and restamping the new revision letter next to the "X".

Revision Is:

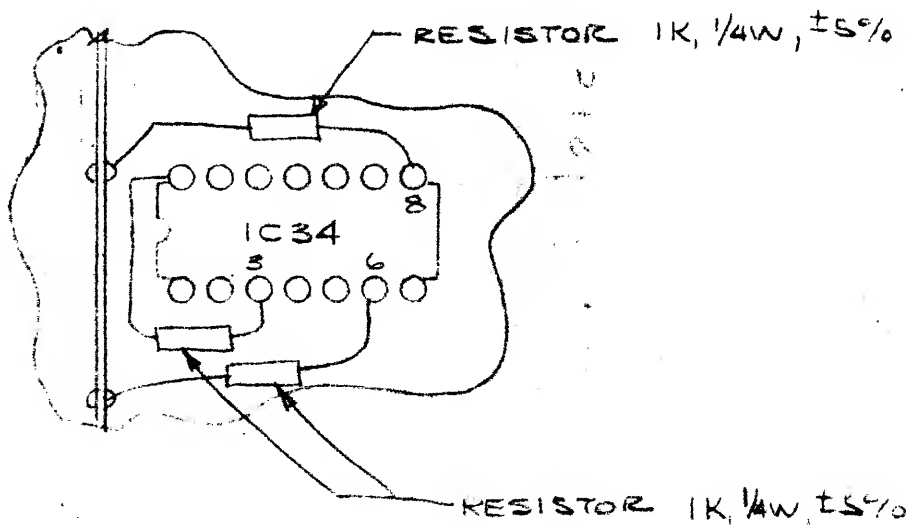
Revision Was:

44P0598-000 E

44P0598-000 D

44P0598-000 INTERRUPT TRAP BOARD DM342

1. REMOVE IC 34 (49A0039-000) AND REPLACE IC 34 USING 49A0042-000 (74H01)
2. ADD 3 RESISTORS 1K, $\frac{1}{4}W$, $\pm 5\%$ (65N2500-102) AS SHOWN.



ENGINEERING NOTICE

varian data machines
a varian subsidiary

E.N. NO. 44-0.2

PAGE 1 OF 2

ISSUE DATE 6-17-73

MODEL NUMBER 620L	EFFECTIVE SERIAL 609/5/73	PARTS DISPOSITION	
DEVICE POWER SUPPLY	EARLIEST OPPORTUNITY	<input checked="" type="checkbox"/> USE AS IS	<input type="checkbox"/> SCRAP
		<input type="checkbox"/> REWORK	<input type="checkbox"/> NOT APPLICABLE

TYPE OF INFORMATION

<input type="checkbox"/> DRAWING RELEASE	<input checked="" type="checkbox"/> DRAWING CHANGE	<input type="checkbox"/> RECORD CHANGE	<input type="checkbox"/> SUBSTITUTION OR DEVIATION	<input type="checkbox"/> STOP ORDER
--	--	--	--	-------------------------------------

REASON FOR ACTION

1. TO ADD NON-STANDARD HARDWARE REQUIREMENTS
2. TO REVISE HARDWARE CALLOUT
3. TO ELIMINATE POSSIBLE SHORTING OF HDW. TO HEATSINK
4. TO ADD MTG BRACKETS TO P.S. PARTS LIST

ACTION TO BE TAKEN

83P0035 PARTS LIST POWER SUPPLY

- 1.) UPDATE REVISION LETTER OF REFERENCE DRAWINGS PER THIS EN.
- 2.) DELETE FIND NO. 45 (79A0050-000)
- 3.) ADD THE FOLLOWING, QTY SHOWN SAME FOR ALL DASH NUMBERS.

QTY	F/N	PART NO.	DESC	REMARKS
2	48	79A0025-034	MICA WASHER	
1	49	79A0050-003	SPACER	
1	50	79A0050-004	WASHER	
1	51	79A0050-002	L.W. TERMINAL	
1	52	79A0050-001	JAM NUT	

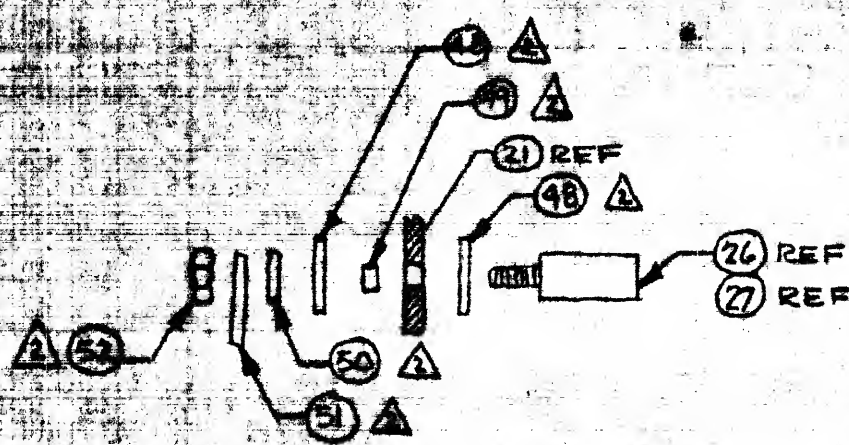
DOCUMENTS AFFECTED

DOCUMENT	REV.	DOCUMENT	REV.	REFERENCE DOCUMENTS
	IS WAS		IS WAS	
83P0035	L K			RECO # 4553
83E0035	H G			4554
				4566

DRAFTSMAN	CHECKER	PROJECT ENGR.	RESP. MGR.	REVIEW BOARD
<i>[Signature]</i> 6/15/73	<i>[Signature]</i> 6/15/73	<i>[Signature]</i> 6/15/73	<i>[Signature]</i> 6/15/73	<i>[Signature]</i> 6/15/73

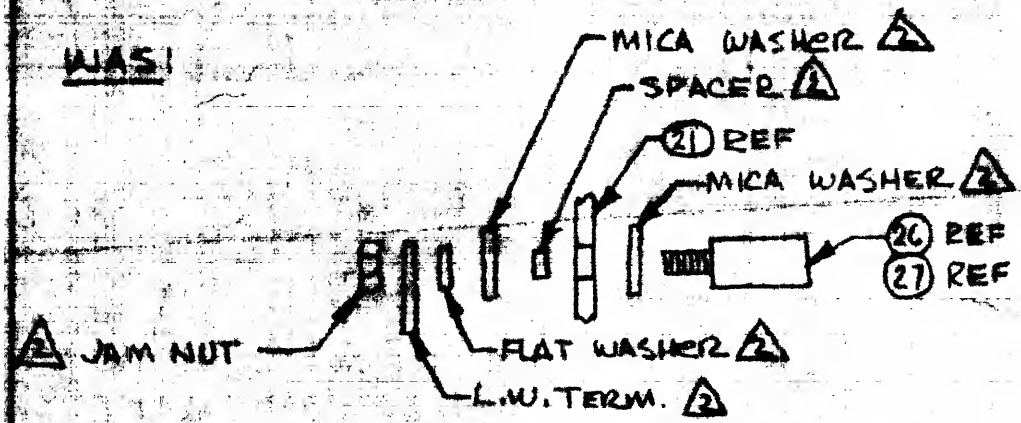
0250035 POWER SUPPLY ASSY

1) REVISE SECTION B-B AS SHOWN BELOW




SECTION B-B
EXPLODED VIEW (TYP FOR CR1-CR4, Q1)

WAS!



SECTION B-B
EXPLODED VIEW (TYP FOR CR1-CR4, Q1)
USE MT6 KIT F/N 45 FOR Q1

ENGINEERING NOTICE

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E.N. NO. 82200
PAGE 1 OF 2
ISSUE DATE 8-15-73

MODEL NUMBER 620/L-100	EFFECTIVE SERIAL EARLIEST OPPORTUNITY	8/14/73	PARTS DISPOSITION	
DEVICE DM 337		<input checked="" type="checkbox"/>	USE AS IS	<input type="checkbox"/> SCRAP
PROCESSOR CONT		<input type="checkbox"/>	REWORK	<input type="checkbox"/> NOT APPLICABLE

TYPE OF INFORMATION

<input type="checkbox"/> DRAWING RELEASE	<input checked="" type="checkbox"/> DRAWING CHANGE	<input type="checkbox"/> RECORD CHANGE	<input type="checkbox"/> SUBSTITUTION OR DEVIATION	<input type="checkbox"/> STOP ORDER
--	--	--	--	-------------------------------------

REASON FOR ACTION

1. TO REMOVE JUMPER A-A

ACTION TO BE TAKEN

97DO697-ARTWORK-PROCESSOR CONTROL-DM337

1. REMOVE ETCH FROM PI-59 TO FEEDTHRU A
2. REMOVE ETCH FROM IC16-7 TO FEED THRU A
3. ADD ETCH FROM PI-59 TO IC16-7.

97DO698-SILKSCREEN-PROCESSOR CONT-DM337

1. REVISE PER CHANGES TO 97DO697

97DO699-SOLDER MASK-PROCESSOR CONT-DM337

1. REVISE PER CHANGES TO 97DO697

40DO506-BD DETAIL-PROCESSOR CONT-DM337

1. UPDATE REV LETTERS OF ARTWORK MASTERS REQ'D PER THIS EN.

DOCUMENTS AFFECTED

DOCUMENT	REV.		DOCUMENT	REV.		REFERENCE DOCUMENTS
	IS	WAS		IS	WAS	
97DO697	H	G	40DO506	L	K	RECO # 04484
97DO698	G	F	44DO593	G	F	
97DO699	G	F	44PO593	N	M	

DRAFTSMAN	CHECKER	PROJECT ENGR.	RESP. ENGR.	REVIEW BOARD
<i>JML</i>	<i>[Signature]</i>	<i>T.E. Hinton</i>	<i>[Signature]</i>	<i>[Signature]</i>
8/14/73	8/13/73	8/14/73	8/14/73	8/14/73

ENGINEERING NOTICE

E. N. No. 82237
PAGE 2 OF 2

4000506 - CONT'D

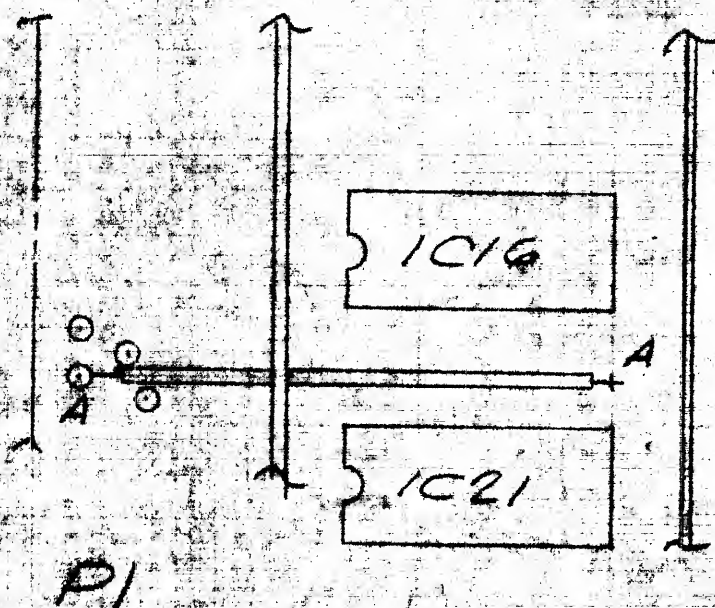
2. HOLE CHART, REVISE QTY FOR SYM A
IS 1230, WAS: 1232

44P0593 - P/L-PROCESSOR CONTROL-DM337

1. UPDATE REV LETTER OF REF DWG'S
PER THIS EN.
2. UPDATE REV LETTER OF FIND N° 1
PER THIS EN.
3. DELETE FIND N° 35, WAS: (QTY, -000, -001)
0, (PART N°) 53A0647-200
(DES) WIRE, TEFLON, (REMARKS) 26 AWG

44D0523 - ASSY-PROCESSOR CONTROL-DM337

1. FIELD OF DWG, REMOVE JUMPER A-A
WAS:



ENGINEERING NOTICE

E.N. NO. 82144

PAGE 1 OF 5

ISSUE DATE 3-26-74



MODEL NUMBER <u>620L-100</u>	EFFECTIVE SERIAL <u>102/2/74</u>	PARTS DISPOSITION	
DEVICE PROC. CONT NO 2	SEE BELOW *	<input type="checkbox"/> USE AS IS	<input type="checkbox"/> SCRAP
		<input checked="" type="checkbox"/> REWORK <u>SEE PG 4</u>	<input type="checkbox"/> NOT APPLICABLE

TYPE OF INFORMATION

<input type="checkbox"/> DRAWING RELEASE	<input checked="" type="checkbox"/> DRAWING CHANGE	<input type="checkbox"/> RECORD CHANGE	<input type="checkbox"/> SUBSTITUTION OR DEVIATION	<input type="checkbox"/> STOP ORDER
--	--	--	--	-------------------------------------

REASON FOR ACTION <u>TO ENABLE THE 620L-100 TO PASS INSTRUCTION TEST NO. 2 BY ELIMINATING A DECODING SPIKE ON IAPX+</u>	CHANGE CODE <u>1</u>
	OUTSTANDING CODE <u>ENS</u>

ACTION TO BE TAKEN

* EFFECTIVE SERIAL

- 1.) SEE PAGE 5 FOR SER. NO. EFFECTIVITY
- 2.) ALL THOSE USED IN SYSTEMS THAT FAIL TO PASS MARGIN TESTS

ACTION TO BE TAKEN

(SEE PAGE 2)

DOCUMENTS AFFECTED

DOCUMENT	REV.		DOCUMENT	REV.		REFERENCE DOCUMENTS
	IS	WAS		IS	WAS	
<u>91D0360</u>	<u>C</u>	<u>B</u>	<u>40D0509</u>	<u>E</u>	<u>D</u>	<u>RECO # 00220</u> <u>RECO # 05190</u>
<u>44D0596</u>	<u>F</u>	<u>E</u>	<u>91D0706</u>	<u>C</u>	<u>B</u>	
<u>44D0596</u>	<u>C</u>	<u>B</u>	<u>91D0707</u>	<u>E</u>	<u>D</u>	
			<u>91D0708</u>	<u>C</u>	<u>B</u>	
DRAFTSMAN	CHECKER		PROJECT ENGR.		RESP. MGR.	REVIEW BOARD
<u>[Signature]</u> <u>3/21/74</u>	<u>[Signature]</u> <u>3/21/74</u>		<u>R.E. Harrison</u> <u>3/25/74</u>		<u>[Signature]</u> <u>3/25/74</u>	<u>[Signature]</u> <u>3/25/74</u>

ENGINEERING NOTICE



E. N. No. 82844

PAGE 2 OF 5

44PD596 P/L PROC. CONTROL NO. 2

- 1.) REVISE QTY AND REMARKS OF FIND NO. 4
(49A0008-000) QTY IS 5 WAS 4, REMARKS
ADD IC49
- 2.) UPDATE REVISION LETTER OF REFERENCE
DRAWINGS AND FIND NO. 1 PER THIS EN.

97D0706 ARTWORK DM340

97D0708 SOLDERMASK DM340

97D0707 SILKSCREEN DM340

- 1.) UPDATE PER CHANGES TO LOGIC DIAGRAM
91D0360

40D0509 P.W. BOARD DM340

- 1.) REVISE F/D TO AGREE WITH CHANGES TO
97D0706
- 2.) UPDATE REVISION LETTERS OF "ARTWORK
MASTERS REQUIRED" PER THIS EN.

44D0596 PROC CONTROL NO. 2 ASSY

- 1.) ADD IC49 TO F/D

ENGINEERING NOTICE

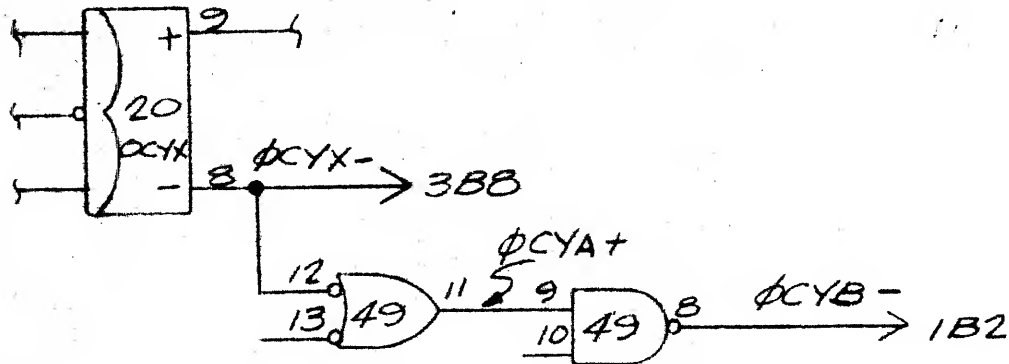
E.N. No. 82844
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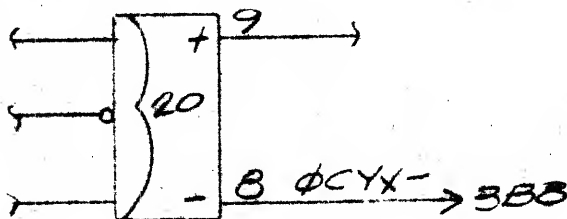
91DO360-LOGIC-PROC. CONTROL N22

1. SHEET 1, ZONE C1, REVISE AS FOLLOWS:

IS:

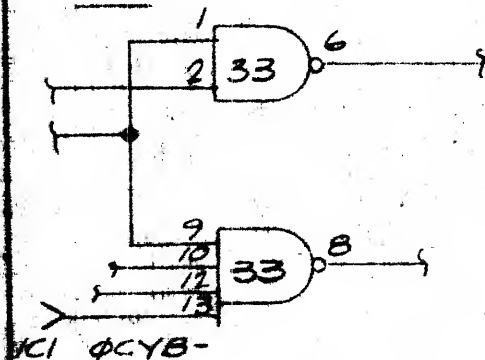


WAS:

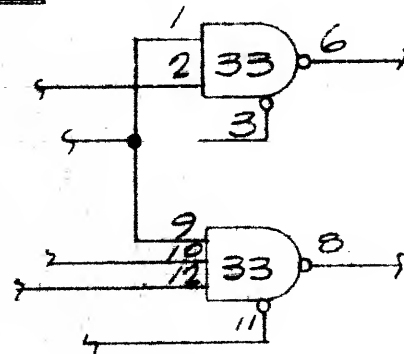


ZONE B2

IS:



WAS:



2. UPDATE REF DES LAST USED BLOCK;

IS: IC 49, WAS IC 48

ENGINEERING NOTICE



REWORK INSTRUCTIONS

E. N. No. 82844

PAGE 4 OF 5

Reidentify the following revision letters by stamping an "X" over the old revision letter and restamping the new revision letter next to the "X".

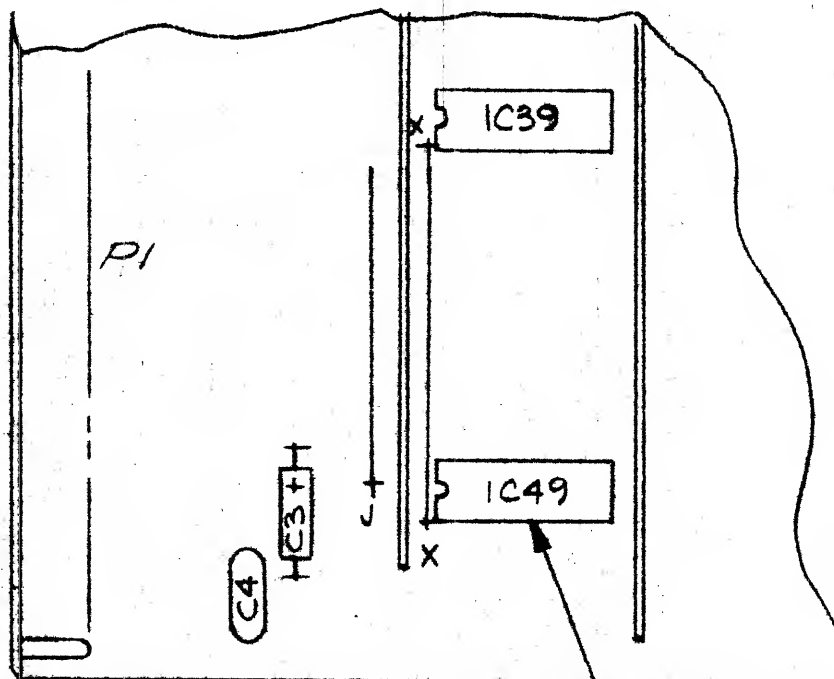
Revision Is:

44P0596-000 F

Revision Was:

44P0596-000 E

- 1.) INSTALL IC49, VDM P/N 49A0008-000 IN SPARE IC LOCATION AS SHOWN BELOW



44P0596-000
COMPONENT
SIDE

ADD IC (49A0008-000)

- 2.) ADD THE FOLLOWING JUMPERS TO COMP. SIDE USING WIRE, 53A0701-000, 30 AWG

FROM	TO
IC49-8	IC33-13
IC49-9	IC49-11
IC49-12	IC20-8

ENGINEERING NOTICE

 varian data machines
a varian subsidiary

E. N. No. 82844

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SERIAL NO. EFFECTIVITY

44P0596-000

SER. NO. 1566 AND ON.

S/N 1166 THRU 1169,

1200

1201

1202

1210

1211

1215

1219

1220

1223

1225

1228

1236

S/N 1243 THRU 1337

S/N 1339 THRU 1359

S/N 1361 THRU 1368

1372

1386

1388

1389

1390

1394

1395

1396

1398

1400

1403

1407

1411

1413

1414

1422

1432

1438

1439

1444

1445

S/N 1451 THRU 1485

1526

1528

1529

1532

1533

1534

1536

1540

1544

S/N 1547 THRU 1550

1554

1557

1559

1561

1565